

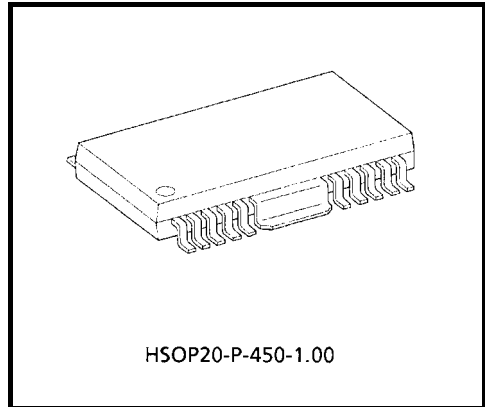
TA8470AF/AFG

3 PHASE FULL WAVE BRUSHLESS DC MOTOR DRIVER IC

TA8470AF/AFG is a low-noise type 3 Phase Bi-direction Motor Driver IC, developed as a 3 Phase Hall motor driver for VTRs (capstan, cylinder), etc.

FEATURES

- Operating Voltage Range : $V_{CC} = 7 \sim 17 \text{ V}$
- Output Current : $I_O (\text{MAX.}) = 1.2 \text{ A}$
- Three Phase Bi-direction, current control mode
- Low Noise (Quasi Sinusoidal Drive)
- Built-in FG Amplifier
- Low Output Impedance with B Class Push-Pull Driver, Capable of Short Brakes (Dumping Brakes)
- Position Detecting Circuit (Hall Input) with High Sensitivity : $V_H = 50 \text{ mV}_{p-p}$
- Enclosed in Space-saving Power Flat Package
- Built-in Thermal Shutdown Circuit



Weight : 0.79 g (Typ.)

TA8470AFG:

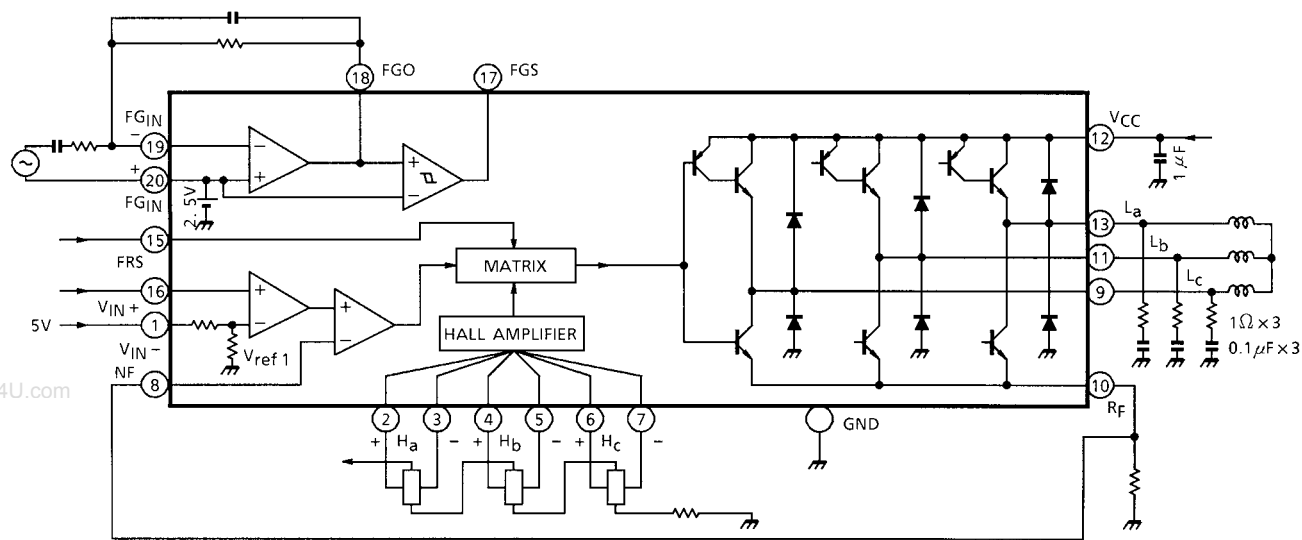
The TA8470AFG is a Pb-free product.

The following conditions apply to solderability:

*Solderability

1. Use of Sn-37Pb solder bath
 - *solder bath temperature = 230°C
 - *dipping time = 5 seconds
 - *number of times = once
 - *use of R-type flux
2. Use of Sn-3.0Ag-0.5Cu solder bath
 - *solder bath temperature = 245°C
 - *dipping time = 5 seconds
 - *the number of times = once
 - *use of R-type flux

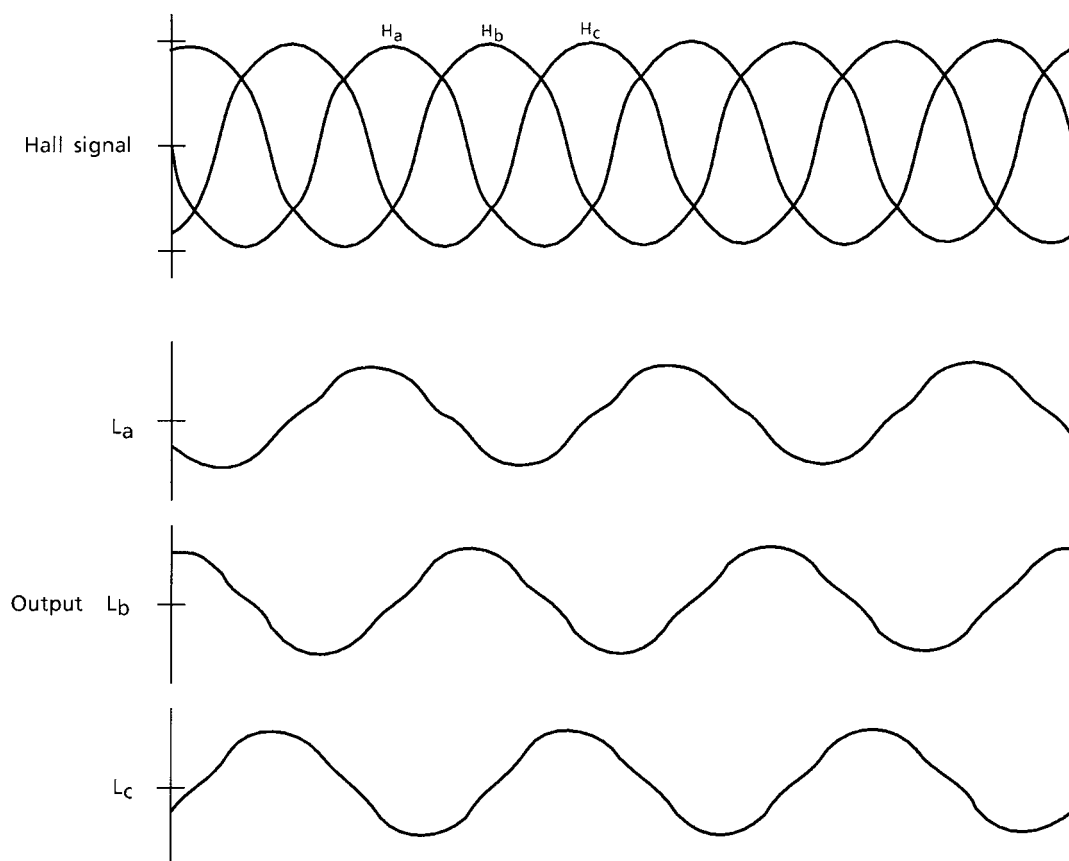
BLOCK DIAGRAM



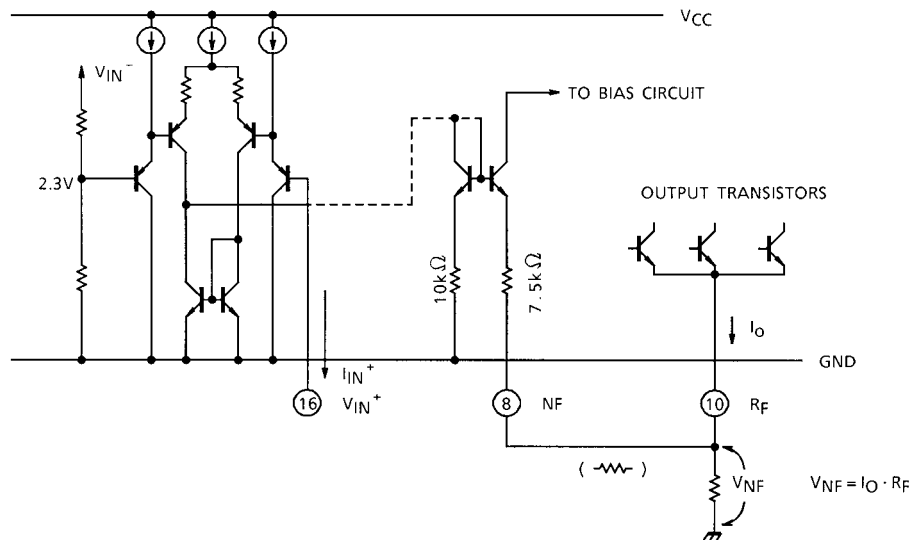
FUNCTION ($V_{IN-} = 5\text{ V}$)

MODE	FRS	V_{IN}	OUTPUT
CW	L	$V_{IN}^{+} > 2.3\text{ V}$	$L_a = H_a - H_b$ $L_b = H_b - H_c$ $L_c = H_c - H_a$
CCW	H	$V_{IN}^{+} > 2.3\text{ V}$	$L_a = -(H_a - H_b)$ $L_b = -(H_b - H_c)$ $L_c = -(H_c - H_a)$
Standby	M	—	Mid-point potentia (Note)
Brake	—	$V_{IN}^{+} < 2.3\text{ V}$	Mid-point potentia (Note)

Note: Low-impedance Mode



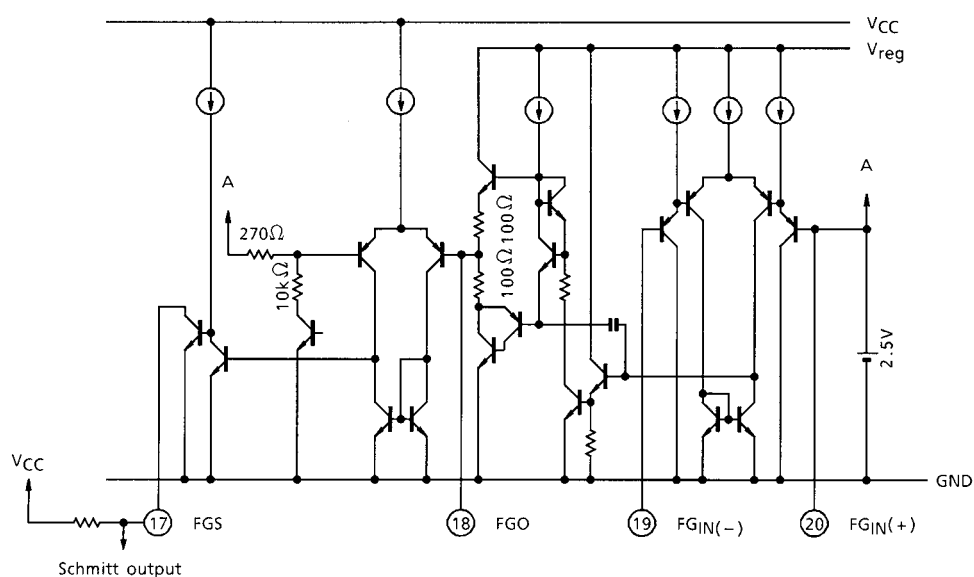
1. Control input circuit



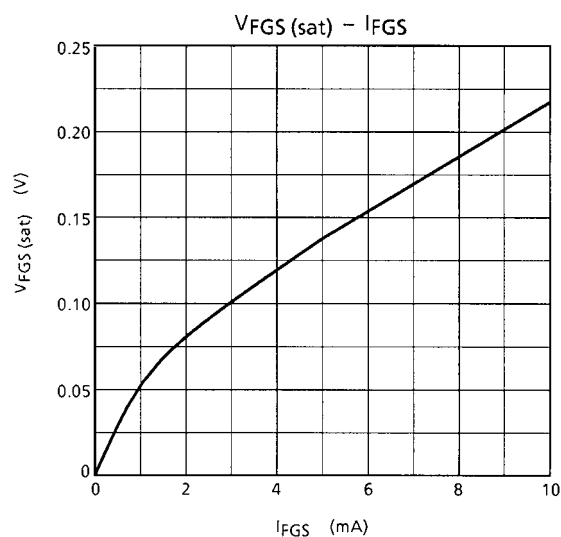
In the case of output current feedback to the motor, connect feedback resistance to R_F pin (10) and feed it back to NF pin (8).

The feedback amount can be adjusted by connecting a resistor between pin (10) and (8) pin.

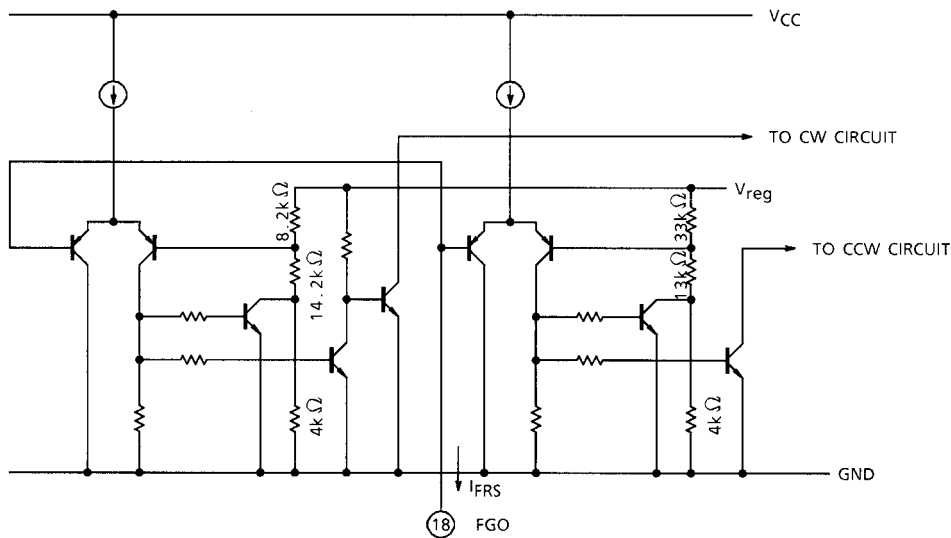
2. FG amplifier and schmitt circuit



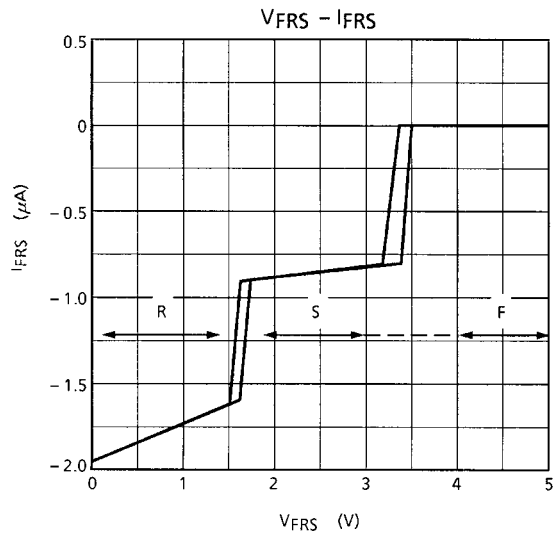
The FG amplifier is stored with internal reference voltage (2.5 V), making it possible to directly input the FG signal from pattern FG. The Schmitt circuit stored within can output wave-shaped FG signals. FGO is in push-pull mode with low impedance.



3. FRS section



Voltage applied to FRS pin (15) makes it possible to select forward, reverse, and stop modes. For the relationships between FRS, control input, and output, refer to the item on these functions. The relationship between input voltage (VFRS) and input current (IFRS) to the DRS pin (15) is shown as a feature in the following graph :



ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC}	18	V
Output Current	I _O	1.2	A
FG Output Current	I _{FGO}	12	mA
	I _{FGS}	14	
Power Dissipation	P _D	1.0 (Note 1)	W
		3.2 (Note 2)	
		5.8 (Note 3)	
Operating Temperature	T _{opr}	−30~75	°C
Storage Temperature	T _{stg}	−55~150	°C

Note 1: Without heat sink

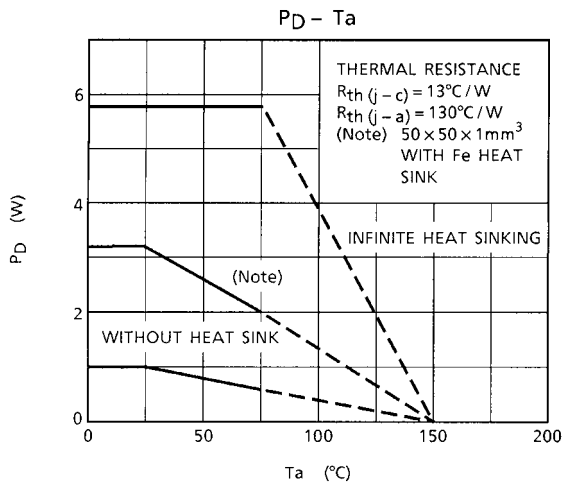
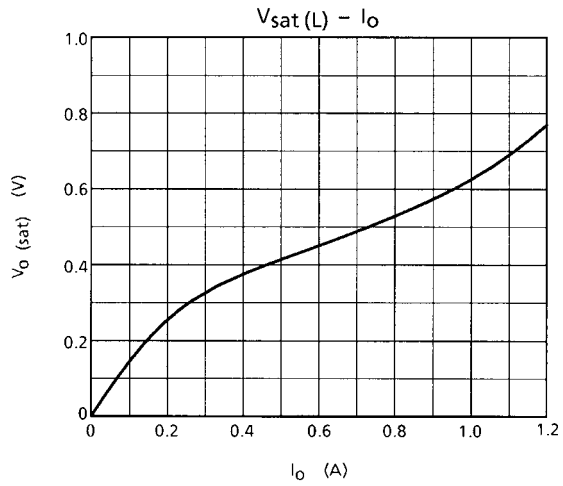
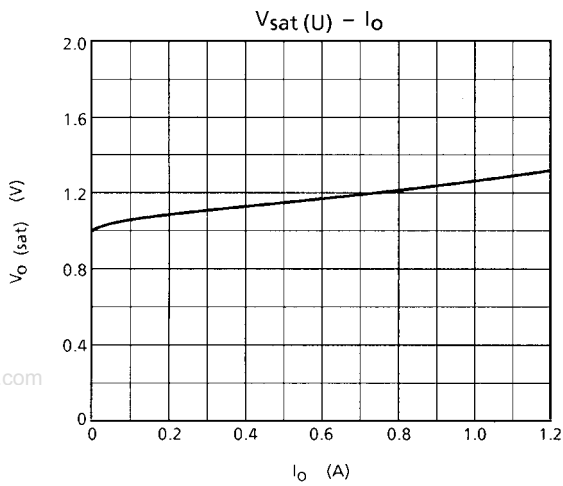
Note 2: 50 × 50 × 1 mm Fe board mounting

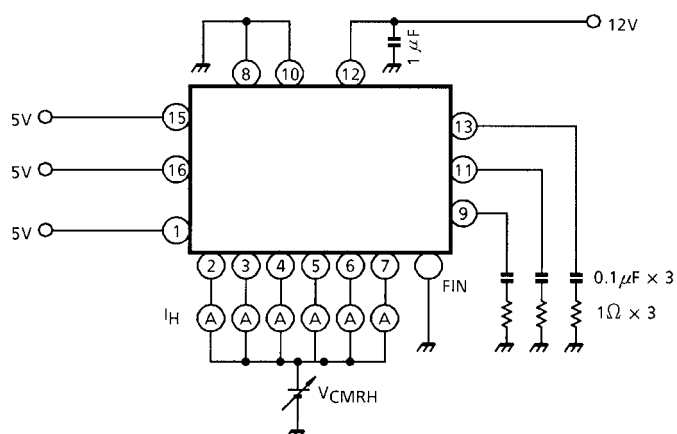
Note 3: Infinite heat sink mounting

ELECTRICAL CHARACTERISTICS ($V_{CC} = 12\text{ V}$, $V_{IN^-} = 5\text{ V}$, $T_a = 25^\circ\text{C}$)

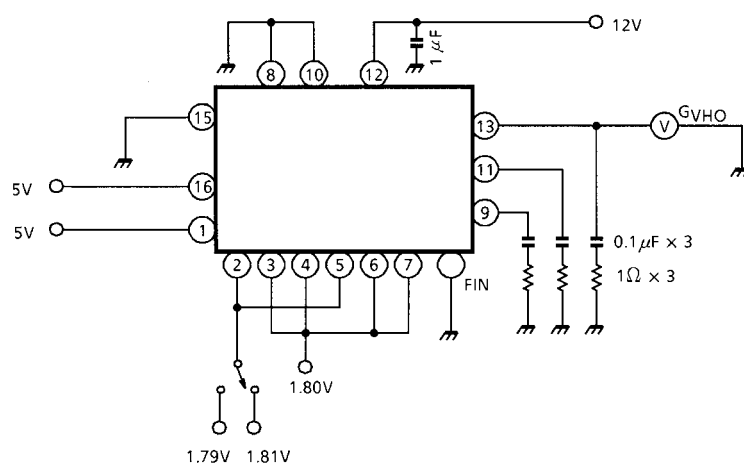
CHARACTERISTIC			SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Supply Current			I _{CC 1}	1	Output Open, FRS = 2.5 V	—	12.5	28	mA
			I _{CC 2}	1	Output Open, FRS = GND	—	14	28	
			I _{CC 3}	1	Output Open, FRS = 5 V	—	14	28	
Speed Control Circuit	Input Voltage Range		V _{CIN}	2		GND	—	V _{CC} -2.5	V
	Control Output Voltage Gain		G _{VCO}	2	V _H = 25 mV _{p-p}	7.5	13	18	dB
	Input Current		I _{CIN}	2	V _{IN} ⁺ = GND (Sink Current)	—	0.2	5	μA
	Internal Reference Voltage 1		V _{ref 1}	—		2.15	2.30	2.45	V
Position Detecting Circuit	Common Mode Inoput Voltage Range		V _{CMRH}	3		1.5	—	5	V
	Input Current		I _H	3	V _{INH} = 2.5 V	—	0.2	3	μA
	Input Voltage Gain		G _{VHO}	4	V _{IN} ⁺ = 5 V	40	47	51	dB
Output Circuit	Saturation Voltage	Upper Side	V _{sat} (U)	5	I _O = 1.0 A	—	1.2	1.9	V
		Lower Side	V _{sat} (L)	5	I _O = 1.0 A	—	0.7	1.5	
	Quiescent Voltage		V _{OS}	5	V _{IN} ⁺ = 1.0 V	5.0	5.5	7.0	V
	Quiescent Voltage Difference		V _{OOF}	5	Each Output to Output	—	25	50	mV
	FG Amp	Open Loop Voltage Gain		G _{VFG}	—	f _{FG} = 1 kHz	—	70	—
Band Width		f _{FG}	6		DC	—	50	kHz	
FGO Output Amplitude		V _{FGO}	6	I _{FGO} = 5 mA	1.0	2.1	4	V	
FGS Output Saturation Voltage		V _{sat} (FGS)	6	I _{FGS} = 4 mA	—	0.15	0.25	V	
Internal Reference Voltage 2		V _{ref 2}	6		2.1	2.5	2.9	V	
Schmitt Circuit Hysteresis Width		V _{HYS}	6		—	100	250	mV	
Rotation Direction Control Circuit	FWD Operating Voltage		V _{FWD}	5		4.0	—	V _{CC}	V
	Stop Operating Voltage		V _{STOP}	5		1.9	—	3.1	V
	Reverse Operating Voltage		V _{REV}	5		0	—	1.3	V
Thermal Shutdown Operating Temperature			T _{SD}	—		150	—	—	°C

CHARACTERISTICS OF OUTPUT AMP SATURATION VOLTAGE

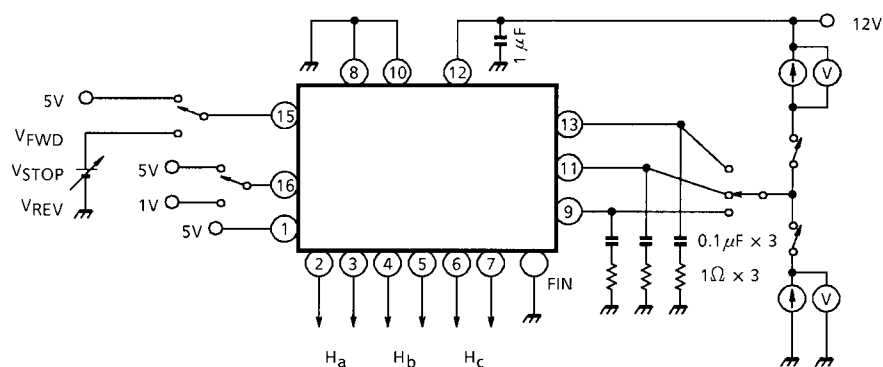




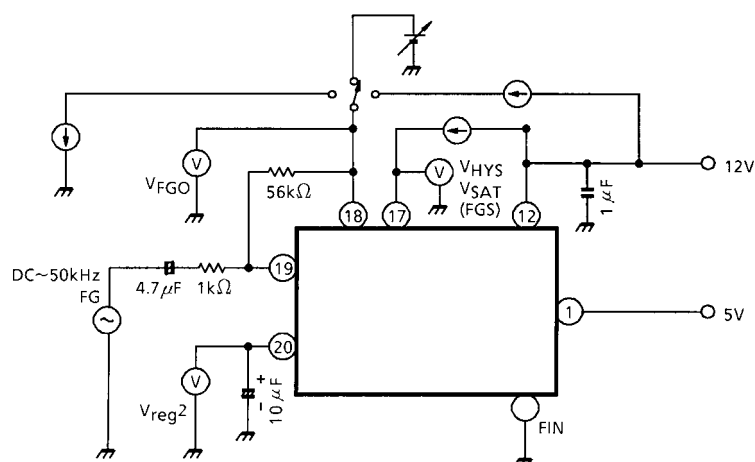
TEST CIRCUIT 4



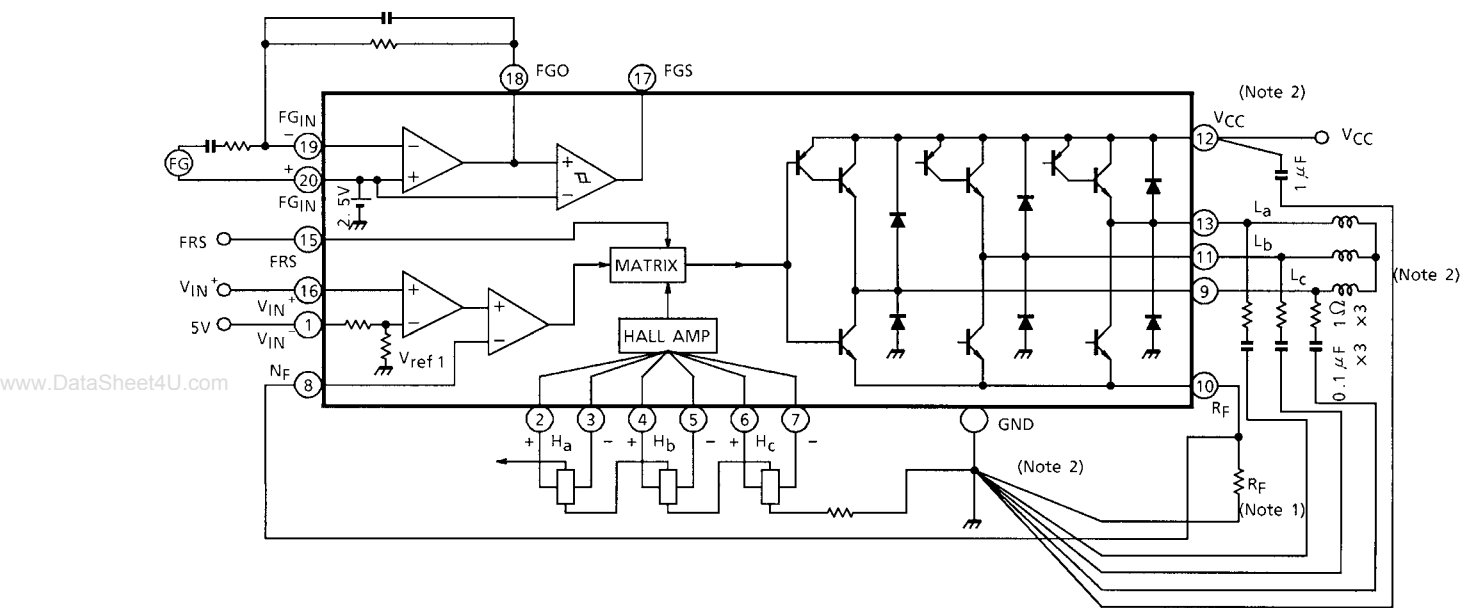
TEST CIRCUIT 5



TEST CIRCUIT 6



APPLICATION CIRCUIT



Note 1: All output currents flow into R_F pins ; therefore, be sure to provide GND separately from other GND lines.

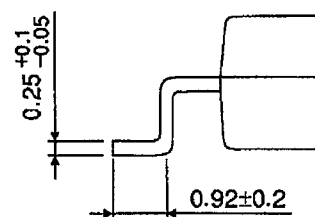
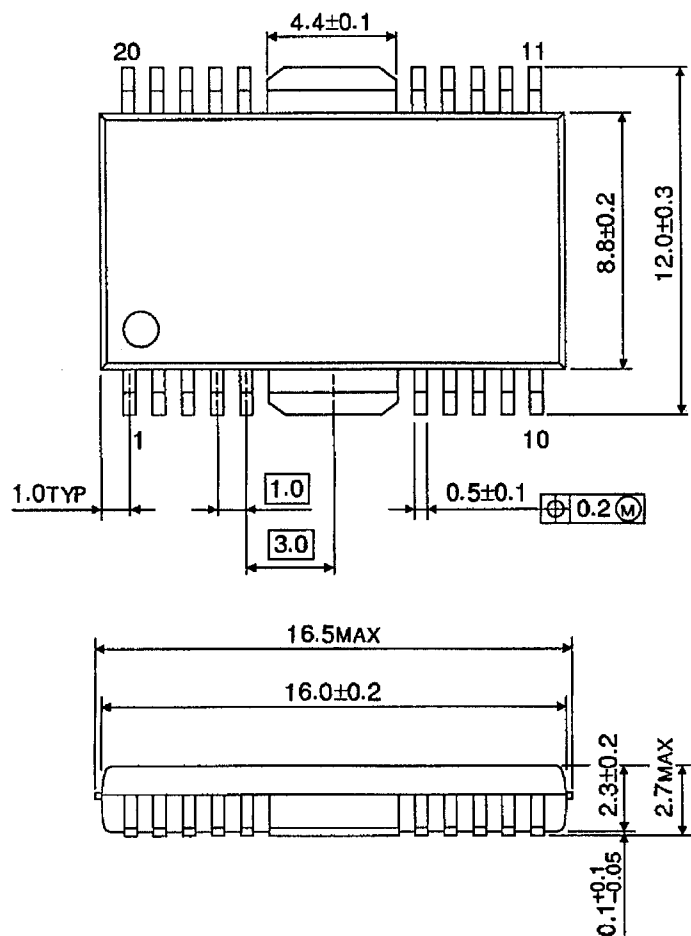
Care should be taken not to have common impedance among other GND lines, either, in making pattern designs (especially for Hall Sensor GND line).

Note 2: Utmost care is necessary in the design of the output, V_{CC} , V_M , and GND lines since the IC may be destroyed by short-circuiting between outputs, air contamination faults, or faults due to improper grounding, or by short-circuiting between contiguous pins.

PACKAGE DIMENSIONS

HSOP20-P-450-1.00

Unit : mm



Weight : 0.79 g (Typ.)

Notes on Contents

1. Block Diagrams

Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purposes.

2. Equivalent Circuits

The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.

3. Timing Charts

Timing charts may be simplified for explanatory purposes.

4. Application Circuits

The application circuits shown in this document are provided for reference purposes only. Thorough evaluation is required, especially at the mass production design stage.

Toshiba does not grant any license to any industrial property rights by providing these examples of application circuits.

5. Test Circuits

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

IC Usage Considerations

Notes on handling of ICs

- [1] The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
- [2] Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- [3] If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition.
Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- [4] Do not insert devices in the wrong orientation or incorrectly.
Make sure that the positive and negative terminals of power supplies are connected properly.
Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.

Points to remember on handling of ICs**(1) Thermal Shutdown Circuit**

Thermal shutdown circuits do not necessarily protect ICs under all circumstances. If the thermal shutdown circuits operate against the over temperature, clear the heat generation status immediately.

Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the thermal shutdown circuit to not operate properly or IC breakdown before operation.

(2) Heat Radiation Design

In using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (T_J) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown.

In addition, please design the device taking into consideration the effect of IC heat radiation with peripheral components.

(3) Back-EMF

When a motor rotates in the reverse direction, stops or slows down abruptly, a current flows back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

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