#### TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TA8464K**

#### **DUAL POWER OPERATIONAL AMPLIFIER**

The TA8464K is a dual power operational amplifier with the output current 1.2 A (PEAK).

This amplifier is usable for CD player arm driver, brushed motor forward / reverse rotation control driver, and FDD / HDD voice coil motor.

Furthermore, this amplifier is best suited for LDP focus tracking actuator driver because of its high through rate.

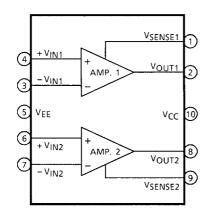
#### **FEATURES**

- Provided with a Current Limiter.
- High Output Current : IO (PEAK) = 1.2 A
- Internal Phase Compensation Type.
- Less Crosstalk :  $C_T = 55 \text{ dB (Typ.)}$
- High Slew Rate  $: SR = 1.0 \text{ V} / \mu \text{s} \text{ (Typ.)}$

# HSIP10-P-2.54

Weight: 2.47 g (Typ.)

#### **BLOCK DIAGRAM**



# **PIN FUNCTION**

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION			
1	V <sub>SENSE1</sub>	V <sub>SENSE1</sub> AMP. 1 output current detective terminal			
2	V <sub>OUT1</sub>	AMP. 1 output terminal			
3	-V <sub>IN1</sub>	AMP. 1 input terminal (-)			
4	+V <sub>IN1</sub>	AMP. 1 input terminal (+)			
5	V <sub>EE</sub>	Negative-side voltage supply terminal			
6	+V <sub>IN2</sub>	AMP. 2 input terminal (+)			
7	-V <sub>IN2</sub>	AMP. 2 input terminal (-)			
8	V <sub>OUT2</sub>	AMP. 2 output terminal			
9	V <sub>SENSE2</sub>	AMP. 2 output current detective terminal			
10	V <sub>CC</sub>	Positive-side voltage supply terminal			

# **MAXIMUM RATINGS (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	$V_{CC}$ , $V_{EE}$	±18	V
Output Current	I <sub>O (PEAK)</sub>	1.2	Α
Power Dissipation	P <sub>D</sub>	12.5 (Note)	W
Operating Temperature	T <sub>opr</sub>	-30~75	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

Note: Tc = 25°C

# **ELECTRICAL CHARACTERISTICS**

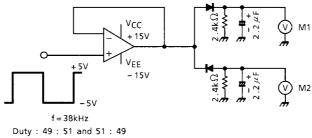
(Unless otherwise specified,  $V_{CC}$  = 15 V,  $V_{EE}$  = -15 V, Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Supply Current		Icc	_	_	_	17	25	mA
Input Offset Current		I <sub>IO</sub>	_	_	_	3	100	nA
Input Bias Current		lı	_	_	_	98	300	nA
Input Offset Voltage		V <sub>IO</sub>	_	_	_	0	7	mV
Maximum Output Voltage	Upper	V <sub>OH</sub>	_	V <sub>CC</sub> = ±15 V, I <sub>O</sub> = 300 mA	12.2	13.3	_	V
	Lower	V <sub>OL</sub>			-12.2	-13.3	_	
	Upper	V <sub>OH</sub>	_	V <sub>CC</sub> = ±6 V, I <sub>O</sub> = 1 A	2.0	3.9	_	- v
	Lower	V <sub>OL</sub>			-2.0	-4.0	-	
Open Loop Gain		G <sub>VO</sub>	_	_	_	80	-	dB
Input Common Mode Voltage Range		CMR	_	_	±13	±14	-	V
Common Mode Rejection Ratio		CMRR	_	V <sub>IN</sub> = -10~10 V	90	113	-	dB
Supply Voltage Rejection Ratio		SVRR	_	V <sub>CC</sub> = -V <sub>EE</sub> = 6~15 V±1 V	_	65	100	μV / V
Slew Rate		SR	_	_	_	1.0	-	V / µs
Output Limiting Current		I <sub>SC</sub>	_	R <sub>SC</sub> = 0.68 Ω	0.8	1.0	-	Α
Crosstalk		C <sub>T</sub>	_	V <sub>IN</sub> = -14~14 V	_	55	-	dB
Slew Rate Symmetry		SR'	1	INPUT : Duty (49 : 51 / 51 : 49) Square wave	_	0.02	1.0	V

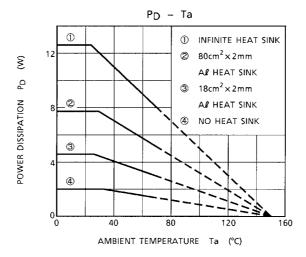
2

## **TEST CIRCUIT 1**

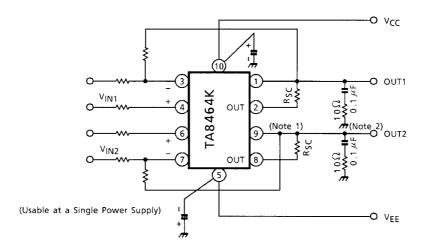
## Slew rate, symmetry SR'



Duty: 49: 51 and 51: 49 Measured Value M = M1 + M2



#### **APPLICATION CIRCUIT 1**



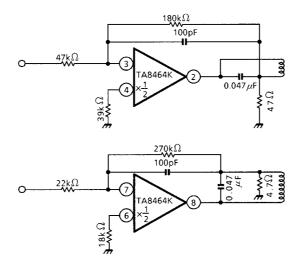
Note 1:  $I_{SC} \approx \frac{0.7(V)}{R_{SC}(\Omega)}(A)$ 

Note 2: If crosstalk is recognizable remarkably in applications above 80 kHz, change a capacitor to one having a value of about 0.33  $\mu$ F as a compensating circuit. Further, no resistor is needed in this case.

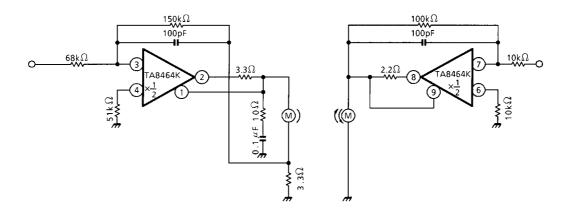
Note 3: Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

3 2001-06-13

# **APPLICATION CIRCUIT 2**



# **APPLICATION CIRCUIT 3**

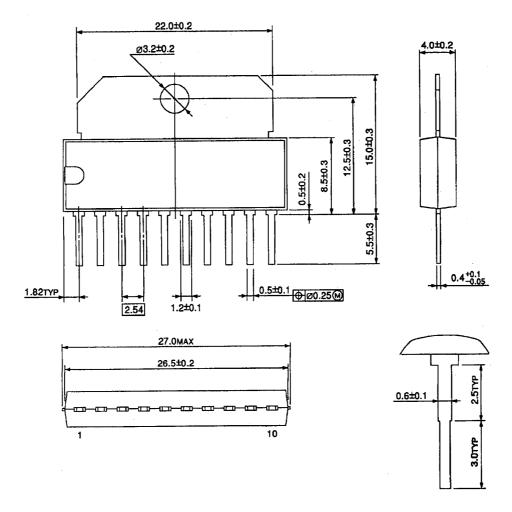


4



# **PACKAGE DIMENSIONS**

HSIP10-P-2.54 Unit: mm



Weight : 2.47 g (Typ.)

5 2001-06-13

# RESTRICTIONS ON PRODUCT USE

000707EBA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.