TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TA8052AS**

### 0.3A MOTOR DRIVER WITH BRAKE FUNCTION

The TA8052AS is a full-bridge driver which directly drives a bidirectional DC motor. Inputs DI1 and DI2 are combined to select one of forward, reverse, stop, and brake modes. Since the inputs are TTL-compatible, the IC can be directly controlled from a CPU or other control system. The IC also has various protective functions.

#### FEATURES

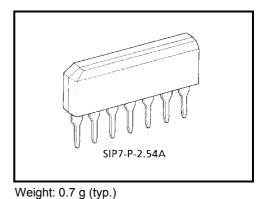
- Output current : 300mA (max.)
- Four modes : Forward, reverse, stop, and brake
- Low Standby Current : 100 µ A (Max.)
- Multiple protective functions

: Thermal shutdown, current limiter, and overvoltage shut down.

- Bulit-in diode for counteracting counter electromotive force
- Small SIP-7pin

#### THERMAL SHUTDOWN CURRENT OVERVOLTAGE LIMITER PROTECTION CONTROL LOGIC Å \* 3 M(+) (4) GND (5) M(-) 6 (2 Z DI2 Vcc ٧ş

#### **BLOCK DIAGRAM AND PIN LAYOUT**



# TOSHIBA

#### **PIN DESCRIPTION**

PIN No.	SYMBOL	DESCRIPTION			
1	DI1	Output status control pin.			
2	DI2	Connects to a PNP-type voltage comparator.			
3	M (+)	Connects to the DC motor. Diodes for absorbing counter electromotive force are contained on the V $_{\rm CC}$ and GND sides.			
4	GND	Grounded			
5	M (-)	Connects to the DC motor together with pin 3 and has the same function as pin 3. This pin is controlled by the inputs from pins 1 and 2.			
6	V <sub>CC</sub>	Power supply pin. This pin has a function to turn off the output when the applied voltage exceeds 30V, thus protecting the IC and the load.			
7	V <sub>S</sub>	Power supply pin for the control section. This pin is completely separated from the $V_{CC}$ pin.			

#### **TRUTH TABLE**

INF	TUY	OUT	PUT				
DI1	DI2	M (+)	M (-)	OUTPUT MODE			
Н	Н	L	L	BRAKE			
L	Н	L	Н	REVERSE			
Н	L	Н	L	FORWARD			
L	L	OFF (high i	mpedance)	STOP (*)			

\*: LOW STANDBY CURRENT MODE : 100µA (MAX.)

## MAXIMUM RATINGS (Ta = 25°C)

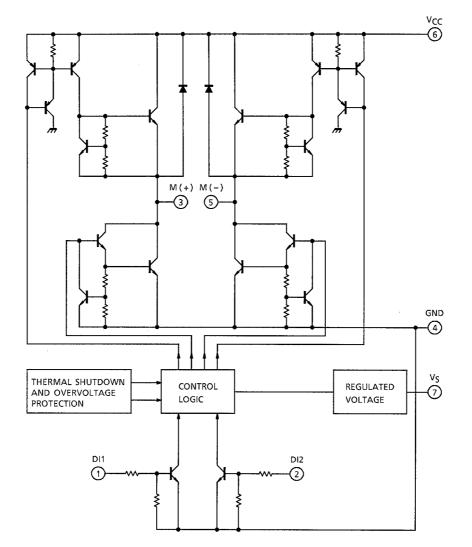
CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V <sub>CC</sub>	50 (1s)	V	
Input Voltage	V <sub>IN</sub>	-0.3~V <sub>CC</sub> +0.3	V	
Output Current	I <sub>OUT</sub>	300	mA	
Power Dissipation	PD	0.92	W	
Operation Temperature	T <sub>opr</sub>	-40~85	°C	
Storage Temperature	T <sub>stg</sub>	-55~150	°C	
Lead Temperature Time	T <sub>sol</sub>	260 (10s)	°C	

# ELECTRICAL CHARACTERISTICS (VS, $V_{CC} = 8 \sim 16V$ , Ta = $-40 \sim 85^{\circ}C$ )

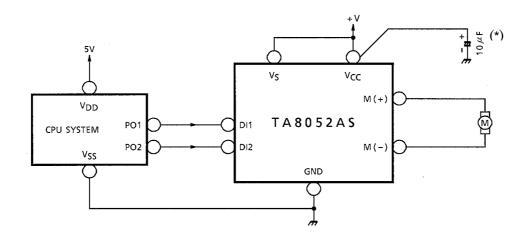
CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
	I <sub>S1</sub>	Vs	-	Stop	-	-	0.05	mA
Current Consumption (I)	I <sub>S2</sub>		_	Forward / Reverse	_	6	15	
	I <sub>S3</sub>			Brake	_	9	20	
	I <sub>CC1</sub>	V <sub>CC</sub>	_	Stop	_	_	0.05	mA
Current Consumption (II)	I <sub>CC2</sub>		_	Forward / Reverse	_	7.5	15	
	I <sub>CC3</sub>		_	Brake	_	_	1	
Input Voltage	VIL	DI1 / DI2			_	_	0.8	v
input voltage	V <sub>IH</sub>		_		2.0	_	_	
Input Current	IIL	DI1 / DI2	_	V <sub>IN</sub> = 0.4V	_	10	20	μA
Input Current	IIH			V <sub>IN</sub> = 5V	_	170	350	
Output Saturation Voltage	V <sub>sat</sub> (total)	M (+) / M (-)	_	I <sub>O</sub> = 200mA	_	1.8	2.5	V
Output Leakage Current	I <sub>LEAK-U</sub>	M (+) / M (-)	_	V <sub>O</sub> = 0V	_	_	-100	μA
Output Leakage Current	I <sub>LEAK-L</sub>		_	V <sub>O</sub> = V <sub>CC</sub>	_	_	100	
Diode Forward Voltage	V <sub>F-U</sub>	M (+) / M (-)	_	I <sub>F</sub> = 200mA	_	1.1	_	v
Diode Forward Voltage	V <sub>F-L</sub>			I <sub>F</sub> = 200mA	_	1.1	_	
Output Limit Current	I <sub>SC</sub>		_	Ta = 25°C	0.3	0.55	_	А
Shutdown Tomporaturo	T <sub>SD-H</sub>		_	$ON \rightarrow OFF$	_	150		°C
Shutdown Temperature	T <sub>SD-L</sub>		_	$OFF\toON$	_	130	_	
Overvoltage Detection	V <sub>SD</sub>		_		27	30	33	V
Transfer Dolou Time	t <sub>pLH</sub>		_		—	1	10	μs
Transfer Delay Time	t <sub>pHL</sub>		_		_	1	10	

# **TOSHIBA**

### I / O EQUIVALENT CIRCUIT



### **EXAMPLE OF APPLICATION CIRCUIT**



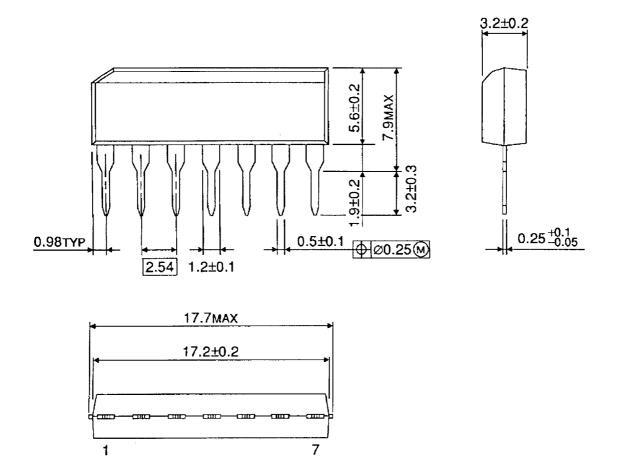
\*: Connect this capacitor as close to the IC as Possible.

# **TOSHIBA**

# PACKAGE DIMENSIONS

SIP7-P-2.54A

Unit : mm



Weight: 0.7g (Typ.)

#### **RESTRICTIONS ON PRODUCT USE**

Handbook" etc..

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