

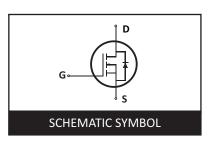
N-CHANNEL POWER MOSFET

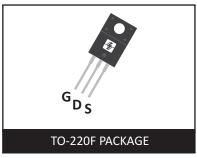
DESCRIPTION

This MOSFET is produced with advanced VDMOS technology of SEMIWILL. This technology enable power MOSFET to have better characteristics, such as fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics. This power MOSFET is usually used at high efficient DC to DC converter block and SMPS. It's typical application is TV and monitor.

FEATURES

- High ruggedness
- RDS(ON)(Max. 1.8Ω)@VGS=10V
- Gate Charge (Typ.26nC)
- Improved dv/dt Capability
- 100% Avalanche Tested





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter		Value	Unit
V _{DSS}	Drain to Source Voltage		900	V
I _D	Continuous Drain Current (@T _C =25 °C)		7.0	A
	Continuous Drain Current (@T _C =100 °C)		4.4	A
I _{DM}	Drain current pulsed	(note 1)	28	А
V _{GS}	Gate to Source Voltage		±30	V
E _{AS}	Single pulsed Avalanche Energy	(note 2)	780	mJ
E _{AR}	Repetitive Avalanche Energy	(note 1)	21	mJ
dv/dt	Peak diode Recovery dv/dt	(note 3)	4.0	V/ns
P _D	Total power dissipation (@T _C =25 °C)		32	W
	Derating Factor above 25°C		0.8	W/°C
T _{STG} , T _J	Operating Junction Temperature & Storage Temperature		-55 ~ + 150	°C
T _L	Maximum Lead Temperature for soldering purpose, 1/8 from Case for 5 seconds.		300	°C

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
R _{thjc}	Thermal resistance, Junction to case 3.87		°C/W
R _{thja}	Thermal resistance, Junction to ambient	hermal resistance, Junction to ambient 62.5	



ELECTRICAL CHARACTERISTIC ($T_C = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Off charact	eristics			•	•	
BV _{DSS}	Drain to source breakdown voltage	Vgs=0V,ID=250uA	900	-	-	V
I _{DSS}	Drain to source leakage current	V _{DS} =800V,V _{GS} =0V	-	-	10	uA
		VDS=640V,Tc=125°C	-	-	100	uA
I _{GSS}	Gate to source leakage current, forward	V _{DS} =30V,V _{GS} =0V	-	-	100	nA
	Gate to source leakage current, reverse	Vps=-30V,Vgs=0V	-	-	-100	nA
On characte	eristics					
V _{GS(TH)}	Gate threshold voltage	VDS=VGS,ID=250uA	3.0	-	5.0	V
R _{DS(ON)}	Drain to source on state resistance	Vgs=10V,ID=1.5A	-	-	1.8	Ω
Dynamic ch	aracteristics					
C _{iss}	Input capacitance	VGS=0V,VDS=25V,f=1MHz	-	1440	1880	pF
C oss	Output capacitance		-	140	185	
C _{rss}	Reverse transfer capacitance		-	17	23	
$t_{d(on)}$	Turn on delay time		-	35	80	ns
tr	Rising time	VDs=400V,ID=3.0A,Rg=25ohm (note 4,5)	-	80	170	
t _{d(off)}	Turn off delay time		-	95	200	
t _f	Fall time		-	55	120	
Qg	Total gate charge	VDS=640V,VGS=10V,ID=3.0A (note 4,5)	-	40	52	nC
Q_{gs}	Gate -source charge		-	8.5	-	
Q_{gd}	Gate -drain charge		-	20	-	

SOURCE TO DRAIN DIODE RATINGS CHARACTERISTICS

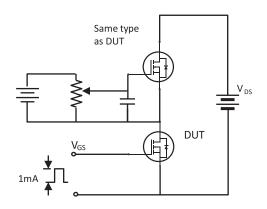
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Is	Continuous source current	Integral reverse p-n Junction diode in the MOSFET	-	-	6.4	А
I _{SM}	Pulsed source current		-	-	25.6	А
V _{SD}	Diode forward voltage drop.	Is=3.0A, VGs=0V	-	-	1.4	V
T _{rr}	Reverse recovery time	Is=3.0A, VGs=0V	-	400	-	ns
Q _{rr}	Breakdown voltage temperature	dlF/dt=100A/us	-	4.3	-	uC

Notes

- $1. \ \ Repeat it ive\ rating: pulse\ width\ limited\ by\ junction\ temperature.$
- 3. ISD \leq 3.0A, di/dt = 200A/us, VDD \leq BVDSS, Staring TJ=25°C
- 4. Pulse Test : Pulse Width ≤ 300us, duty cycle ≤ 2%
- $5. \ Essentially independent of operating temperature.\\$



Fig. 1. Gate charge test circuit & waveform



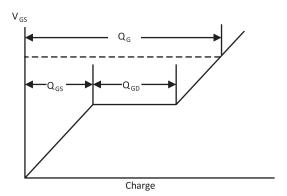
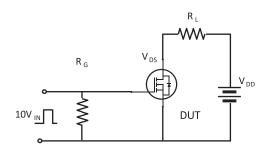


Fig. 2. Switching time test circuit & waveform



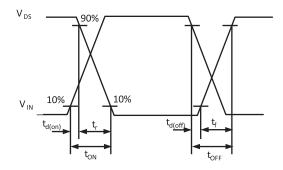
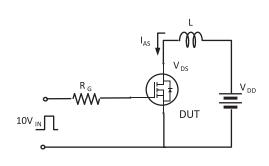


Fig. 3. Unclamped Inductive switching test circuit & waveform



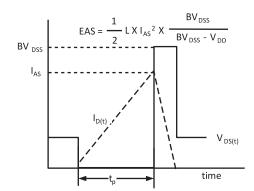
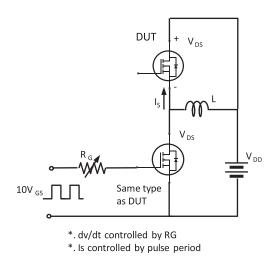
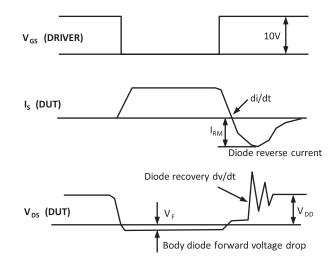




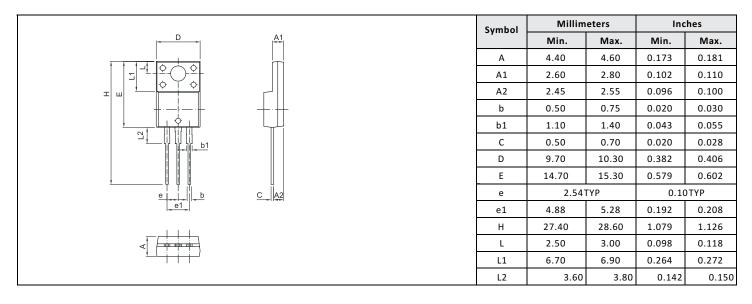
Fig. 4. Peak diode recovery dv/dt test circuit & waveform





PACKAGE DIMENSIONS

TO-220F





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