

SOLID STATE DEVICES, INC.

14830 Valley View Blvd * La Mirada, Ca 90638
Phone: (562) 404-7855 * Fax: (562) 404-1773

Designer's Data Sheet

Part Number /Ordering Information	1/
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SVR1085 - 12 D2 DB H -- Screening ^{2/} _ = Not Screened
H = High Rel Level
K = Space Level
R = Radiation Tolerant
Lead Bend: ^{3/} _ = Straight
DB = Down Bend
UB = Up Bend
Package: ^{4/} M = TO-254
Z = TO-254Z
D2 = D² Pack
D3 = D³ Pack
Voltage: _ = Variable Voltage
3.3 = 3.3V
3.6 = 3.6V
5 = 5V
12 = 12V

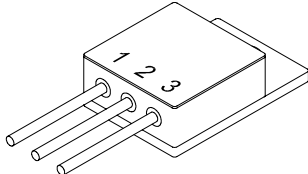
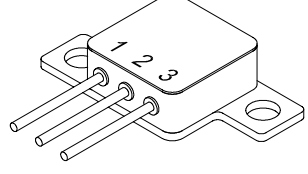
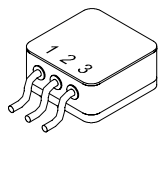
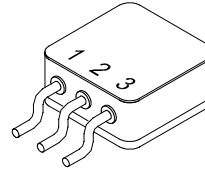
**SVR1085-3.3M, Z, D2, & D3
thru
SVR1085-12M, Z, D2, & D3**

**1.5 Amps
2.85 thru 12Volts
LOW DROPOUT
POSITIVE LINEAR
VOLTAGE REGULATOR**

FEATURES:

- Eutectic Die Attach
- Superior to LM117 Types
- Complimentary Use with SVR1086 Types
- Fast Switching
- Isolated Hermetically Sealed Power Package
- 150°C Operating Temperature
- Custom Lead Forming Available
- Ceramic Seal Package Available
- Class H or K (Space) Screening Available
- Radiation Tolerant Devices are Available

MAXIMUM RATINGS	SYMBOL	VALUE	UNITS
Power Dissipation	P _D	Internally Limited	
Input Voltage ^{5/}	V _{IN}	30	V
Operating Input Voltage	V _{IN(OP)}	20	V
SVR1085 -3.3 & -3.6		20	
SVR1085 -5		25	
In-to-Out Differential	ΔV _{IN/OUT}	35	
Operating Junction Temperature	T _J	-55 TO +150	°C
Control Section Power Transistor		-55 TO +200	
Storage Temperature	T _{STG}	-65 TO +150	°C

TO-254 (M):	TO-254 (Z):	D ² Pack (D2):	D ³ Pack (D3):
 Diagram of the TO-254 (M) package. It is a square component with three pins extending from one side. The pins are labeled 1, 2, and 3. The component is mounted on a larger base plate.	 Diagram of the TO-254 (Z) package. It is a square component with three pins extending from one side. The pins are labeled 1, 2, and 3. The component has a mounting tab on the opposite side.	 Diagram of the D² Pack (D2) package. It is a square component with three pins extending from one side. The pins are labeled 1, 2, and 3. The component has a mounting tab on the opposite side.	 Diagram of the D³ Pack (D3) package. It is a square component with three pins extending from one side. The pins are labeled 1, 2, and 3. The component has a mounting tab on the opposite side.

NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: SVR0002A

**SVR1085-3.3M, Z, D2, & D3
thru
SVR1085-12M, Z, D2, & D3**

PRELIMINARY



SOLID STATE DEVICES, INC.

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Electrical Characteristics			t°	SYMBOL	MIN	TYP	MAX	UNITS
Output Voltage ^{7/}	SVR1085 -3.3	(0 ≤ I _{OUT} ≤ 5A, 4.5V ≤ V _{IN} ≤ 15V)	*	V _{OUT}	3.235	3.300	3.365	V
	SVR1085 -3.6	(0 ≤ I _{OUT} ≤ 3A, 5V ≤ V _{IN} ≤ 15V)	*		3.500	3.600	3.672	V
	SVR1085 -5	(0 ≤ I _{OUT} ≤ I _{FULLLOAD} , 6.5V ≤ V _{IN} ≤ 20V)	*		4.900	5.000	5.100	V
	SVR1085 -12	(0 ≤ I _{OUT} ≤ I _{FULLLOAD} , 13.5V ≤ V _{IN} ≤ 25V)	*		11.76	12.00	12.24	V
Line Regulation (I _{OUT} = 0A)	SVR1085 -3.3	(4.8V ≤ V _{IN} ≤ 15V)	25	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	--	0.5	6.00	mV
			*		--	1.0	6.00	mV
	SVR1085 -3.6	(4.8V ≤ V _{IN} ≤ 15V)	25		--	0.5	6.00	mV
			*		--	1.0	6.00	mV
	SVR1085 -5	(6.5V ≤ V _{IN} ≤ 20V)	25		--	0.5	6.00	mV
			*		--	1.0	6.00	mV
	SVR1085 -12	(13.5V ≤ V _{IN} ≤ 25V)	25		--	1.0	25.0	mV
			*		--	2.0	25.0	mV
Load Regulation ^{6/, 7/}	SVR1085 -_	(ΔV = 3V, 10mA ≤ I _{OUT} ≤ I _{FULLLOAD})	25	$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	--	0.1	0.3	%
			*		--	0.2	0.4	%
	SVR1085 -3.3	(V _{IN} = 5V, 0 ≤ I _{OUT} ≤ 5A)	25		--	3	15	mV
			*		--	7	20	mV
	SVR1085 -3.6	(V _{IN} = 5V, 0 ≤ I _{OUT} ≤ 3A)	25		--	3	15	mV
			*		--	7	20	mV
	SVR1085 -5	(V _{IN} = 5.25V, 0 ≤ I _{OUT} ≤ I _{FULLLOAD})	25		--	5	20	mV
			*		--	10	35	mV
	SVR1085 -12	(V _{IN} = 8V, 0 ≤ I _{OUT} ≤ I _{FULLLOAD})	25		--	12	36	mV
			*		--	24	72	mV
Dropout Voltage ^{8/} (I _{OUT} = I _{FULLLOAD})	SVR1085 -_	(ΔV _{REF} = 15V)	*	ΔV	--	1.3	1.5	V
	SVR1085 -3.3	(ΔV _{REF} = 33mV)	*		--	1.3	1.5	V
	SVR1085 -3.6	(ΔV _{REF} = 36mV)	*		--	1.3	1.5	V
	SVR1085 -5	(ΔV _{REF} = 50mV)	*		--	1.3	1.5	V
	SVR1085 -12	(ΔV _{REF} = 120mV)	*		--	1.3	1.5	V
Current Limit	SVR1085 -_	(ΔV = 5V)	*	I _{LIMIT}	3.2	4.0	--	A
			*		0.02	0.05	--	A
	SVR1085 -3.3	(V _{IN} = 8V)	*		3.2	4.0	--	A
			*		3.2	4.0	--	A
	SVR1085 -5	(V _{IN} = 10V)	*		3.2	4.0	--	A
	SVR1085 -12	(V _{IN} = 17V)	*		3.2	4.0	--	A
Min. Load Current	SVR1085 -_	ΔV = 25V	*	I _{LOAD}	--	5	10	A
Quiescent Current	SVR1085 -3.3	(V _{IN} = 18V)	*	I _Q	--	5	10	mA
	SVR1085 -3.6	(V _{IN} = 18V)	*		--	5	10	mA
	SVR1085 -5	(V _{IN} = 20V)	*		--	5	10	mA
	SVR1085 -12	(V _{IN} = 25V)	*		--	5	10	mA

NOTE:

* Full Temperature Range



PRELIMINARY

SVR1085-3.3M, Z, D2, & D3 thru SVR1085-12M, Z, D2, & D3

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Electrical Characteristics (Continue)			t°	SYMBOL	MIN	TYP	MAX	UNITS
Thermal Regulation (30 msec Pulse)			25		--	0.004	0.02	%/W
Ripple Rejection (f =120Hz, C _{OUT} =25μF, I _{OUT} = I _{FULLLOAD})	SVR1085 -_	(ΔV=3V, C _{ADJ} =25μF)	*		60	75	--	dB
	SVR1085 -3.3	(V _{IN} =6.3V, I _{OUT} =3A)	*		60	72	--	dB
	SVR1085 -3.6	(V _{IN} =6.6V, I _{OUT} =3A)	*		60	72	--	dB
	SVR1085 -5	(V _{IN} = 8V)	*		60	68	--	dB
	SVR1085 -12	(V _{IN} = 15V)	*		54	60	--	dB
Adjust Pin Current	SVR1085 -_		25 *	I _{ADJ}	-- --	55 --	-- 120	μA μA
Adjust Pin Current Change	SVR1085 -_	10mA ≤ I _{OUT} ≤ I _{FULLLOAD} , 1.5V ≤ ΔV ≤ 25V	*	ΔI _{ADJ}	--	0.2	5.0	μA
Temperature Stability			*	$\frac{\Delta V_{OUT}}{\Delta T}$	--	0.5	--	%
Long Term Strability			125	$\frac{\Delta V_{OUT}}{\Delta V_{time}}$	--	0.3	1.0	%
RMS Output Noice (10Hz ≤ f ≤ 10kHz)			25	e _n	--	0.003	--	%
Thermal Resistance Junction to Case	Control Circuitry Power Transistor			R _{θJC}	-- --	-- --	0.9 3.0	°C/W °C/W

NOTES:

* Full Temperature Range

1/ For Ordering Information, Price, and Availability Contact Factory.

2/ Screening per MIL-PRF-19500.

3/ For Lead Bend Options Request Document # DSB-001 (Available for Download @ ssdi-power.com).

4/ For Package Outlines Request Document # DSA-001. (Available for Download @ ssdi-power.com)

5/ Devices are guaranteed to withstand transient Input Voltage up to 30V. For Input Voltages greater then the maximum operating Input Voltage some degradation of specifications will occur. For 5V and 12V devices operating at Input/Output differentials greater then 15V, a minimum external load of 5mA is required to maintain regulation.

6/ See Thermal Regulation specifications for changes in Output Voltage due to the heating effects. Line and Load Regulations are measured at a constant Junction Temperature by low duty cycle pulse testing. Load Regulation is measured at the Output Lead at approx. 1/8" from the package.

7/ Line and Load Regulation are guaranteed up to the maximum power dissipation of 30W. Power Dissipation is determined by the Input/Output differential and the Output Current. Guaranteed maximum power dissipation will not be available over the full Input/Output range.

8/ Dropout Voltage is specified over the full Output Current range of the Device.

9/ Minimum Load Current is defined as the minimum current required to maintain regulation. At 25V Input/Output differential the device is guaranteed to regulate if the Output Current is greater than 10mA.

PIN ASSIGNMENT**FUNCTION****PIN 1****PIN 2****PIN 3**

Voltage Regulator

Adjust

Input

Output

**SVR1085-3.3M, Z, D2, & D3
thru
SVR1085-12M, Z, D2, & D3**

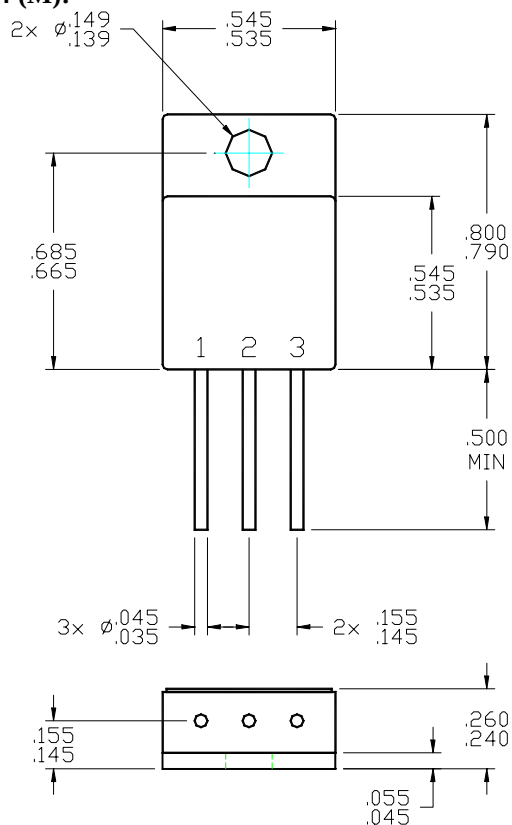
PRELIMINARY



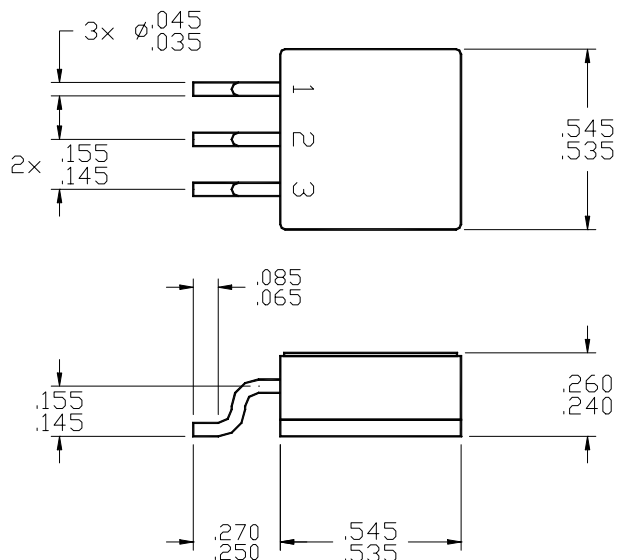
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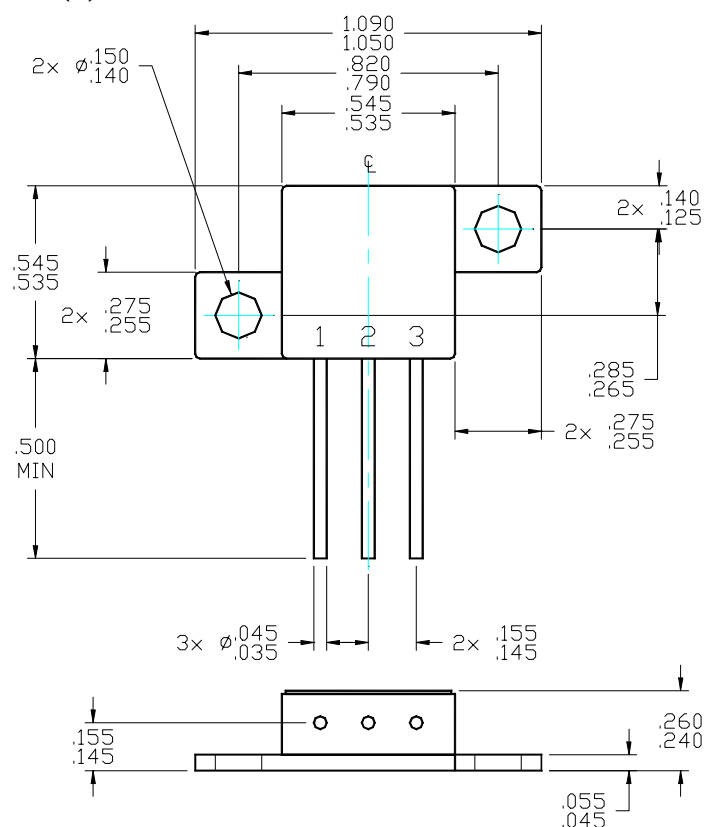
TO-254 (M):



D³ Pack (D3):



TO-254 (Z):



D² Pack (D2):

