



N-Channel 60-V (D-S) 175°C MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ (Ω)	I_D (A)
60	0.015 @ $V_{GS} = 10$ V	60 ^a

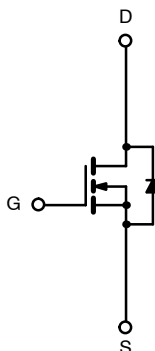
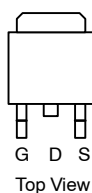
FEATURES

- TrenchFET® Power MOSFET
- 175°C Junction Temperature

APPLICATIONS

- Automotive Applications Such As:
 - ABS
 - EPS
 - Motor Drives
- Industrial

TO-263



Ordering Information: SUM60N06-15
SUM60N06-15-E3 (Lead Free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ($T_J = 175^\circ\text{C}$)	$T_C = 25^\circ\text{C}$	I_D	60	A
	$T_C = 125^\circ\text{C}$		35	
Pulsed Drain Current		I_{DM}	100	
Avalanche Current		I_{AR}	35	mJ
Repetitive Avalanche Energy ^a	$L = 0.1$ mH	E_{AR}	61	
Maximum Power Dissipation ^a	$T_C = 25^\circ\text{C}$	P_D	100 ^b	W
	$T_A = 25^\circ\text{C}$ ^c		3.75	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient—PCB Mount ^c	R_{thJA}	40	$^\circ\text{C/W}$
Junction-to-Case	R_{thJC}	1.4	

Notes

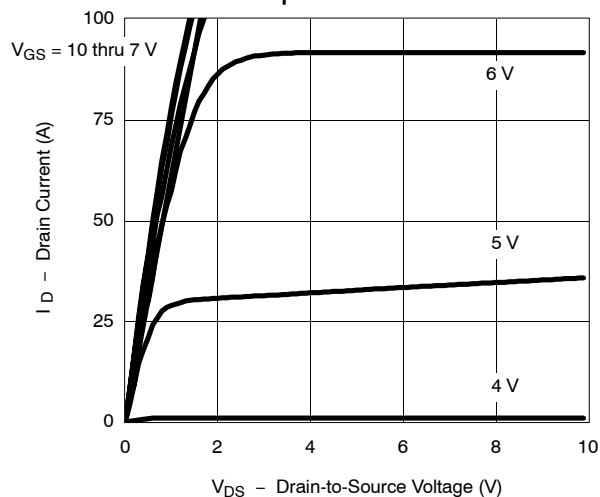
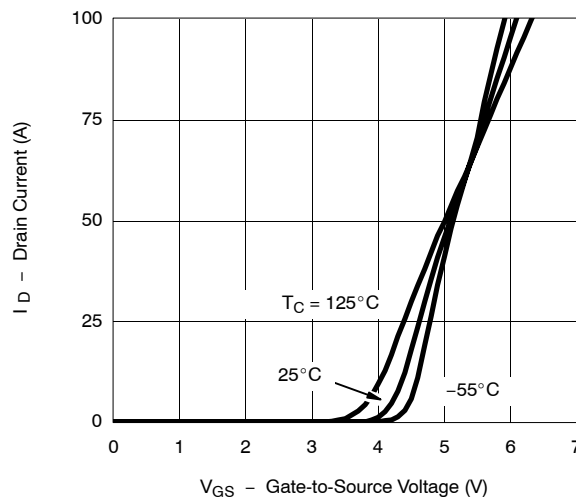
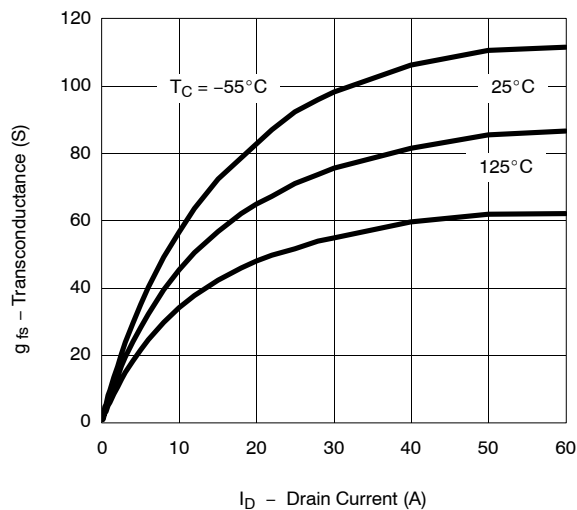
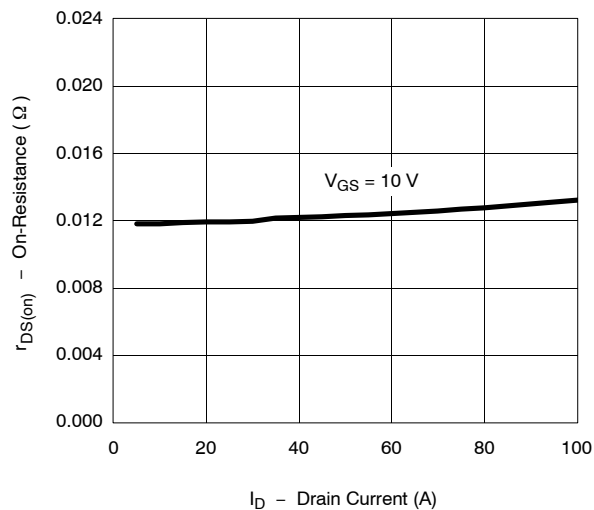
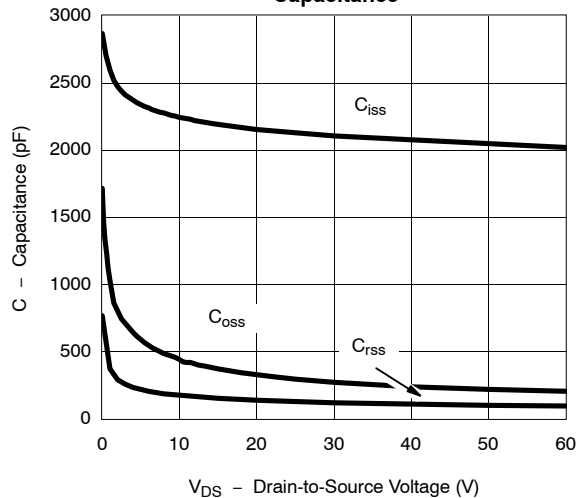
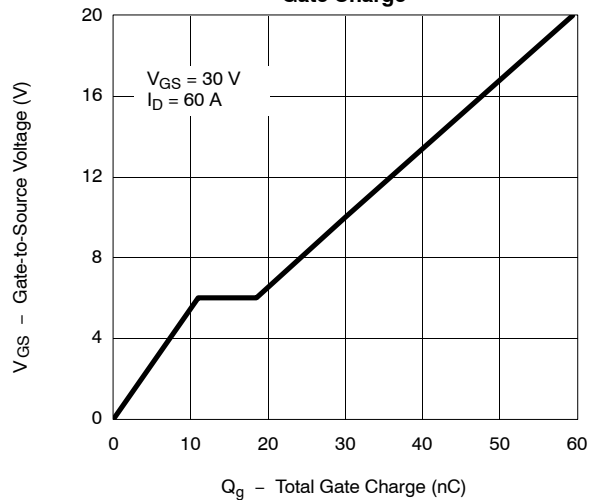
- Duty cycle $\leq 1\%$.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).

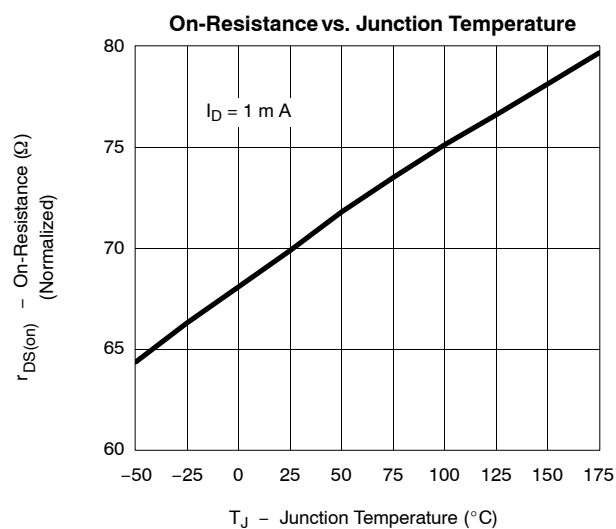
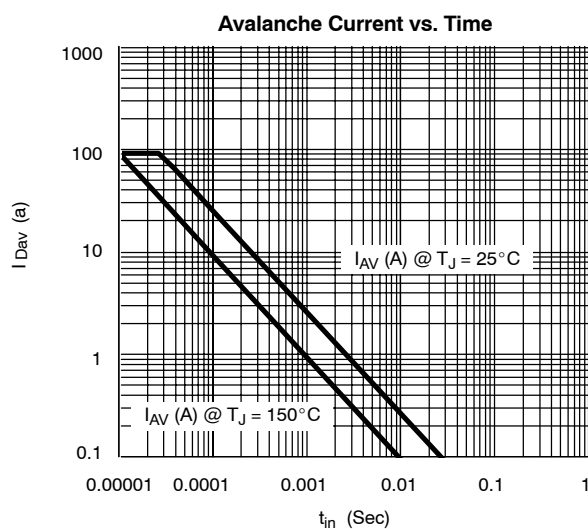
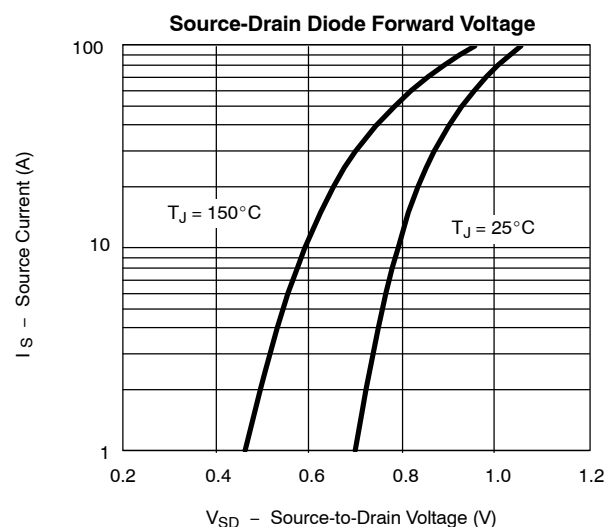
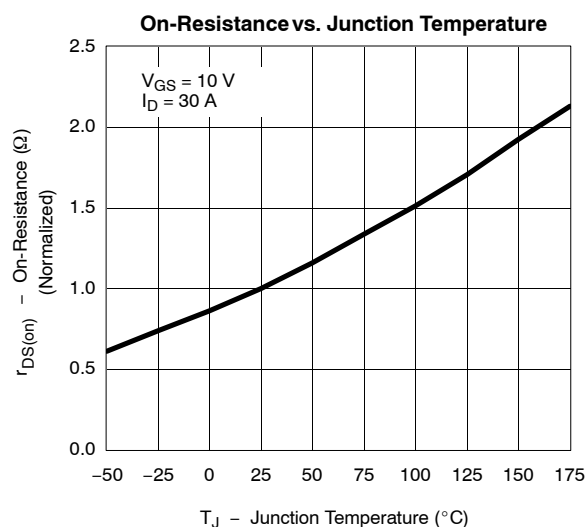
SUM60N06-15**Vishay Siliconix**

SPECIFICATIONS (T _J =25° C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V, I _D = 250 μA	60			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2		4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1	μA
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 125° C			50	
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 175° C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	100			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 30 A		0.012	0.015	Ω
		V _{GS} = 10 V, I _D = 30 A, T _J = 125° C			0.025	
		V _{GS} = 10 V, I _D = 30 A, T _J = 175° C			0.030	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 30 A	20			S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		2100		pF
Output Capacitance	C _{oss}			300		
Reverse Transfer Capacitance	C _{rss}			125		
Total Gate Charge ^c	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 60 A		30	45	nC
Gate-Source Charge ^c	Q _{gs}			11		
Gate-Drain Charge ^c	Q _{gd}			8		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 0.5 Ω I _D ≅ 60 A, V _{GEN} = 10 V, R _G = 2.5 Ω		10	15	ns
Rise Time ^c	t _r			12	20	
Turn-Off Delay Time ^c	t _{d(off)}			20	30	
Fall Time ^c	t _f			10	15	
Source-Drain Diode Ratings and Characteristics (T _C = 25° C) ^b						
Continuous Current	I _S				110	A
Pulsed Current	I _{SM}				300	
Forward Voltage ^a	V _{SD}	I _F = 30 A, V _{GS} = 0 V		1.1	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 60 A, di/dt = 100 A/μs		50	85	ns
Peak Reverse Recovery Current	I _{RM(REC)}			2	4	A
Reverse Recovery Charge	Q _{rr}			0.05	0.17	μC

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

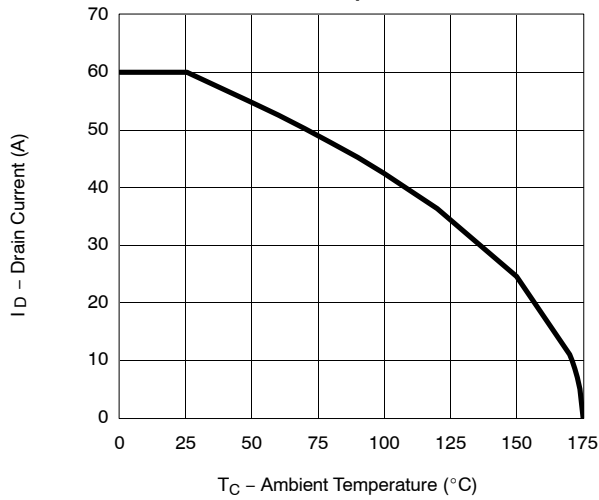

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)
Output Characteristics

Transfer Characteristics

Transconductance

On-Resistance vs. Drain Current

Capacitance

Gate Charge


SUM60N06-15**Vishay Siliconix****TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

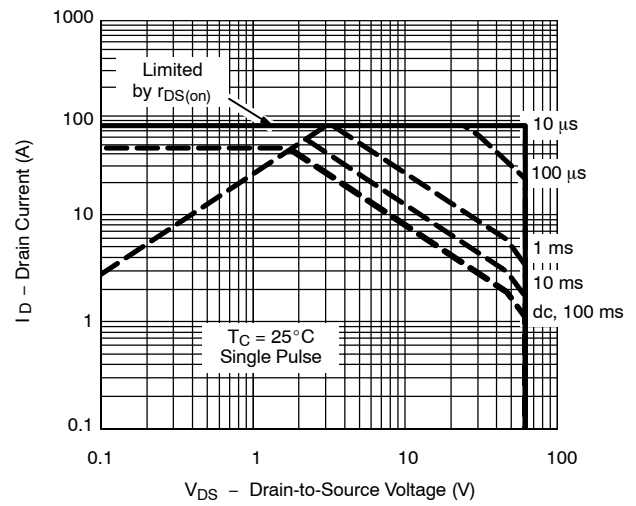


THERMAL RATINGS

Maximum Drain Current vs.
Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

