

## Isc N-Channel MOSFET Transistor

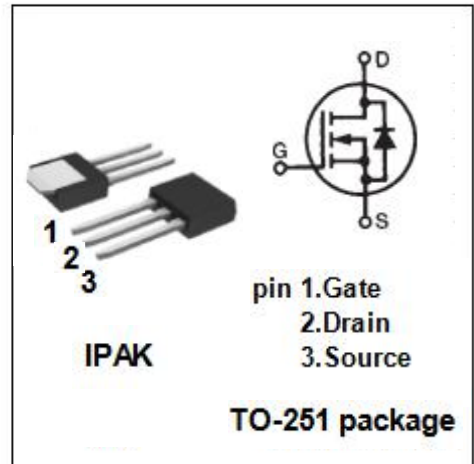
## STU10NM65N

## • FEATURES

- With TO-251(IPAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

## • APPLICATIONS

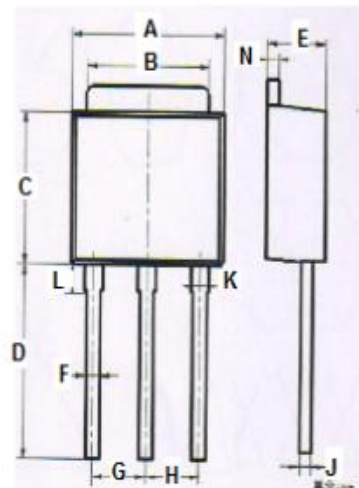
- Switching applications

• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 25$	V
$I_D$	Drain Current-Continuous@ $T_c=25^\circ\text{C}$ $T_c=125^\circ\text{C}$	9 5.7	A
$I_{DM}$	Drain Current-Single Pulsed	36	A
$P_D$	Total Dissipation @ $T_c=25^\circ\text{C}$	90	W
$T_{ch}$	Max. Operating Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55~150	$^\circ\text{C}$

## • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.38	$^\circ\text{C/W}$



DIM	mm	
	MIN	MAX
A	6.40	6.48
B	5.10	5.50
C	5.80	6.20
D	9.20	9.60
E	2.20	2.40
F	0.50	0.70
G	2.09	2.49
H	2.09	2.49
J	0.40	0.60
K	0.70	0.90
L	1.60	2.00
N	0.40	0.60

**Isc N-Channel MOSFET Transistor****STU10NM65N****ELECTRICAL CHARACTERISTICS****T<sub>c</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 1mA	650			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = ±25V; I <sub>D</sub> =0.25mA	2		4	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =4.5A			480	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±25V; V <sub>DS</sub> = 0V			±0.1	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 650V; V <sub>GS</sub> = 0V; T <sub>J</sub> =25°C T <sub>J</sub> =125°C			1 100	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>SD</sub> = 9A, V <sub>GS</sub> = 0 V			1.3	V

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