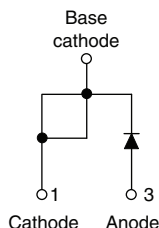


Schottky Rectifier, 20 A

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TO-220AC



FEATURES

- 125 °C T_J operation ($V_R < 5$ V)
- Center tap module
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level



RoHS*
COMPLIANT

PRODUCT SUMMARY

$I_{F(AV)}$	20 A
V_R	15 V
I_{RM}	600 mA at 100 °C

DESCRIPTION

The Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	20	A
V_{RRM}		15	V
I_{FSM}	$t_p = 5 \mu s$ sine	700	A
V_F	19 Apk, $T_J = 125$ °C (typical)	0.25	V
T_J	Range	- 55 to 125	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	STPS20L15DPbF	UNITS
Maximum DC reverse voltage	V_R	$T_J = 100$ °C	15	V
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle, T _C = 85 °C, rectangular waveform		20	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	700	A
		10 ms sine or 6 ms rect. pulse		330	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 6 mH		10	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	A

* Pb containing terminations are not RoHS compliant, exemptions may apply

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS
Forward voltage drop See fig. 1	$V_{FM}^{(1)}$	19 A	-	0.41	V
		40 A		0.52	
		19 A	-	0.25	
		40 A		0.37	
Reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	10	mA
		$T_J = 100\text{ }^{\circ}\text{C}$		600	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$	0.182		V
Forward slope resistance	r_t		7.6		m Ω
Maximum junction capacitance	C_T	$V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 $^{\circ}\text{C}$	-	2000	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body	8	-	nH
Maximum voltage rate of change	dV/dt	Rated V_R	10 000		V/ μs

Note(1) Pulse width < 300 μs , duty cycle < 2 %**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J		- 55 to 125	°C
Maximum storage temperature range	T _{Stg}		- 55 to 150	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	1.5	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased (For TO-220)	0.50	
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation (For D ² PAK)	40	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum	Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-220AC	STPS20L15D	

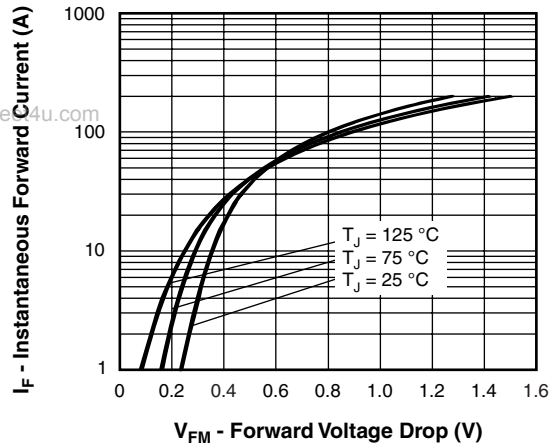


Fig. 1 - Maximum Forward Voltage Drop Characteristics

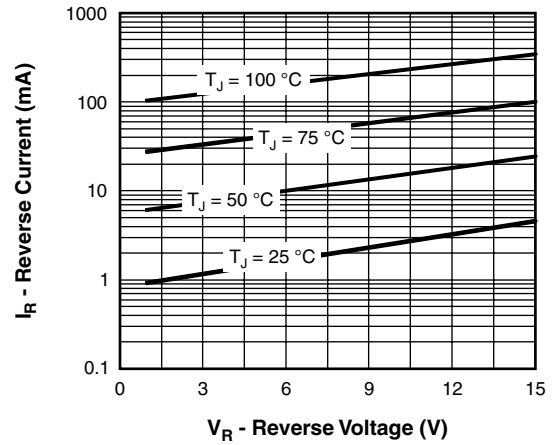


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

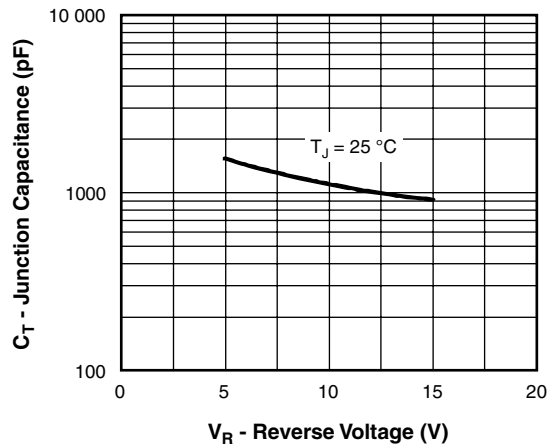
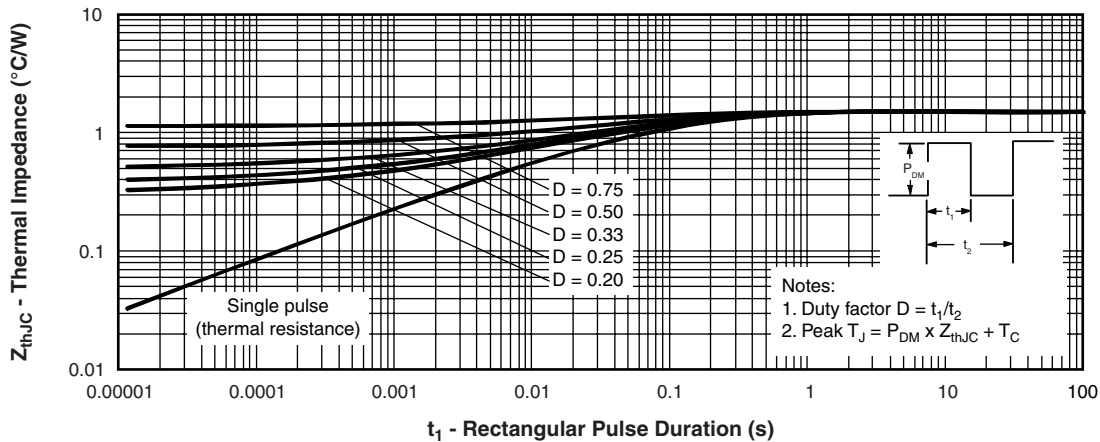


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

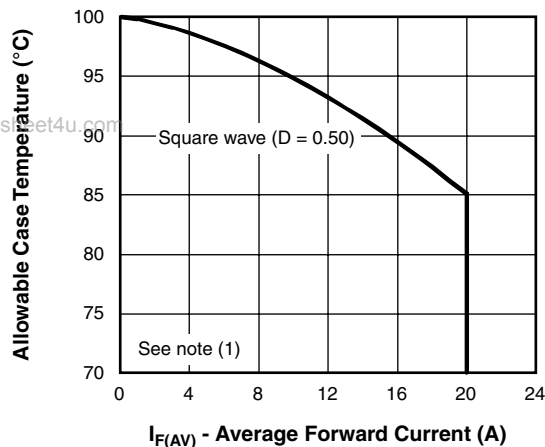


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

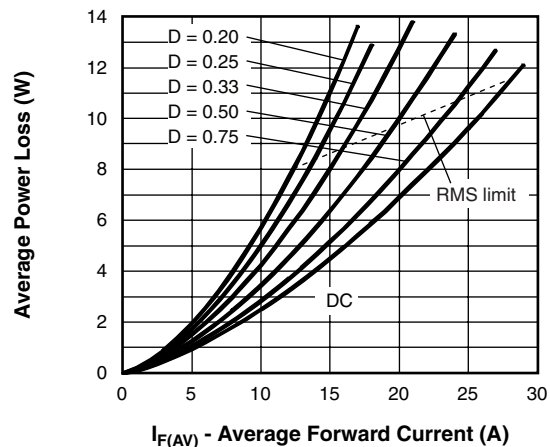


Fig. 6 - Forward Power Loss Characteristics

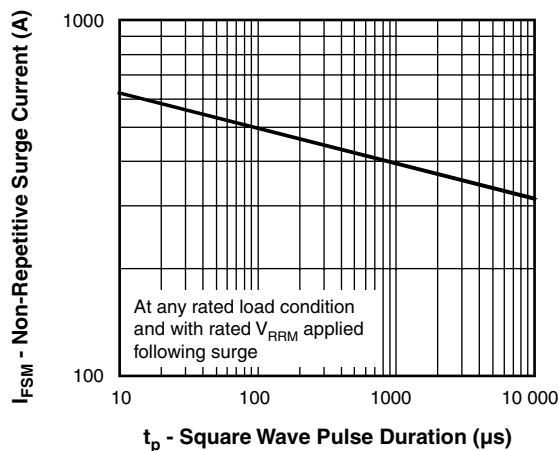


Fig. 7 - Maximum Non-Repetitive Surge Current

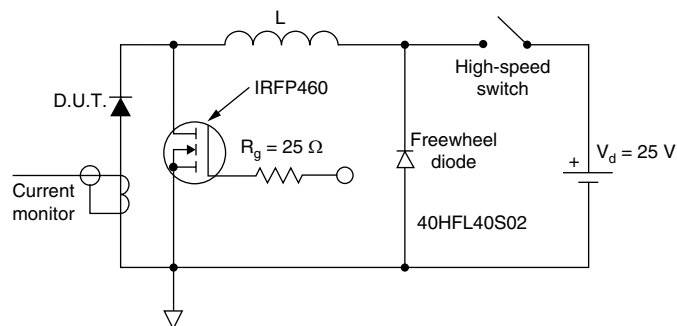


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



ORDERING INFORMATION TABLE

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Device code

STPS	20	L	15	D	PbF
1	2	3	4	5	6

- | | | |
|---|---|--|
| 1 | - | Schottky STPS series |
| 2 | - | Current rating (20 = 20 A) |
| 3 | - | L = Low voltage drop |
| 4 | - | Voltage rating (15 = 15 V) |
| 5 | - | D = Essential part number |
| 6 | - | • None = Standard production
• PbF = Lead (Pb)-free |

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95221
Part marking information	http://www.vishay.com/doc?95224
SPICE model	http://www.vishay.com/doc?95305



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