AUK Semiconductor

STK55X40LM

Advanced Power MOSFET

SWITCHING REGULATOR APPLICATIONS

Features

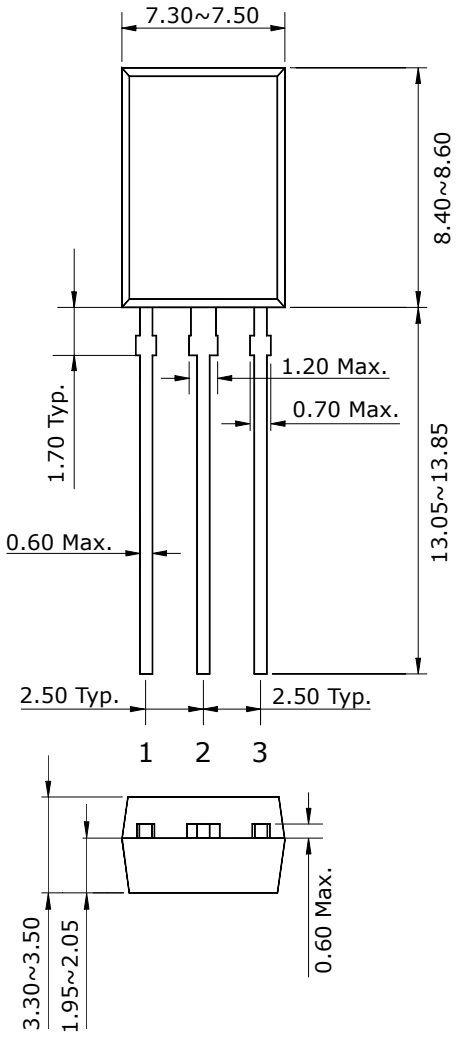
- High Voltage: BV_{DSS}=400V(Min.)
- Low C_{rss} : C_{rss}=4.9pF(Typ.)
- Low gate charge : Qg=4.6nC(Typ.)
- Low $R_{DS(on)}$:$R_{DS(on)}$=4.1Ω(Max.)

Ordering Information

Type NO.	Marking	Package Code
STK55x40LM	STK5x40	MPT

Outline Dimensions

unit : mm



PIN Connections
1. Gate
2. Drain
3. Source

Absolute maximum ratings (Ta=25°C)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	400	V
Gate-source voltage	V_{GSS}	±30	V
Drain current (DC)	I_D	0.55	A
Drain current (Pulsed) *	I_{DP}	2.4	A
Drain Power dissipation	P_D	1.3	W
Avalanche current (Single) ②	I_{AS}	0.6	A
Single pulsed avalanche energy ②	E_{AS}	4.1	mJ
Avalanche current (Repetitive) ①	I_{AR}	0.6	A
Repetitive avalanche energy ①	E_{AR}	0.1	mJ
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-55~150	

* Limited by maximum junction temperature

Characteristic		Symbol	Typ.	Max	Unit
Thermal resistance	Junction-ambient	$R_{th(J-a)}$	-	96.2	°C/W

Electrical Characteristics (Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DSS}	I_D=250μA, V_{GS}=0	400	-	-	V
Gate-threshold voltage	$V_{GS(th)}$	I_D=250μA, V_{DS}= V_{GS}	3.0	-	5.0	V
Drain-source leakage current	I_{DSS}	V_{DS}=400V, V_{GS}=0V	-	-	1	μA
Gate-source leakage	I_{GSS}	V_{DS}=0V, V_{GS}=±30V	-	-	±100	nA
Drain-Source on-resistance ④	$R_{DS(ON)}$	V_{GS}=10V, I_D=0.28A	-	4.2	5.0	Ω
Forward transfer admittance ④	g_{fs}	V_{DS}=10V, I_D=0.28A	-	1.4	-	S
Input capacitance	Ciss	V_{GS}=0V, V_{DS}=25V, f=1MHz	-	127	190	pF
Output capacitance	Coss		-	25	37.5	
Reverse transfer capacitance	Crss		-	4.9	7.4	
Turn-on delay time	$t_{d(on)}$	V_{DD}=200V, V_{GS}=10V I_D=0.55A, R_G=25Ω ③④	-	8.5	-	ns
Rise time	t_r		-	3.9	-	
Turn-off delay time	$t_{d(off)}$		-	9	-	
Fall time	t_f		-	3.9	-	
Total gate charge	Q_g	V_{DD}=200V, V_{GS}=10V I_D=0.55A ③④	-	4.6	6.9	nC
Gate-source charge	Q_{gs}		-	1.1	-	
Gate-drain charge	Q_{gd}		-	1.7	-	

Source-Drain Diode Ratings and Characteristics (Ta=25°C)

Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Continuous source current	I_S	Integral reverse diode in the MOSFET	-	-	0.6	A
Source current (Pulsed) ①	I_{SM}		-	-	2.4	
Forward voltage ④	V_{SD}	V_{GS}=0V, I_S=0.28A	-	-	1.4	V
Reverse recovery time	t_{rr}	I_s=0.55A, V_{GS}=0V di_s/dt=100A/us	-	180	-	ns
Reverse recovery charge	Q_{rr}		-	0.64	-	uC

Note ;

① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature

② L=20mH, I_{AS}=0.55A, V_{DD}=50V, R_G=25Ω

③ Pulse Test : Pulse Width < 300us, Duty cycle≤ 2%

④ Essentially independent of operating temperature

Electrical Characteristic Curves

Fig. 1 I_D - V_{DS}

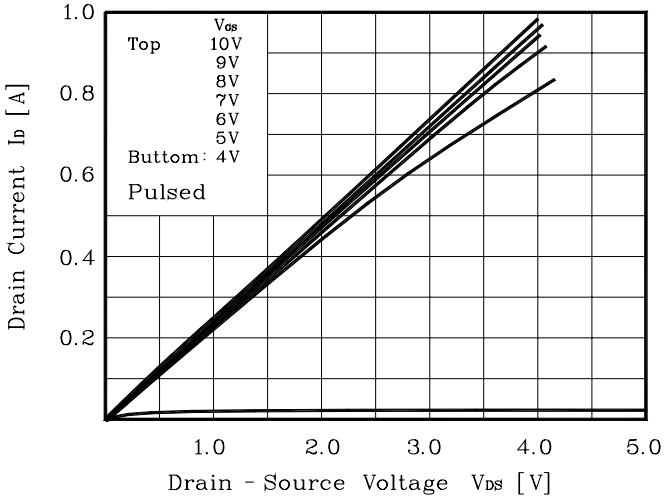


Fig. 2 I_D - V_{GS}

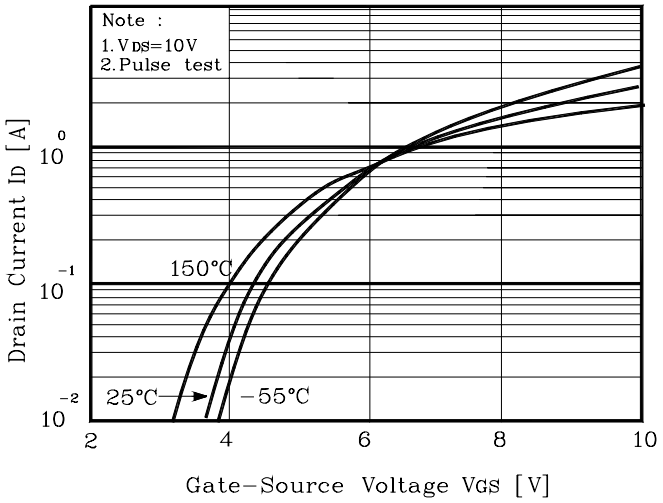


Fig. 3 $R_{DS(on)}$ - I_D

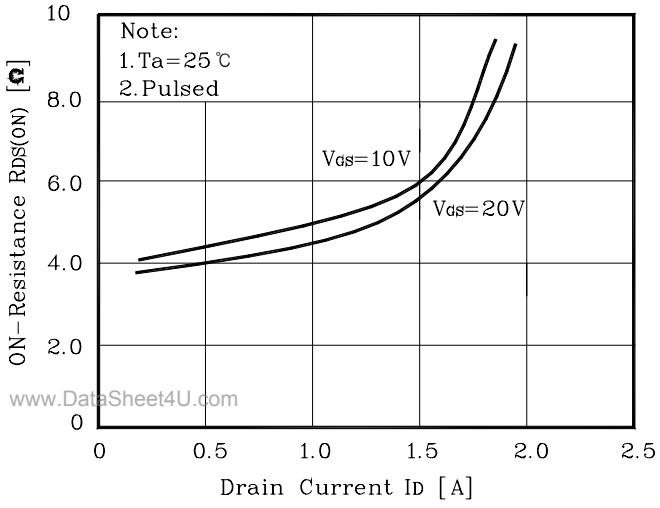


Fig. 4 I_S - V_{SD}

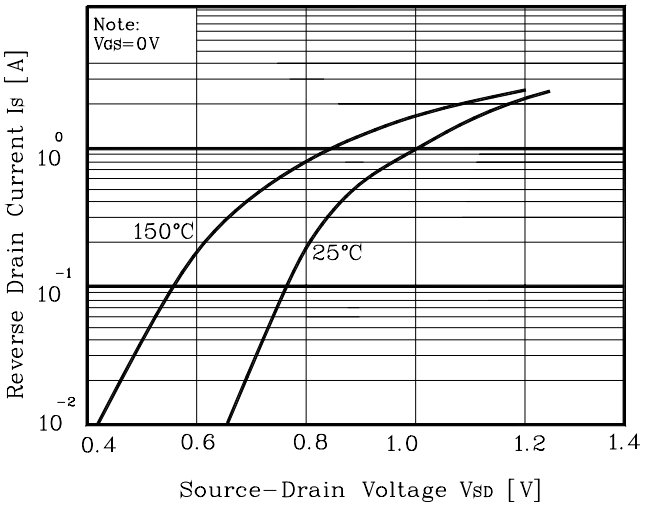


Fig. 5 Capacitance - V_{DS}

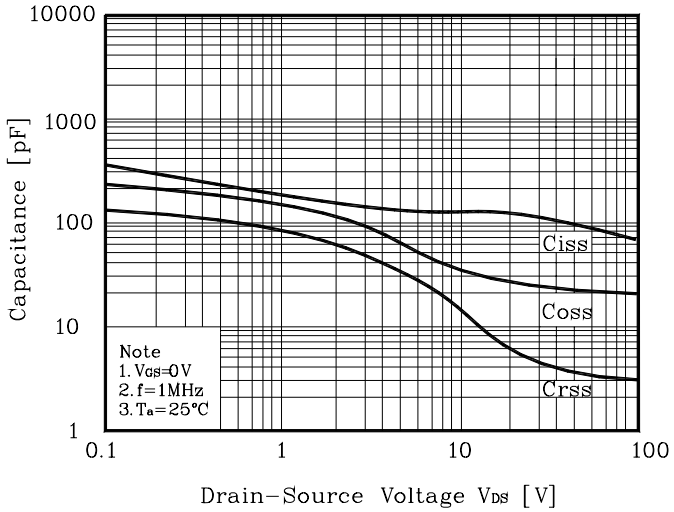


Fig. 6 V_{GS} - Q_G

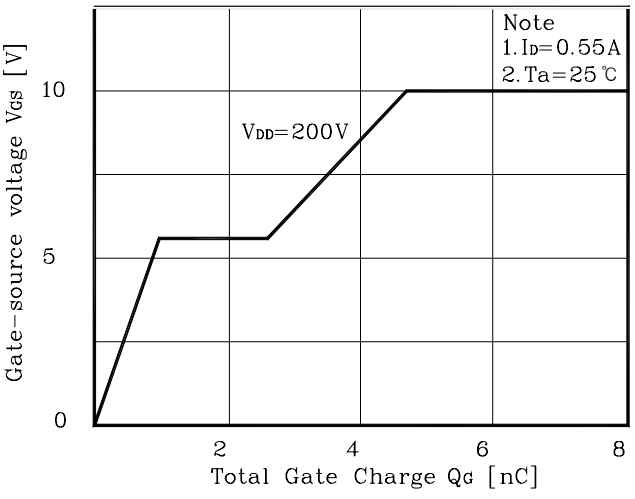


Fig. 7 V_{DSS} - T_J

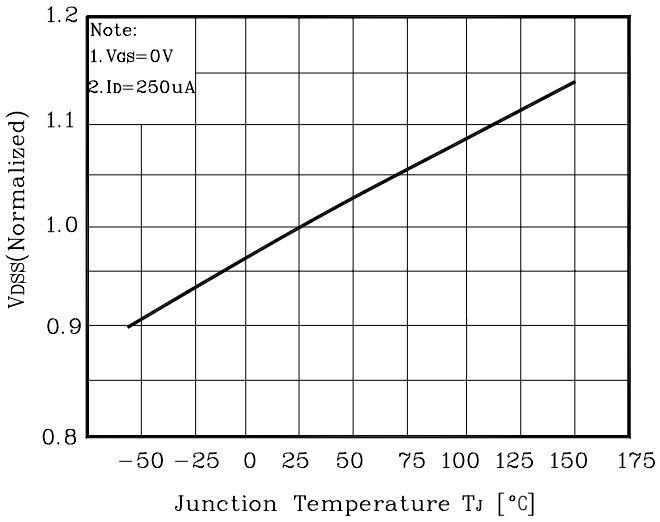


Fig. 8 $R_{DS(on)}$ - T_J

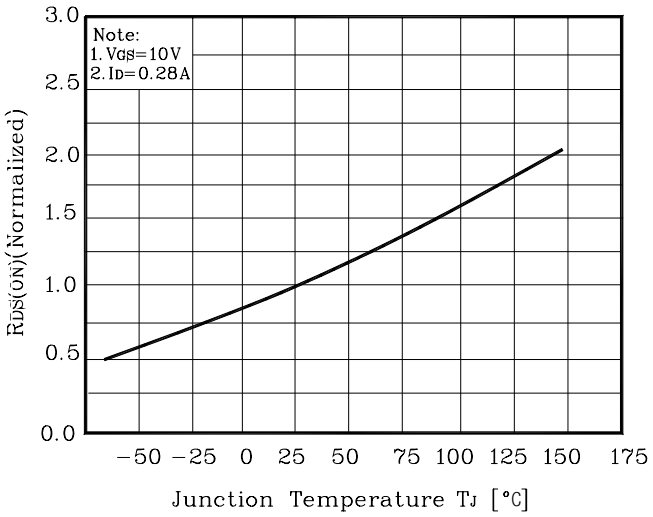


Fig. 9 I_D - T_C

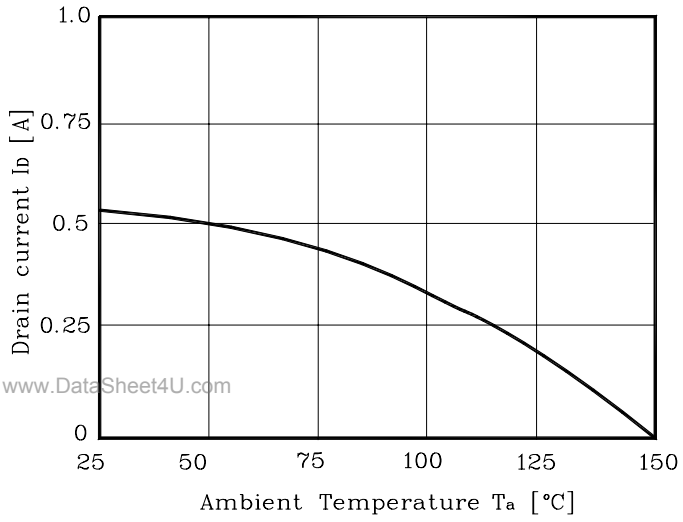


Fig. 10 Safe Operating Area

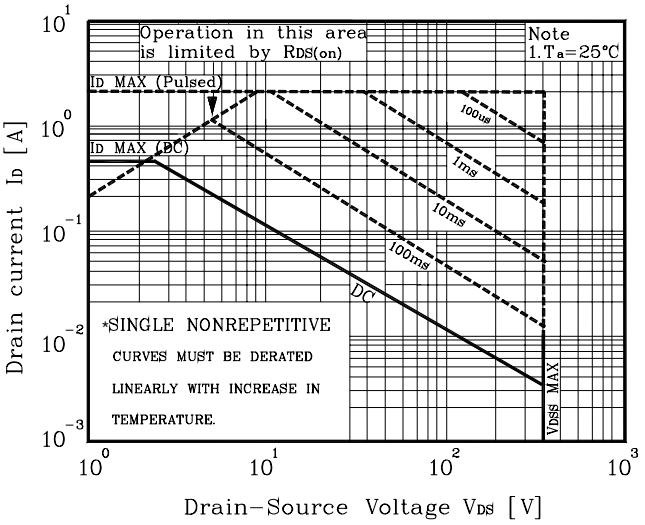


Fig. 11 Gate Charge Test Circuit & Waveform

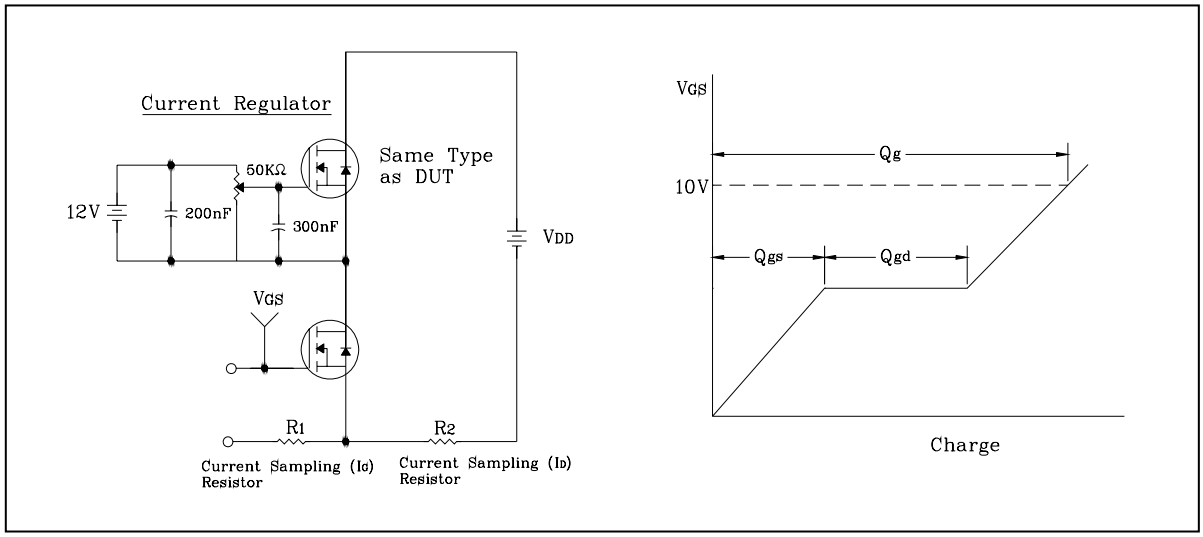


Fig. 12 Resistive Switching Test Circuit & Waveform

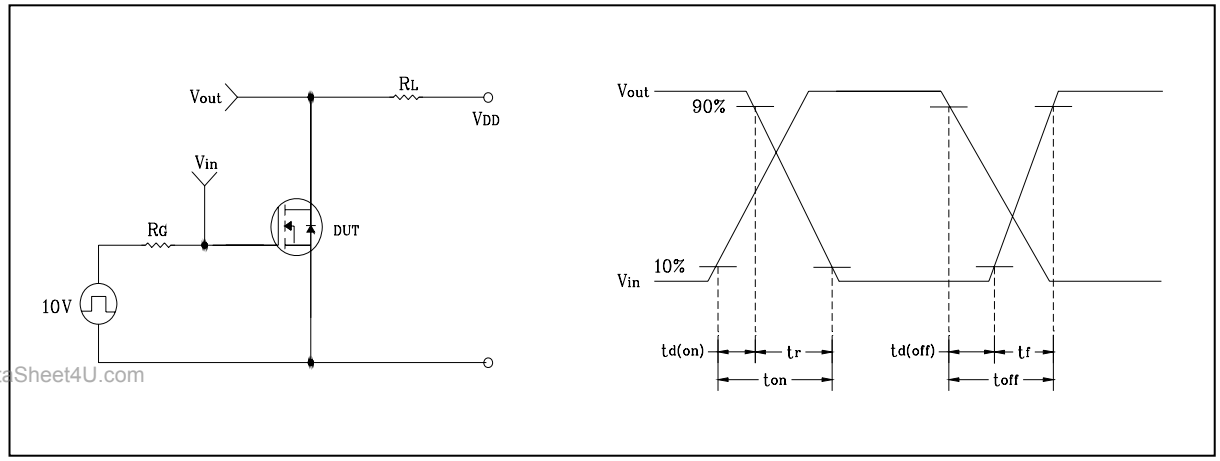


Fig. 13 E_{AS} Test Circuit & Waveform

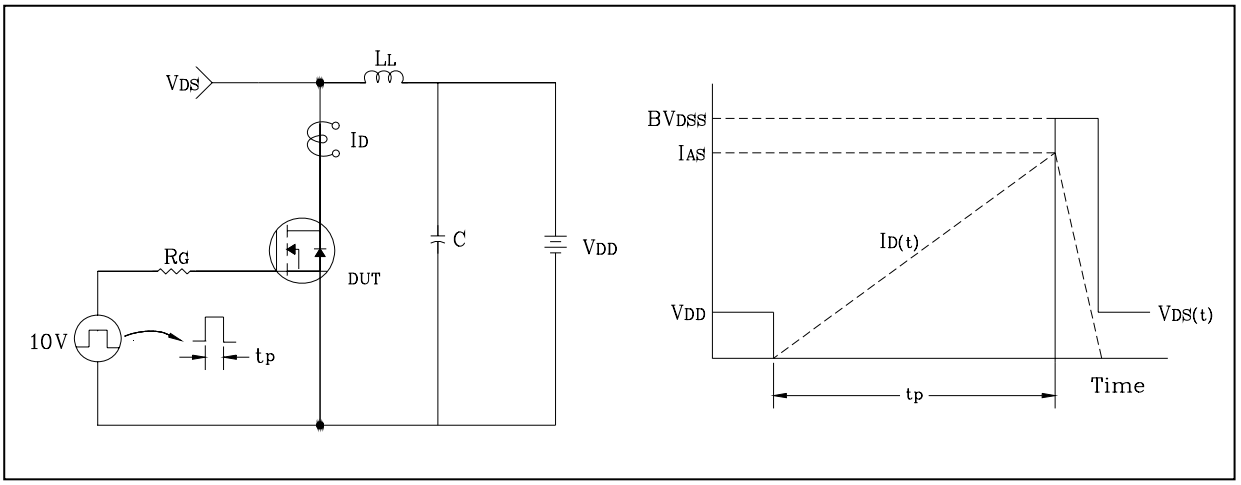
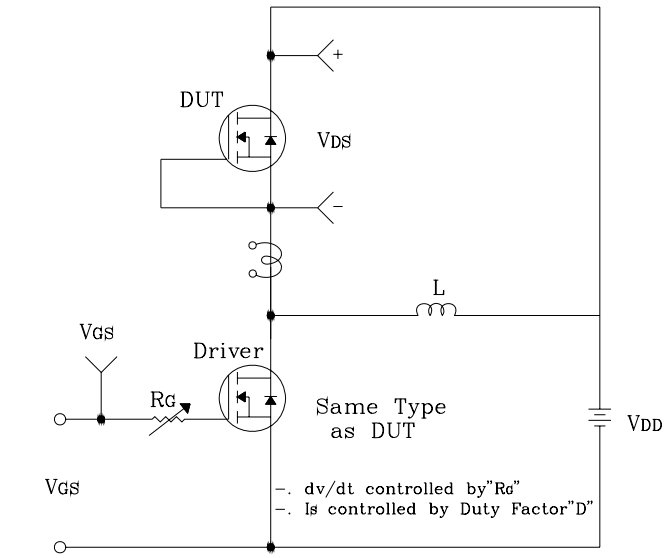
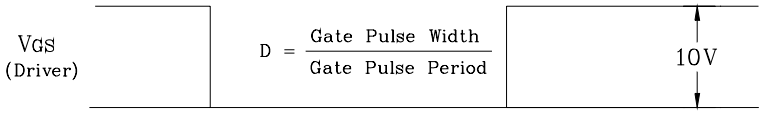
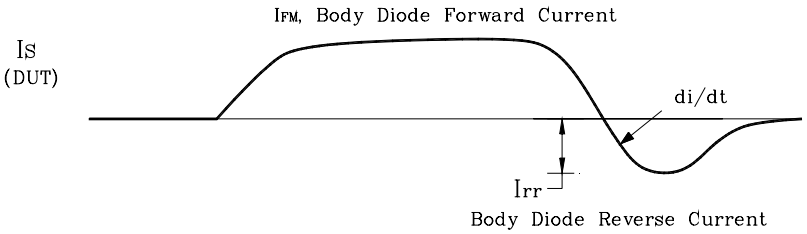
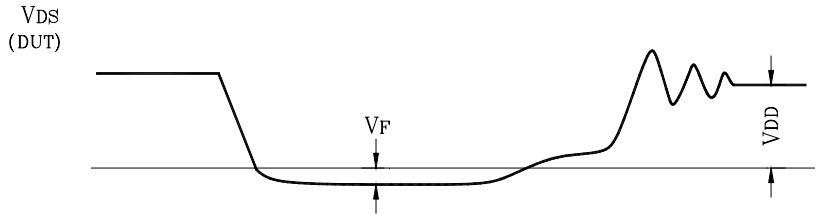


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform









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