



STGD3HF60HD

3 A, 600 V very fast IGBT

Preliminary data

Features

- Minimal tail current
- Low conduction and switching losses
- Ultra fast soft recovery antiparallel diode

Applications

- Motor drive

Description

The STGD3HF60HD is based on a new advanced planar technology concept to yield an IGBT with more stable switching performance (E_{off}) versus temperature, as well as lower conduction losses. The device is tailored to be a cost effective solution for motor drives.

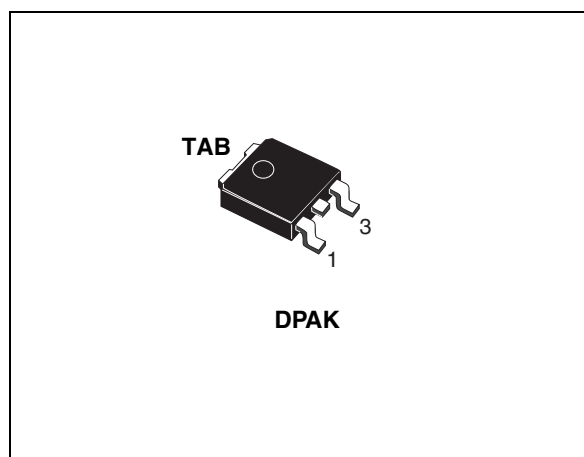


Figure 1. Internal schematic diagram

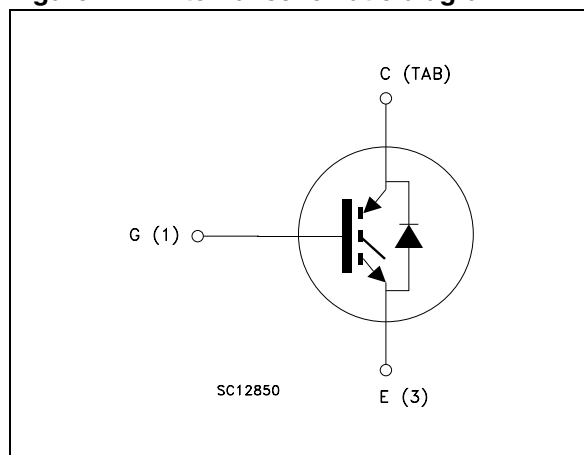


Table 1. Device summary

Order codes	Marking	Package	Packaging
STGD3HF60HDT4	GD3HF60HD	DPAK	Tape and reel

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage ($V_{GE} = 0$)	600	V
$I_C^{(1)}$	Continuous collector current at $T_C = 25\text{ °C}$	13	A
$I_C^{(1)}$	Continuous collector current at $T_C = 100\text{ °C}$	6	A
$I_{CL}^{(2)}$	Turn-off latching current	TBD	A
$I_{CP}^{(3)}$	Pulsed collector current	TBD	A
V_{GE}	Gate-emitter voltage	± 20	V
I_F	Diode RMS forward current at $T_C = 25\text{ °C}$	10	A
I_{FSM}	Surge non repetitive forward current $t_p=10\text{ms}$ sinusoidal	25	A
P_{TOT}	Total dissipation at $T_C = 25\text{ °C}$	57	W
T_j	Operating junction temperature	- 55 to 150	°C

1. Calculated according to the iterative formula:

$$I_C(T_C) = \frac{T_{j(max)} - T_C}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_C(T_C))}$$

2. $V_{clamp} = 80\%, (V_{CES})$, $T_j = 150\text{ °C}$, $R_G = 10\text{ }\Omega$, $V_{GE} = 15\text{ V}$.

3. Pulse width limited by maximum junction temperature and turn-off within RBSOA.

Table 3. Thermal data

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case IGBT	2.2	°C/W
	Thermal resistance junction-case diode	TBD	°C/W
$R_{thj-amb}$	Thermal resistance junction-ambient	100	°C/W

2 Electrical characteristics

($T_j=25\text{ }^{\circ}\text{C}$ unless otherwise specified).

Table 4. Static electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage ($V_{GE} = 0$)	$I_C = 1\text{ mA}$	600			V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}$, $I_C = 0.5\text{ A}$, $T_j = 125^{\circ}\text{C}$ $V_{GE} = 15\text{ V}$, $I_C = 1.5\text{ A}$ $V_{GE} = 15\text{ V}$, $I_C = 1.5\text{ A}$, $T_j = 125^{\circ}\text{C}$		1.3 2.3 1.8		V
$V_{GE(th)}$	Gate threshold voltage	$V_{CE} = V_{GE}$, $I_C = 250\text{ }\mu\text{A}$	3.75		5.75	V
I_{CES}	Collector cut-off current ($V_{GE} = 0$)	$V_{CE} = 600\text{ V}$ $V_{CE} = 600\text{ V}$, $T_j = 125\text{ }^{\circ}\text{C}$			250 1	μA mA
I_{GES}	Gate-emitter leakage current ($V_{CE} = 0$)	$V_{GE} = \pm 20\text{ V}$			± 100	nA
g_{fs}	Forward transconductance	$V_{CE} = 15\text{ V}$, $I_C = 1.5\text{ A}$		TBD		S

Table 5. Dynamic electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{ies} C_{oes} C_{res}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{CE} = 25\text{ V}$, $f = 1\text{ MHz}$, $V_{GE} = 0$	-	TBD TBD TBD	-	pF pF pF
Q_g Q_{ge} Q_{gc}	Total gate charge Gate-emitter charge Gate-collector charge	$V_{CE} = 390\text{ V}$, $I_C = 1.5\text{ A}$, $V_{GE} = 15\text{ V}$ (see Figure 3)	-	TBD TBD TBD	-	nC nC nC

Electrical characteristics

STGD3HF60HD

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 390\text{ V}$, $I_C = 1.5\text{ A}$		TBD		ns
t_r	Current rise time	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$	-	TBD	-	ns
$(di/dt)_{on}$	Turn-on current slope	(see Figure 4)		TBD		A/ μ s
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 390\text{ V}$, $I_C = 1.5\text{ A}$		TBD		ns
t_r	Current rise time	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	TBD	-	ns
$(di/dt)_{on}$	Turn-on current slope	$T_j = 125\text{ }^\circ\text{C}$ (see Figure 4)		TBD		A/ μ s
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 390\text{ V}$, $I_C = 1.5\text{ A}$,		TBD		ns
$t_{d(off)}$	Turn-off delay time	$R_{GE} = 10\ \Omega$, $V_{GE} = 15\text{ V}$	-	TBD	-	ns
t_f	Current fall time	(see Figure 4)		TBD		ns
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 390\text{ V}$, $I_C = 1.5\text{ A}$,		TBD		ns
$t_{d(off)}$	Turn-off delay time	$R_{GE} = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	TBD	-	ns
t_f	Current fall time	$T_j = 125\text{ }^\circ\text{C}$ (see Figure 4)		TBD		ns

Table 7. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 390\text{ V}$, $I_C = 1.5\text{ A}$		12		μ J
$E_{off}^{(2)}$	Turn-off switching losses	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$	-	30	-	μ J
E_{ts}	Total switching losses	(see Figure 4)		42		μ J
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 390\text{ V}$, $I_C = 1.5\text{ A}$		20		μ J
$E_{off}^{(2)}$	Turn-off switching losses	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	40	-	μ J
E_{ts}	Total switching losses	$T_j = 125\text{ }^\circ\text{C}$ (see Figure 4)		60		μ J

1. E_{on} is the turn-on losses when a typical diode is used in the test circuit in (see Figure 5). If the IGBT is offered in a package with a co-pak diode, the co-pak diode is used as external diode. IGBTs and diode are at the same temperature (25°C and 125°C).
2. Turn-off losses include also the tail of the collector current.

Table 8. Collector-emitter diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_F	Forward on-voltage	$I_F = 1.5\text{ A}$ $I_F = 1.5\text{ A}$, $T_j = 125\text{ }^\circ\text{C}$	-	1.4 1.15	1.8	V
t_{rr}	Reverse recovery time	$I_F = 1.5\text{ A}$, $V_R = 40\text{ V}$,		TBD		ns
Q_{rr}	Reverse recovery charge	$di/dt = 100\text{ A}/\mu\text{s}$	-	TBD		nC
I_{rrm}	Reverse recovery current	(see Figure 5)		TBD		A
t_{rr}	Reverse recovery time	$I_F = 1.5\text{ A}$, $V_R = 40\text{ V}$,		TBD		ns
Q_{rr}	Reverse recovery charge	$T_j = 125\text{ }^\circ\text{C}$, $di/dt = 100\text{ A}/\mu\text{s}$	-	TBD		nC
I_{rrm}	Reverse recovery current	(see Figure 5)		TBD		A

3 Test circuits

Figure 2. Test circuit for inductive load switching

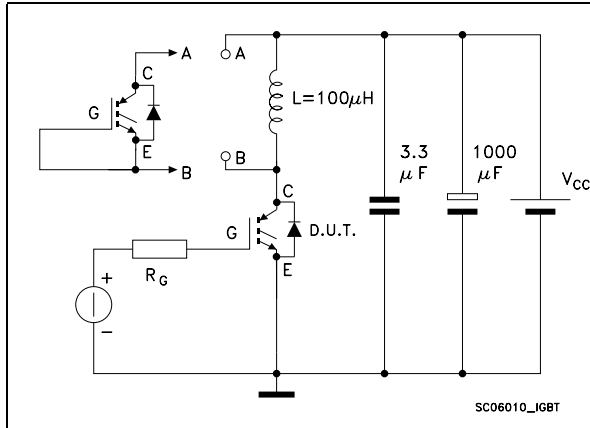


Figure 3. Gate charge test circuit

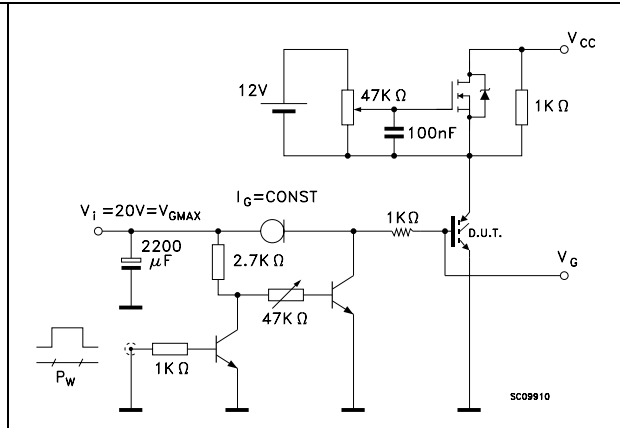


Figure 4. Switching waveform

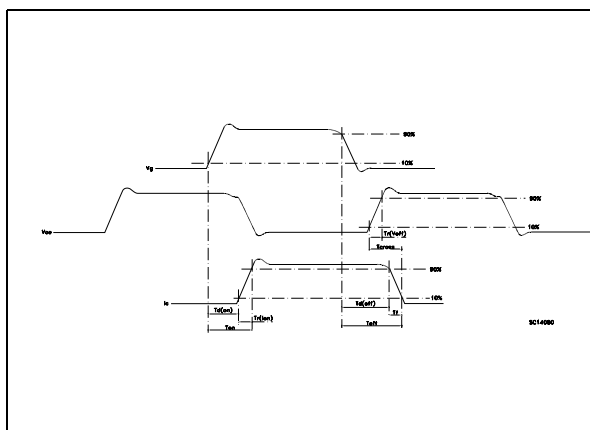
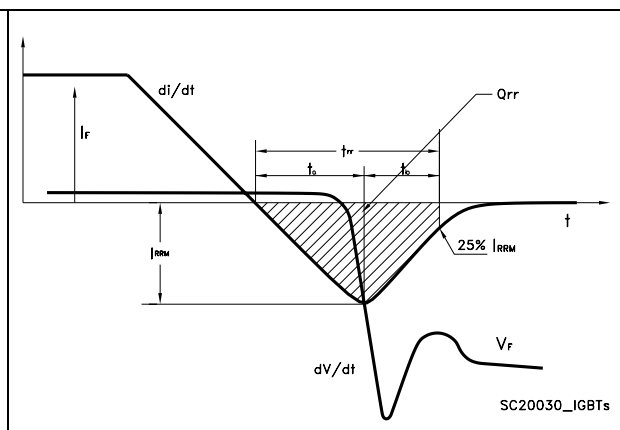


Figure 5. Diode recovery time waveform

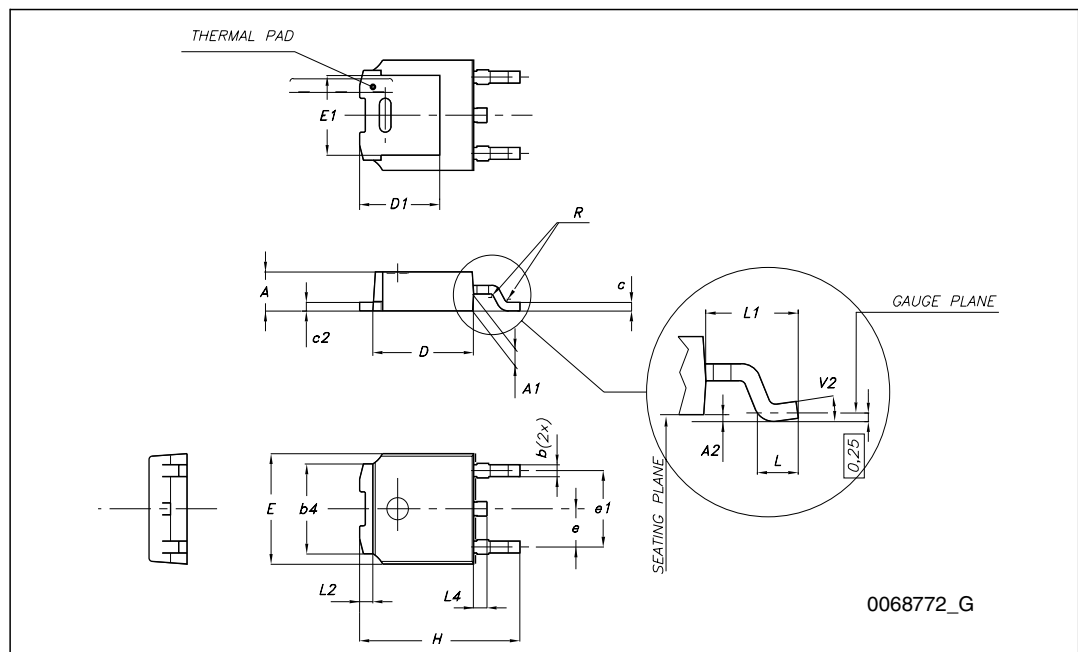


4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

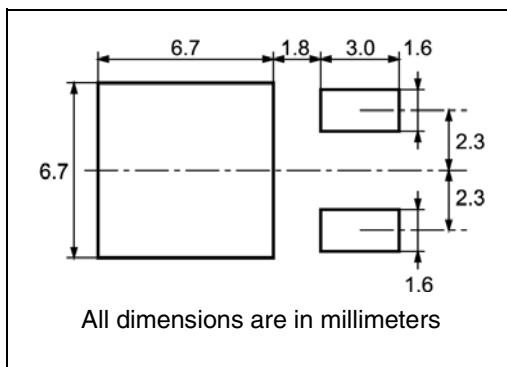
TO-252 (DPAK) mechanical data

DIM.	mm.		
	min.	typ	max.
A	2.20		2.40
A1	0.90		1.10
A2	0.03		0.23
b	0.64		0.90
b4	5.20		5.40
c	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
D1		5.10	
E	6.40		6.60
E1		4.70	
e		2.28	
e1	4.40		4.60
H	9.35		10.10
L	1		
L1		2.80	
L2		0.80	
L4	0.60		1
R		0.20	
V2	0°		8°

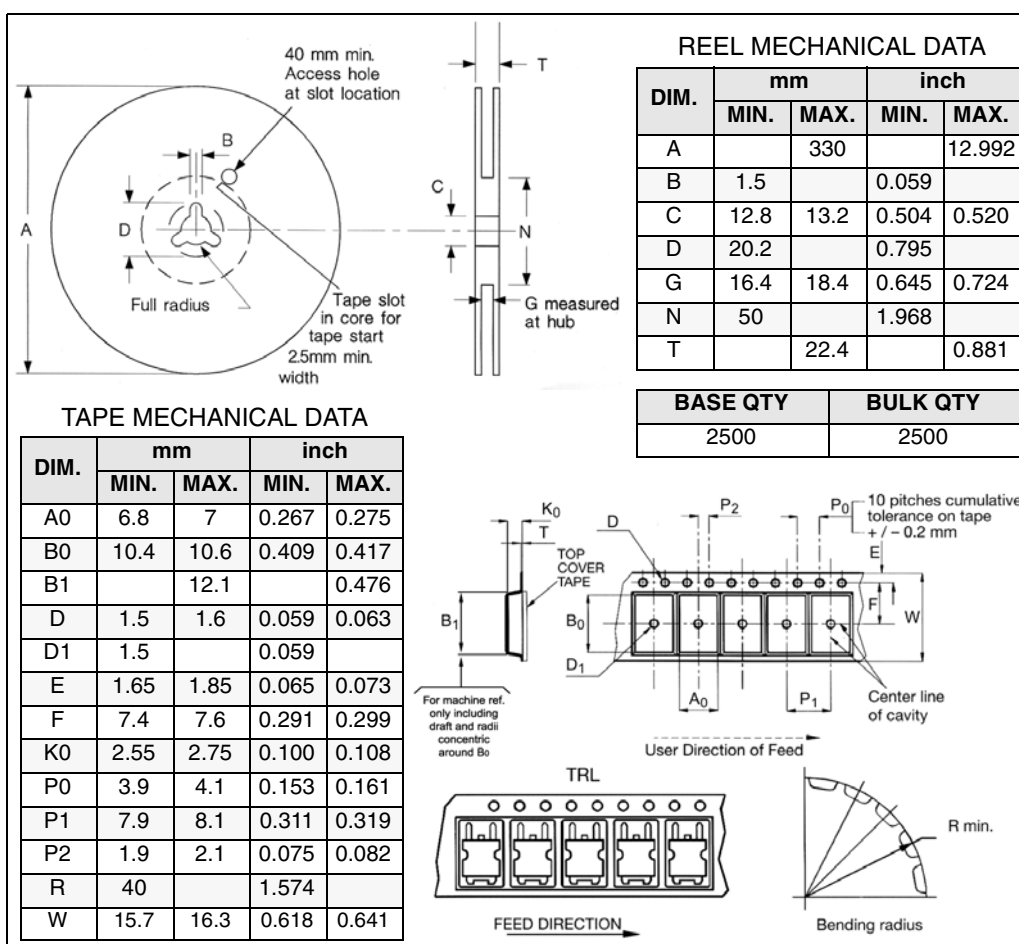


5 Packaging mechanical data

DPAK FOOTPRINT



TAPE AND REEL SHIPMENT



6 Revision history

Table 9. Document revision history

Date	Revision	Changes
29-Jun-2010	1	First release.

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2010 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com