

Epitaxial planar NPN silicon transistor

Descriptions

- High voltage application
- Telephone application

Features

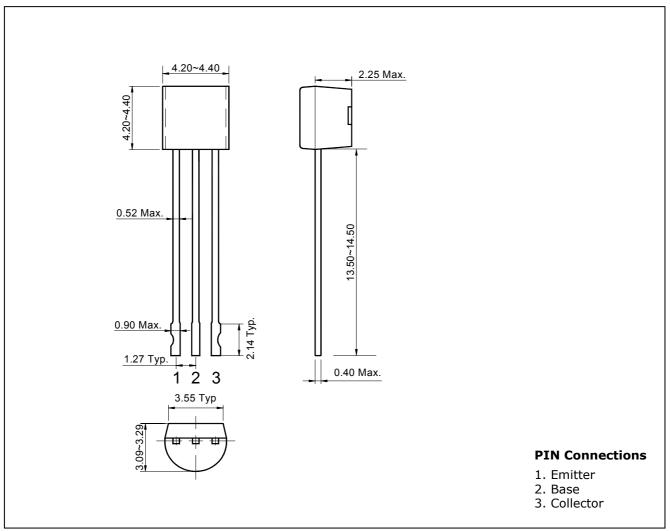
- High collector-emitter voltage : BV_{CEO}=300V
- Complementary pair with STA92N

Ordering Information

Type NO.	Marking	Package Code	
STC42N	STC42	TO-92N	

Outline Dimensions

unit : mm



Absolute Maximum Ratings				
Characteristic	Symbol	Rating	Unit	
Collector-base voltage	V _{CBO}	300	V	
Collector-emitter voltage	V _{CEO}	300	V	
Emitter-base voltage	V _{EBO}	6	V	
Collector current	I _C	500	mA	
Collector power dissipation	Pc	400	mW	
Junction temperature	Τ _J	150	°C	
Storage temperature range	T _{stg}	-55~150	°C	

Electrical Characteristics

(Ta=25°C)

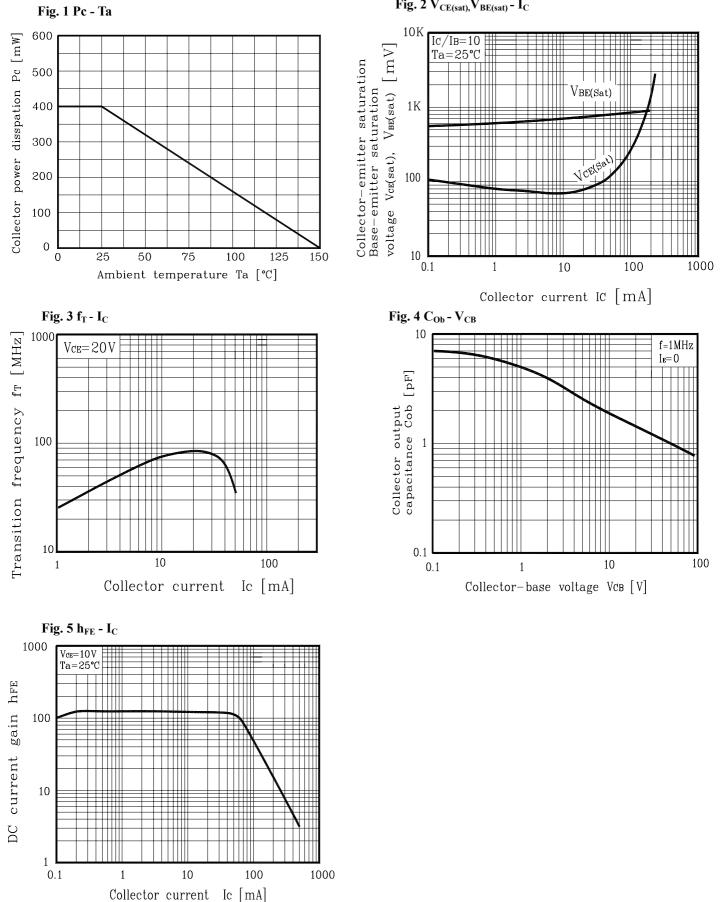
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-emitter breakdown voltage	BV_{CEO}	$I_C=1mA$, $I_B=0$	300	-	-	V
Collector cut-off current	I_{CBO}	V _{CB} =300V, I _E =0	-	-	0.1	μA
Emitter cut-off current	\mathbf{I}_{EBO}	V_{EB} =6V, I_{C} =0	-	-	0.1	μA
DC current gain	h _{FE} *	V_{CE} =10V, I_C =30mA	40	-	-	-
Collector-emitter saturation voltage	V _{CE(sat)} *	I_{C} =20mA, I_{B} =2mA	-	-	0.5	V
Base-emitter saturation voltage	$V_{BE(sat)}^{*}$	I_{C} =20mA, I_{B} =2mA	-	-	0.9	V
Base-emitter voltage	V_{BE}	V_{CE} =10V, I_C =30mA	-	0.7	0.9	V
Transition frequency	f_{T}	V_{CE} =20V, I_{C} =10mA	-	130	-	MHz
Collector output capacitance	C _{ob}	V_{CB} =20V, I_{E} =0, f=1MHz	-	2	-	рF

* : Pulse Tester : Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

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Fig. 2 V_{CE(sat)}, V_{BE(sat)} - I_C



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