

STC4250F

NPN Silicon Transistor

unit: mm

Applications

- Power amplifier application
- High current switching application

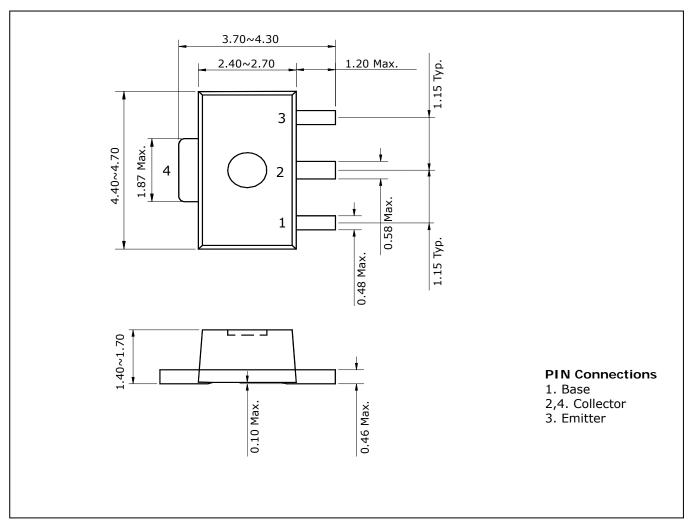
Features

- Low saturation voltage: $V_{\text{CE(sat)}}$ =0.15V Typ. @ I_{C} =1A, I_{B} =50mA
- Large collector current capacity: I_C=2A
- Small and compact SMD type package
- Complementary pair with STA3250F

Ordering Information

Type NO.	Marking	Package Code		
STC4250F	HW2	SOT-89		

Outline Dimensions



STC4250F

Absolute Maximum Ratings

[Ta=25°℃]

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	V_{CEO}	50	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	I_{C}	2	Α
Collector Power dissipation	P _C	0.5	W
	P _C *	1	W
Junction temperature	T ₁	150	°C
Storage temperature range	T _{stg}	-55~150	°C

[★] Device mounted on ceramic substrate (250mm² x 0.8t)

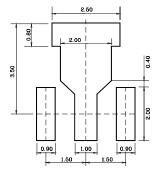
Electrical Characteristics

[Ta=25°℃]

Chara	cteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-emitter breakdown voltage		BV _{CEO}	$I_C=10$ mA, $I_B=0$	50	-	-	V
Collector cut-off current		I_{CBO}	V _{CB} =50V, I _E =0	-	-	0.1	μА
Emitter cut-off current		I_{EBO}	$V_{EB}=5V$, $I_C=0$	-	-	0.1	μА
DC current gain		h _{FE}	V _{CE} =2V, I _C =0.5A*	120	-	240	
		h _{FE}	V _{CE} =2V, I _C =1.5A*	40	-	-	
Collector-emitter	saturation voltage	$V_{CE(sat)}$	I _C =1A, I _B =0.05A*	-	-	0.35	V
Base-emitter saturation voltage		$V_{BE(sat)}$	I _C =1A, I _B =0.05A*	-	-	1.2	V
Transition frequency		f _T	$V_{CE}=2V$, $I_{C}=50$ mA	-	240	-	MHz
Collector output capacitance		C _{ob}	V _{CB} =10V, I _E =0, f=1MHz	-	15	-	pF
Switching Time	Turn-on Time	t _{on}	INPUT INDUT INPUT SOUTH SOUTH INPUT INPUT SOUTH SOUTH INPUT SOUTH SOUTH INPUT SOUTH SOUTH INPUT SOUTH	-	100	-	nS
	Storage Time	t _{stg}		-	300	-	
	Fall Time	t _f		-	50	-	

^{*:} Pulse test: $t_P \le 300 \mu s$, Duty cycle $\le 2\%$

* Recommend PCB solder land [Unit: mm]



Electrical Characteristic Curves

Fig. 1 P_C - T_a

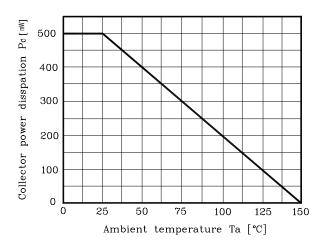


Fig. 2 $I_{\rm C}~$ - V_{BE}

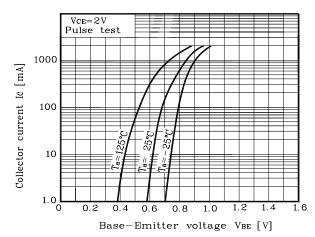


Fig. 3 I_C - V_{CE}

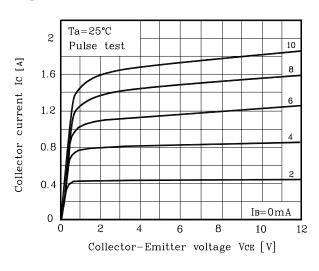


Fig. 4 h_{FE} - I_C

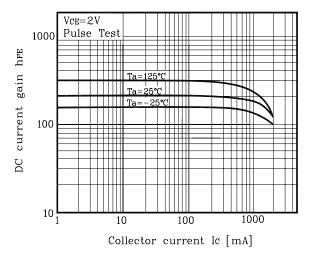


Fig. 5 $V_{CE(sat)}$ - I_C

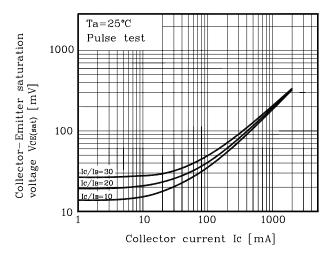
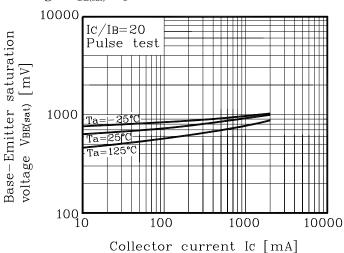


Fig. 6 $V_{BE(sat)}$ - I_{C}



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Electrical Characteristic Curves

Fig. 7 C_{Ob} - V_{CB}

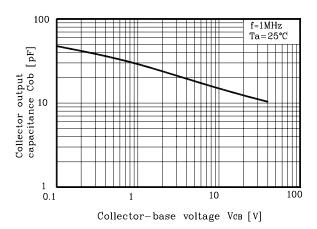
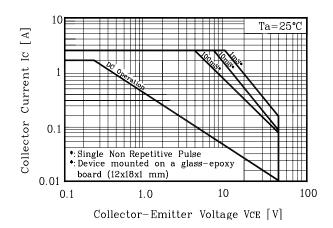


Fig. 8 Safe Operating Area



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