

### INCHANGE SEMICONDUCTOR

# Isc N-Channel MOSFET Transistor

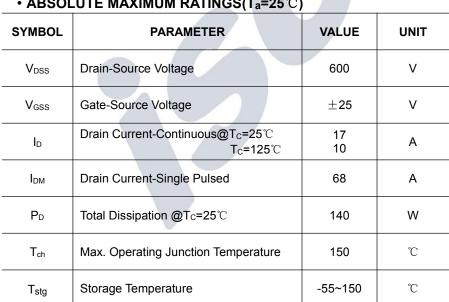
# STB21NM60ND

### • FEATURES

- With To-263(D2PAK) package
- · Low input capacitance and gate charge
- · Low gate input resistance
- 100% avalanche tested
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

#### APPLICATIONS

· Switching applications

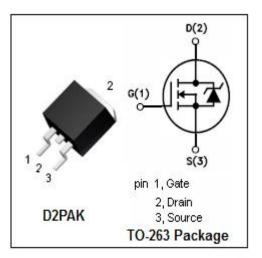


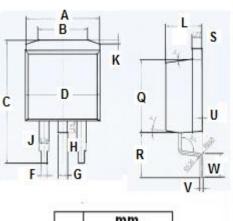
## • ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

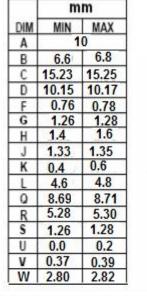
#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER		UNIT	
Rth(ch-c)	Channel-to-case thermal resistance	0.89	°C <b>/W</b>	
Rth(ch-a)	Channel-to-ambient thermal resistance	62.5	°C/W	

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### isc website: www.iscsemi.cn



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### **ELECTRICAL CHARACTERISTICS**

#### $T_{C}\text{=}25^{\circ}\!\!\!\!\mathrm{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 1mA	600			v
V <sub>GS</sub> (th)	Gate Threshold Voltage	V <sub>DS</sub> = ±25V; I <sub>D</sub> =0.25mA	3		5	v
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =8.5A		170	220	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±25V;V <sub>DS</sub> =0V			±0.1	μ Α
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 600V; V <sub>GS</sub> = 0V; T <sub>J</sub> =25°C T <sub>J</sub> =125°C			1 100	μ Α
V <sub>SDF</sub>	Diode forward voltage	I <sub>SD</sub> =17A, V <sub>GS</sub> = 0 V			1.6	V



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