

RoHS Compliant Product

A suffix of "-C" specifies halogen or lead -free

DESCRIPTION

SSMG501 is a simple general purpose current regulation component that can be easily used in various LED lighting applications. With the excellent load/line regulation and minimized chip current skew, SSMG501 keep LED's current very stable even when power or load fluctuate in a wide range and make intensity very uniform in large area of LED light source.

Except power supply function, the VDD pin of SSMG501 is output enable (OE) also, and can be used in digital PWM controlled circuit to achieve more precise current adjusting in gray level applications.

The minimized power supply voltage let SSMG501 be used as a current regulative diode (CRD) when VDD and VP pin are tight together. This application makes SSMG501 very easy to be used. Just like a diode, when this diode is inserted in LED series, the current in circuit is regulated.

In high supply voltage and low LED load voltage application, two or more single channel SSMG501 can be connected in series to share redundant high voltage. the extra redundant voltage can be share by each SSMG501 by a reasonable mechanism. This special capability let SSMG501 very suit for the usage of wide range power supply that many liner type LED drivers cannot work.

FEATURES

- 18mA~30mA constant current regulator
- Self power structure, no extra power supply is needed
- 0.4V~17V output voltage
- 2 μ S/2 μ S current rising/falling time
- Cascade-able for higher voltage application
- No external current setting resistor is needed
- 1.6V~12V supply voltage
- PWM dimming by V_{DD} pin
- -40°C~120°C junction operating temperature
- Less than 1%/V load(or line) regulation

APPLICATIONS

- General LED Lighting
- LED back lighting
- LED torch/flashing
- RGB lighting

MARKING CODE

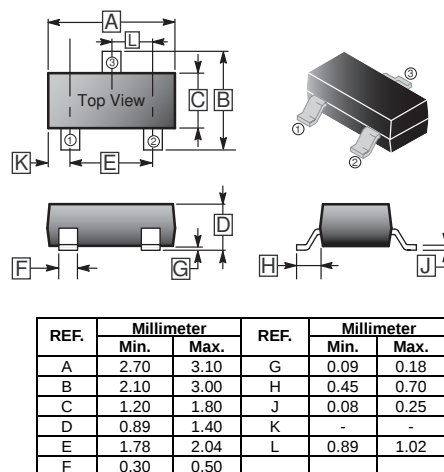
SSMG501-18: 1A18
SSMG501-20: 1A20
SSMG501-25: 1A25
SSMG501-30: 1A30

Note: 18, 20, 25, 30 = Output Current(mA)

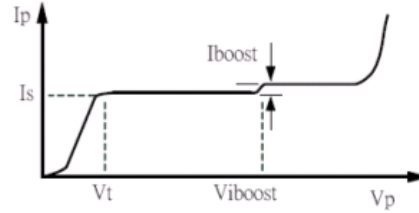
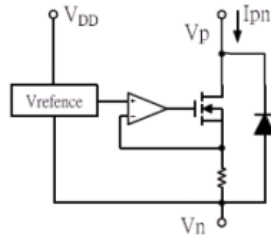
PACKAGE INFORMATION

Weight: 0.07800g (Approximately)

SOT-23



BLOCK DIAGRAMS PER CHANNEL AND IDEAL I_V CHARACTERISTICS



TERMINAL DESCRIPTION

Pin name	Function	Pin #
VDD	Power supply	②
VP	Current in	①
VN	Current out	③

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{DD}	0 ~ 17	V
Output Voltage	V_P	-0.2 ~ 20	V
Output Current	I_{PN}	$I_S^{**} + 10\%$	mA
Power Dissipation	P_D	250	mW
Thermal Resistance	$R_{\theta JA}$	300	$^\circ\text{C}/\text{W}$
Operating Temperature	T_{OPR}	-40 ~ +85	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS AND RECOMMENDED OPERATING CONDITIONS

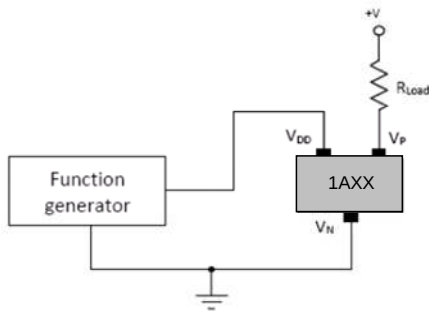
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Supply Voltage	V_{DD}	Room Temp.	1.6	-	12	V
Supply Current	I_{DD}	-	100	150	250	μA
Output Voltage	V_P	$V_{DD}=5\text{V}, I_P \div I_S^{**}$	0.4	-	17	V
		$V_{DD}=3\text{V}, I_P \div I_S^{**}$	0.45	-	17	V
		$V_{DD}=1.7\text{V}, I_P \div I_S^{**}$	1.2	-	17	V
Output Current	I_S	Spec	15	-	30	mA
Leakage	I_{Leakage}	$0.4\text{V} > V_{DD} > 0\text{V}, V_P=15\text{V}$	1	-	5	μA
Line Regulation	$\%/V_{DD}$	$12\text{V} > V_{DD} > 1.6\text{V}$	-	-	± 1	$\%/V$
Load Regulation	$\%/V_P$	$10\text{V} > V_P > 1.6\text{V}$	-	-	± 1	$\%/V$
Thermal Regulation	$\%/10^\circ\text{C}$	$V_{DD}=V_P=2\text{V}$	-	-	± 0.5	$\%/10^\circ\text{C}$
Threshold Voltage	V_{iboot}	$I_P=I_S*1.1$	11	12	13	V
Current Boost	I_{boost}	$V_P=V_{\text{iboot}}$	7	10	13	$\%* I_S$

** I_S is output saturation current.

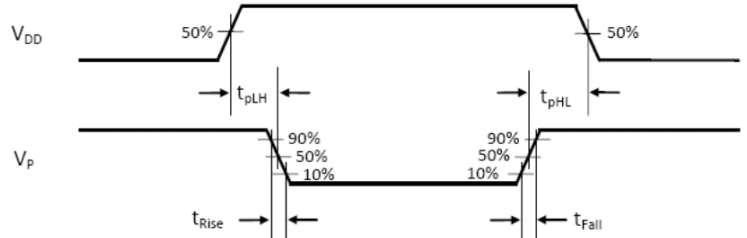
SWITCHING CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Propagation Delay Time VDD "L" $\rightarrow$ "H"	t_{pLH}	$V_P=1\text{V}, V_{DD}=0\text{V} \rightarrow 3\text{V}$	-	1	-	μs
Output Current Rising Time	t_{rise}	$V_P=1\text{V}, V_{DD}=0\text{V} \rightarrow 3\text{V}$	-	1.5	5	μs
Propagation Delay Time VDD "H" $\rightarrow$ "L"	t_{pHL}	$V_P=1\text{V}, V_{DD}=3\text{V} \rightarrow 0\text{V}$	-	1	-	μs
Output Current Falling Time	t_{fall}	$V_P=1\text{V}, V_{DD}=3\text{V} \rightarrow 0\text{V}$	-	1.5	5	μs

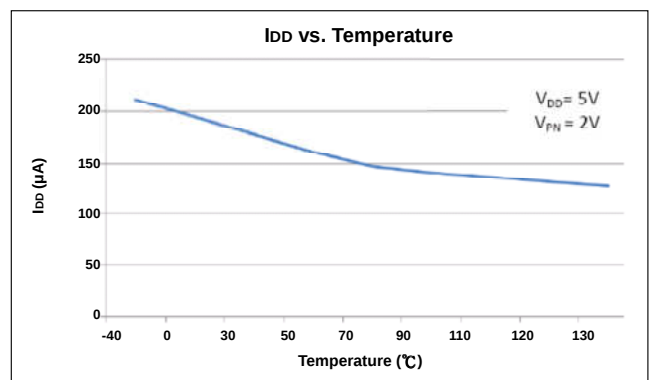
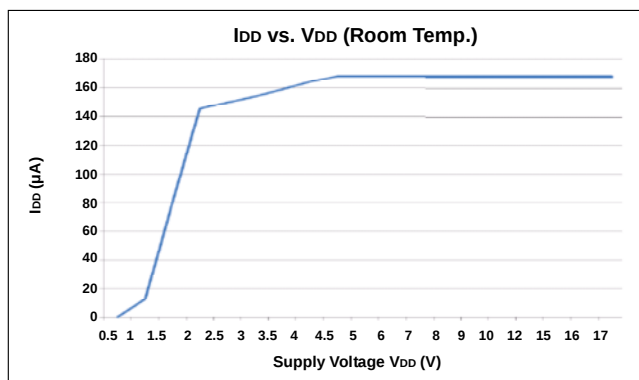
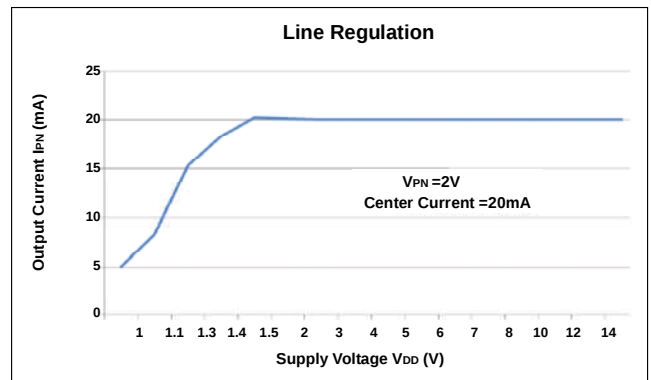
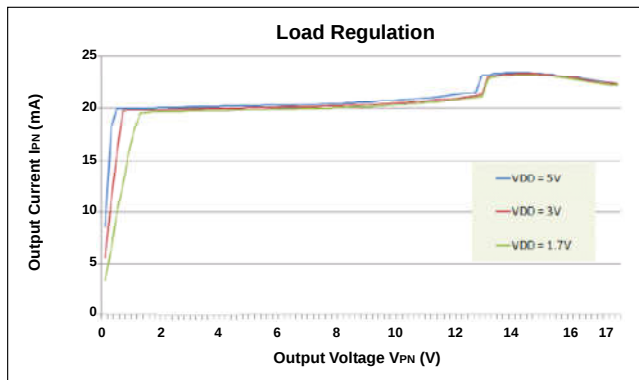
TEST CIRCUIT



TIMING WAVEFORM

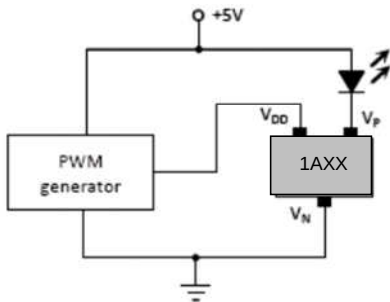


CHARACTERISTIC CURVE

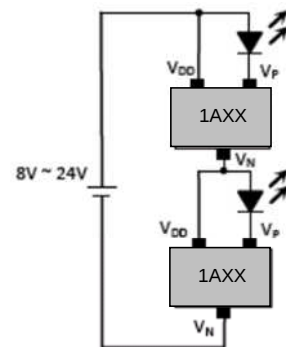


APPLICATION CIRCUITS

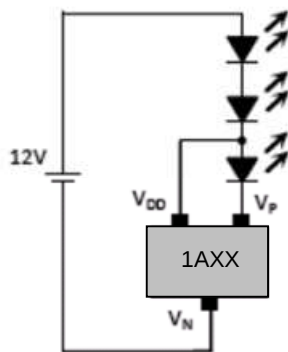
● 5V PWM Lighting Application



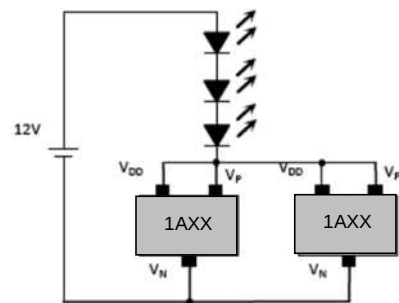
● High Voltage Drop Application



● 12V Lighting Application



● Parallel Application



● 36V Lighting Application

LED $V_f = 3.3 \text{ V} \sim 3.5 \text{ V}$
 $V_{DD} = 35.5 \text{ V} \sim 40 \text{ V}$

