

## N-Channel Enhancement Mode MOSFET

- Features

VDS	VGS	RDSon TYP	ID
20V	$\pm 8V$	240mR@4V5	0.4A
		280mR@2V5	
		410mR@1V8	
		450mR@1V5	

- General Description

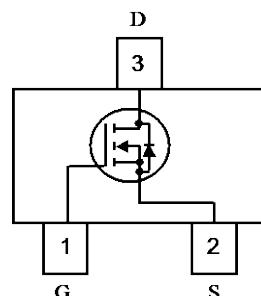
This device is a N-Channel enhancement mode MOSFET which is produced with high cell density and DMOS trench technology .This device particularly suits low voltage applications, especially for battery powered circuits, the tiny and thin outline saves PCB consumption.

- Applications

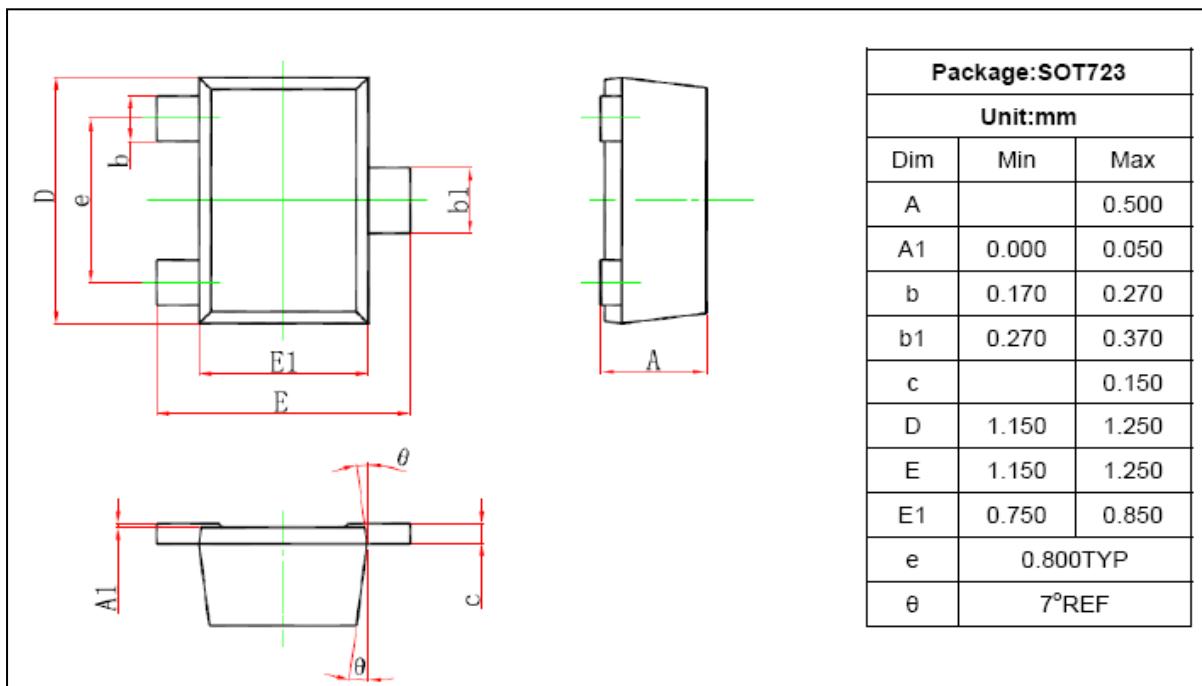
- Replace Digital Transistor
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers

- Pin Configuration

Top View



- Package Information





# SSC8020GS9

- **Absolute Maximum Ratings @ TA = 25°C unless otherwise specified**

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	±8	
Drain Current (Note 1)	Continuous	0.4	A
	Pulsed	1.2	
Power Dissipation Derating above T <sub>A</sub> = 25°C (Note 1)	P <sub>d</sub>	175	mW
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

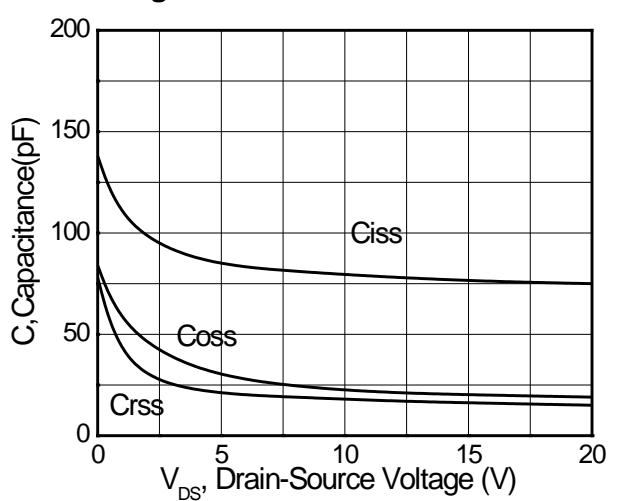
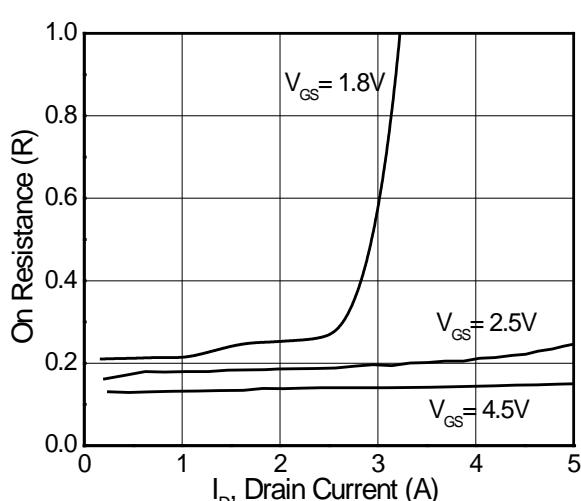
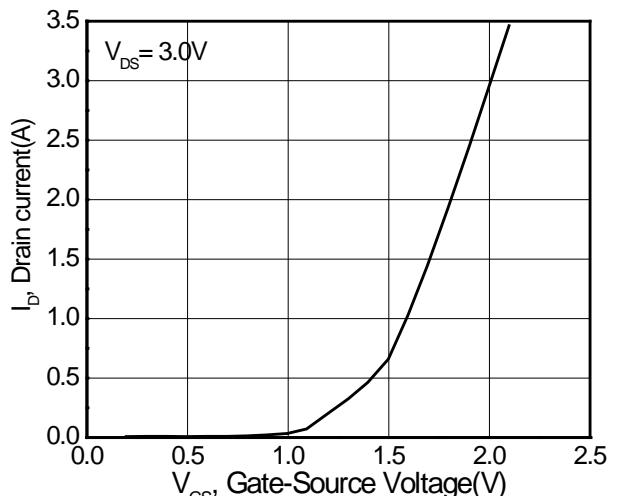
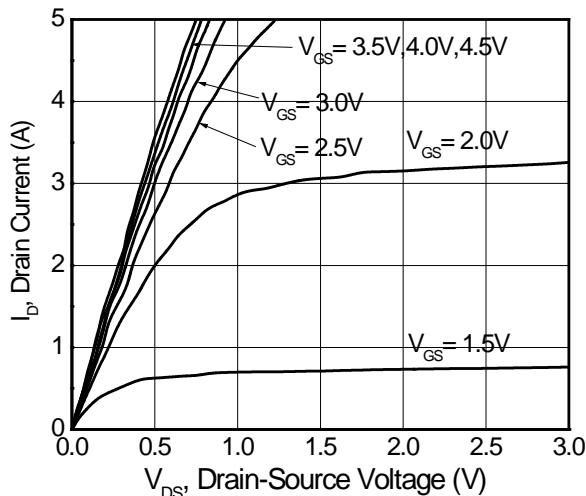
Note1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inches. The rating is for each chip in the package.

- **Electrical Characteristics @ TA = 25°C unless otherwise specified**

Parameter (Note 2)	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250µA	20	--	--	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V	--	--	1	uA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V	--	--	±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	0.35	--	1	V
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	I <sub>D</sub> = 400mA, V <sub>GS</sub> = 4.5V	--	240	450	mR
		I <sub>D</sub> = 300mA, V <sub>GS</sub> = 2.5V	--	280	765	
		I <sub>D</sub> = 200mA, V <sub>GS</sub> = 1.8V	--	410	850	
		I <sub>D</sub> = 100mA, V <sub>GS</sub> = 1.5V	--	450	950	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -6V, R <sub>L</sub> = 6R, I <sub>D</sub> = -1A, V <sub>GEN</sub> = -4.5V, R <sub>G</sub> = 6R	--	6	--	ns
Turn-Off Delay Time	t <sub>d(off)</sub>		--	28	--	
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V, f = 200KHz	--	130	--	pF
Output Capacitance	C <sub>oss</sub>		--	20	--	
Reverse Transfer Capacitance	C <sub>RSS</sub>		--	16	--	
BODY DIODE CHARACTERISTICS						
Diode Forward Voltage <sup>(1)</sup>	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 150mA	--	0.68	1.2	V

Note 2. Short duration test pulse used to minimize self-heating effect.

- **Typical Performance Characteristics**



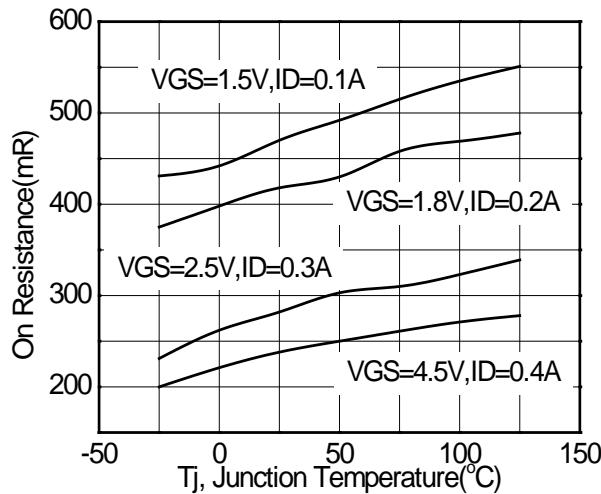


Figure 5. On resistance vs. Temperature

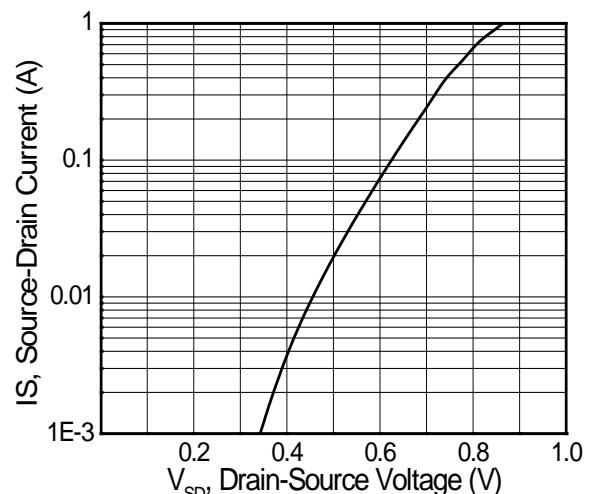


Figure 6. Diode Forward Characteristics

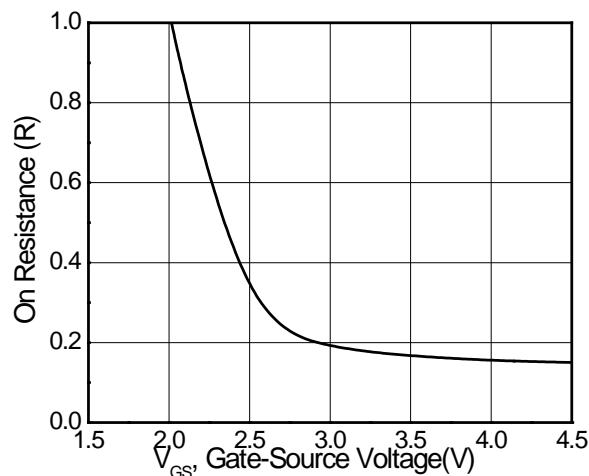


Figure 7. On Resistance vs. Gate-Source Voltage

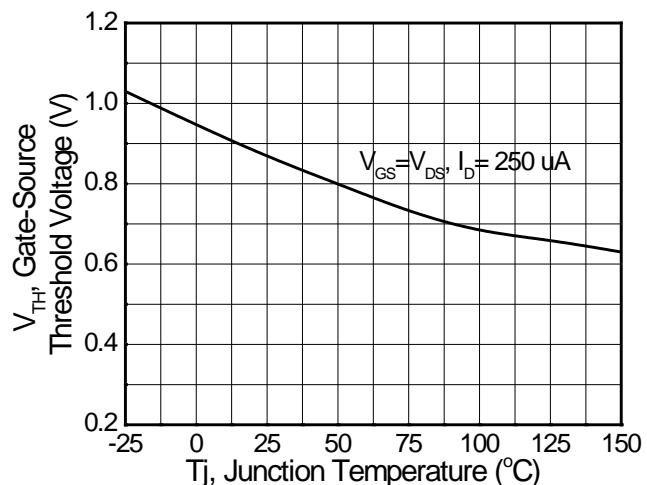


Figure 8. Gate Threshold vs. Temperature



# SSC8020GS9

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