



SUPER-SEMI



## SUPER-MOSFET

Super Junction Metal Oxide Semiconductor Field Effect Transistor

900V Super Junction Power MOSFET Gen-II  
SS\*90R240S2

Rev. 1.0  
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# SSB90R240S2/SSW90R240S2

## 900V N-Channel Super-Junction MOSFET Gen-II

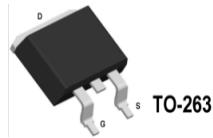
### Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

### Features

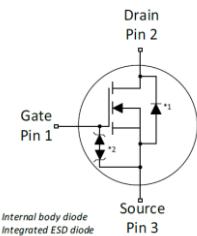
- Multi-Epi process SJ-FET
- 950V @ $T_J = 150^\circ\text{C}$
- Typ.  $R_{DS(on)} = 0.205\Omega$
- Ultra Low Gate Charge (typ.  $Q_g = 41\text{nC}$ )
- 100% avalanche tested
- Integrated Zener diode for high ESD robustness(>2kV HBM)

SSB90R240S2



Absolute Maximum Ratings

SSW90R240S2



Symbol	Parameter	SSB_W90R240S2	Unit
$V_{DSS}$	Drain-Source Voltage	900	V
$I_D$	Drain Current - Continuous ( $TC = 25^\circ\text{C}$ ) - Continuous ( $TC = 100^\circ\text{C}$ )	20* 12.5*	A
$I_{DM}$	Drain Current - Pulsed (Note 1)	60	A
$V_{GSS}$	Gate-Source voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)	540	mJ
$I_{AS}$	Avalanche current, repetitive or not-repetitive (pulse width limited by $T_j$ max)	6	A
$dv/dt$	Peak Diode Recovery $dv/dt$ (Note 3)	15	V/ns
$dVds/dt$	Drain Source voltage slope ( $V_{ds}=480\text{V}$ )	50	V/ns
$P_D$	Power Dissipation ( $TC = 25^\circ\text{C}$ )	170	W
$T_j, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	°C
$T_L$	Maximum Lead Temperature for Soldering Purpose, $1/16''$ from Case for 10 Seconds	260	°C

\* Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75

### Thermal Characteristics

Symbol	Parameter	SSB_W90R240S2	Unit
$R_{eJC}$	Thermal Resistance, Junction-to-Case	0.75	°C/W
$R_{eCS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	°C/W
$R_{eJA}$	Thermal Resistance, Junction-to-Ambient	62	°C/W



# Electrical Characteristics TC = 25°C unless otherwise noted

SSB90R240S2/SSW90R240S2 900V N-Channel Super-Junction MOSFET Gen-II

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA, T <sub>J</sub> = 25°C	900	-	-	V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA, T <sub>J</sub> = 150°C	950	-	-	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250µA, Referenced to 25°C	-	0.8	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>D</sub> S = 900V, V <sub>G</sub> S = 0V -T <sub>J</sub> = 125°C	-	-	100	µA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>G</sub> S = 20V, V <sub>D</sub> S = 0V	-	-	1	µA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>G</sub> S = -20V, V <sub>D</sub> S = 0V	-	-	-1	µA
<b>On Characteristics</b>						
V <sub>G</sub> (th)	Gate Threshold Voltage	V <sub>D</sub> S = V <sub>G</sub> S, I <sub>D</sub> = 250µA	3.0	4.0	5.0	V
R <sub>D</sub> S(on)	Static Drain-Source On-Resistance	V <sub>G</sub> S = 10V, I <sub>D</sub> = 10A	-	0.205	0.24	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>D</sub> S = 50V, V <sub>G</sub> S = 0V, f = 1.0MHz	-	1995	-	pF
C <sub>oss</sub>	Output Capacitance		-	74	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	4	-	pF
E <sub>oss</sub>	Stored Energy in Output Capacitance	V <sub>D</sub> S = 0 to 600V, V <sub>G</sub> S = 0V	-	8.4	-	µJ
C <sub>o(er)</sub>	Energy Related Output Capacitance		-	47	-	pF
C <sub>o(tr)</sub>	Time Related Output Capacitance		-	172	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>D</sub> S = 600V, I <sub>D</sub> = 10A, V <sub>G</sub> S = 10V (Note 4)	-	41	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	11	-	nC
Q <sub>gd</sub>	Gate-Drain Charge		-	15.1	-	nC
V <sub>plateau</sub>	Gate plateau voltage		-	5.8	-	V
R <sub>g</sub>	Gate resistance	f=1 MHz, open drain	-	4.0	-	Ω
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>D</sub> S = 400V, I <sub>D</sub> = 10A R <sub>G</sub> = 4.7Ω, V <sub>G</sub> S = 10V (Note 4)	-	27	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	18	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	69	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	15	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current	-	-	20	-	A
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current	-	-	60	-	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>G</sub> S = 0V, I <sub>S</sub> = 20A	-	0.9	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>G</sub> S = 0V, V <sub>D</sub> S = 600V, I <sub>S</sub> = 10A, dI/dt = 100A/µs	-	365	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	4.2	-	µC
I <sub>rrm</sub>	Peak Reverse Recovery Current		-	22	-	A

### NOTES:

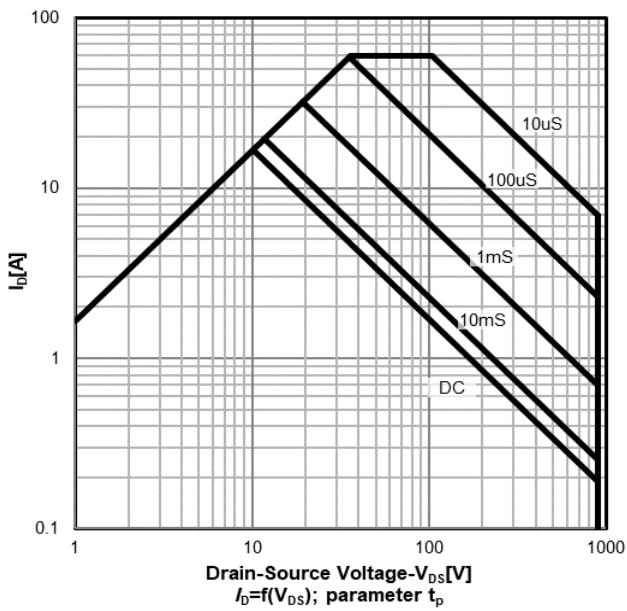
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. I<sub>D</sub>=I<sub>AS</sub>, V<sub>D</sub>D=50V, L=30mH, Starting T<sub>J</sub>=25 °C
3. I<sub>SD</sub>≤I<sub>D</sub>, di/dt ≤ 200A/µs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25 °C
4. Essentially Independent of Operating Temperature Typical Characteristics



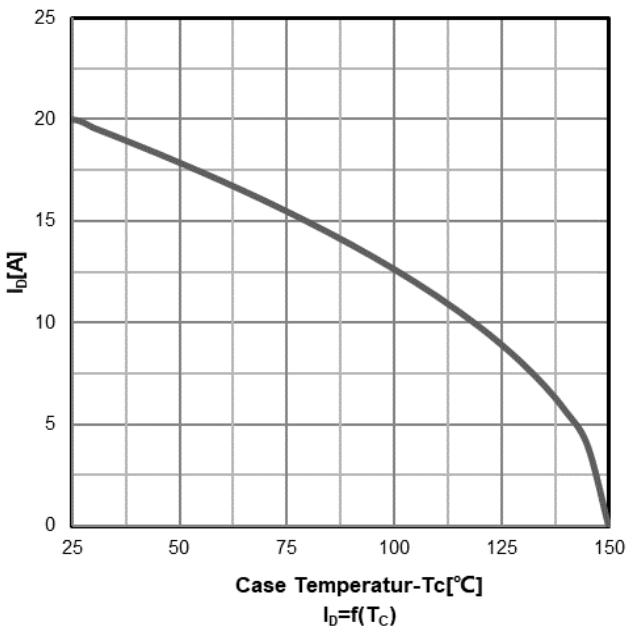
## Typical Performance Characteristics

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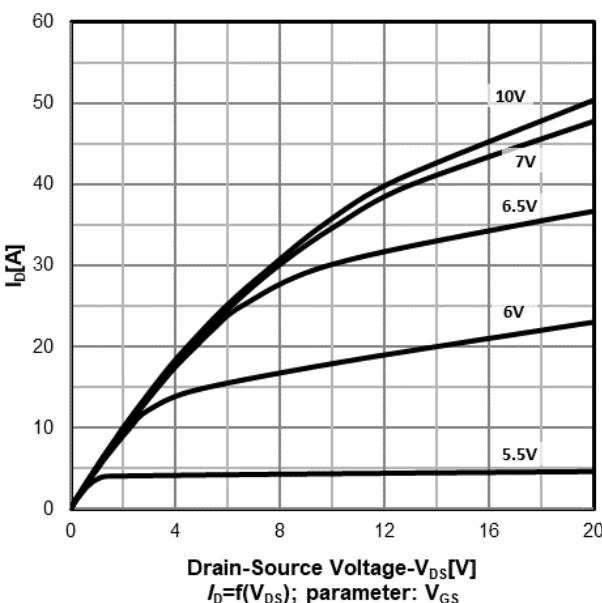
Safe operating area TC=25 °C  
Non FullPAK



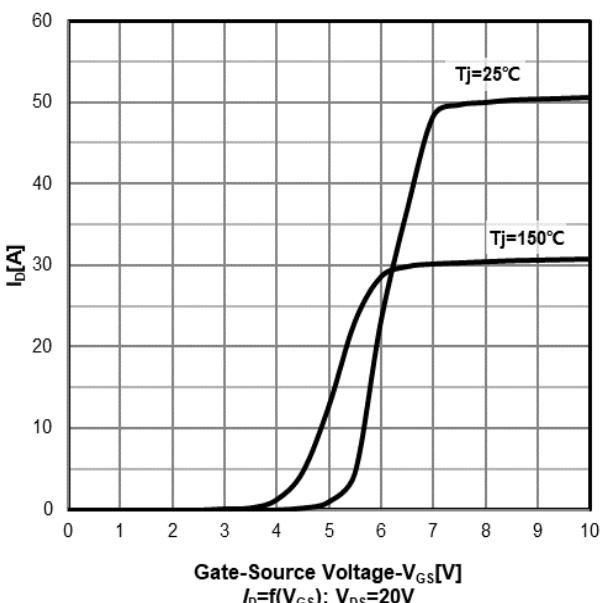
Drain current vs temperature



Typ. output characteristics  $T_j=25$  °C



Typ. transfer characteristics

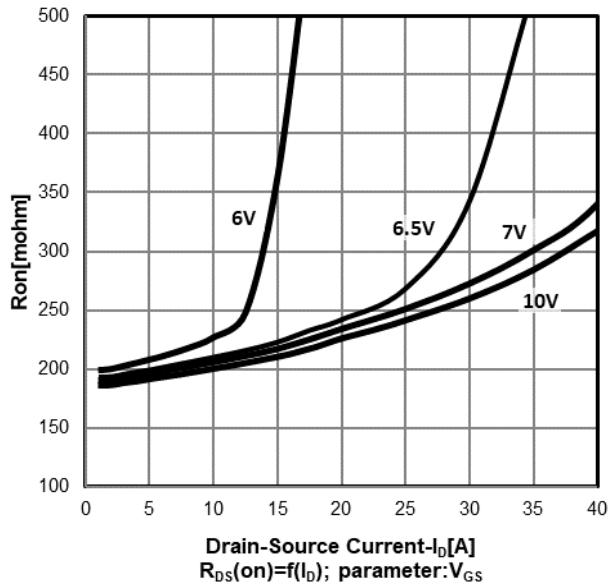




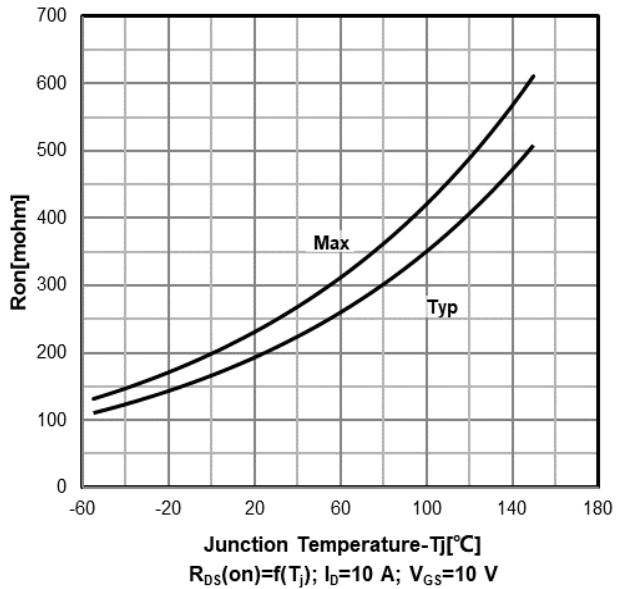
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## Typical Performance Characteristics

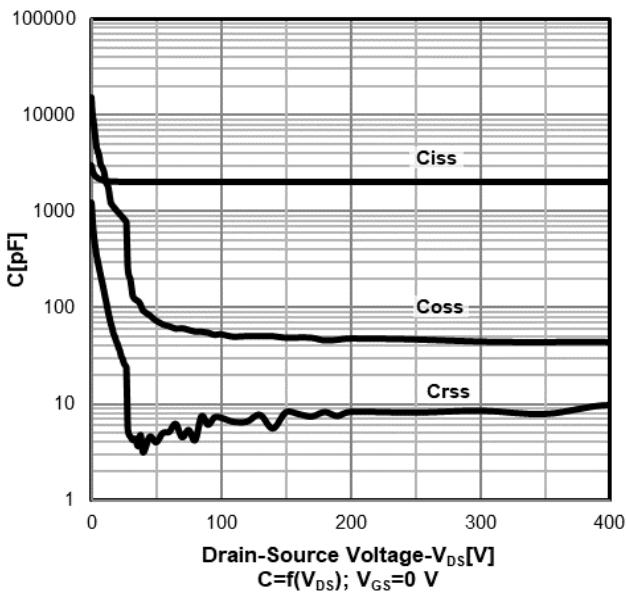
Typ. drain-source on-state resistance



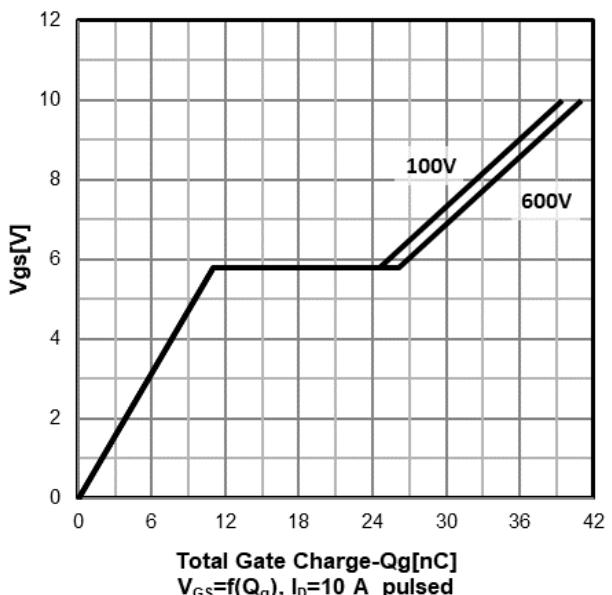
On resistance vs temperature



Typ. capacitances



Typ. gate charge characteristics

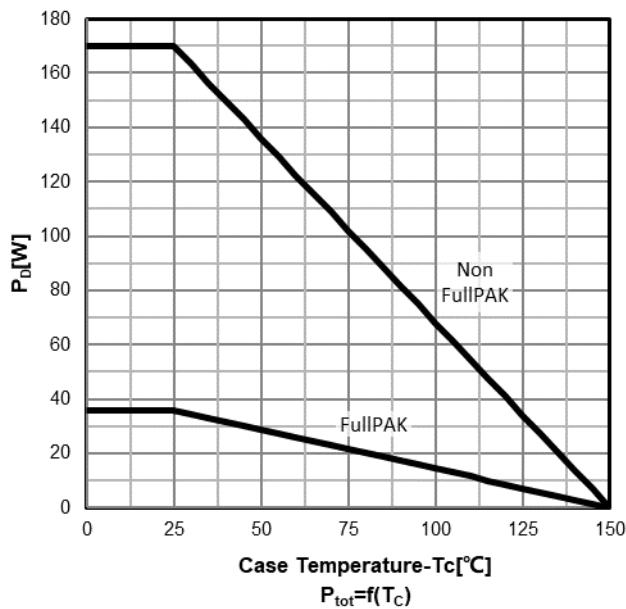




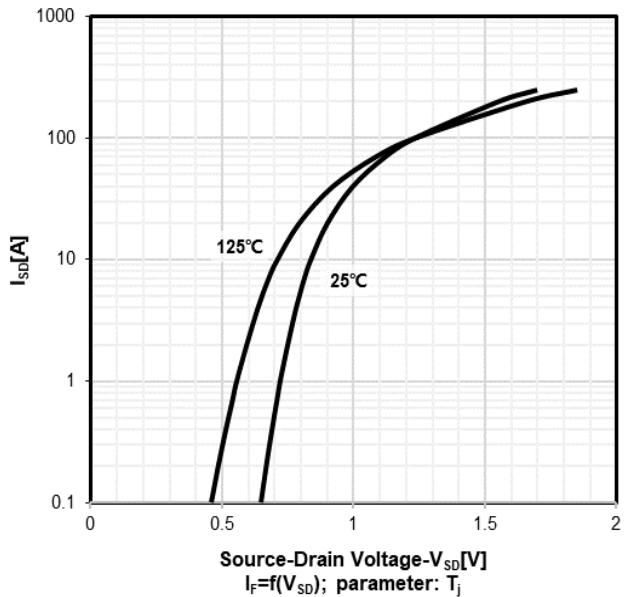
## Typical Performance Characteristics

SSB90R240S2/SSW90R240S2 900V N-Channel Super-Junction MOSFET Gen-II

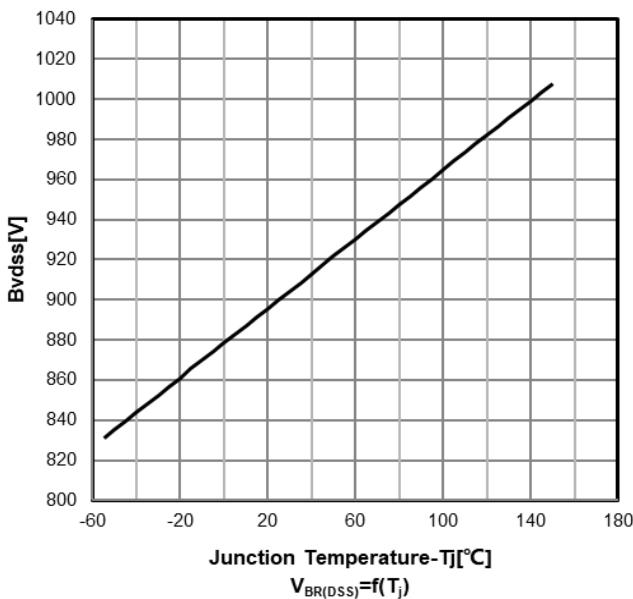
Power dissipation



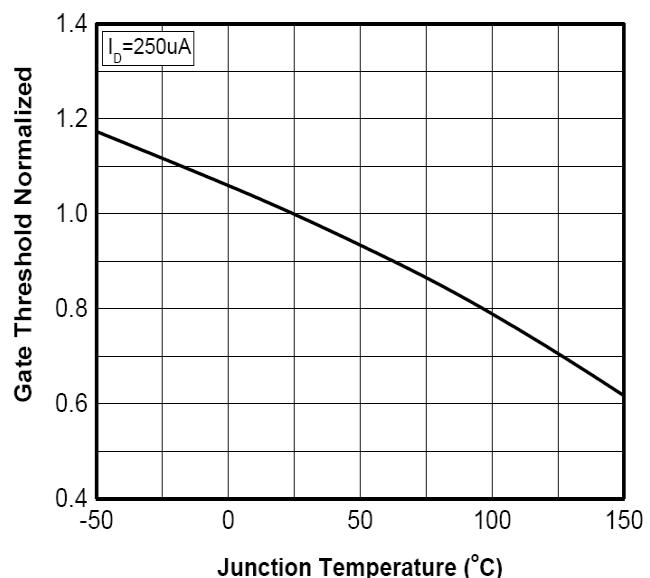
Forward characteristics of reverse diode



Drain-source breakdown voltage



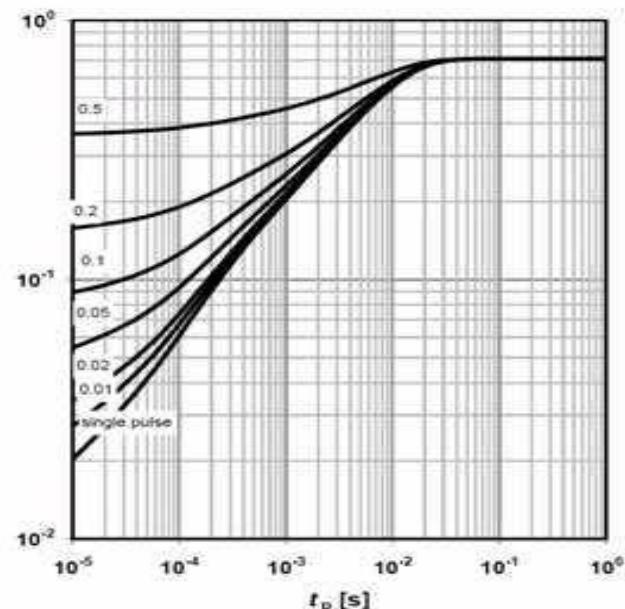
Normalized  $V_{GS(th)}$  characteristics



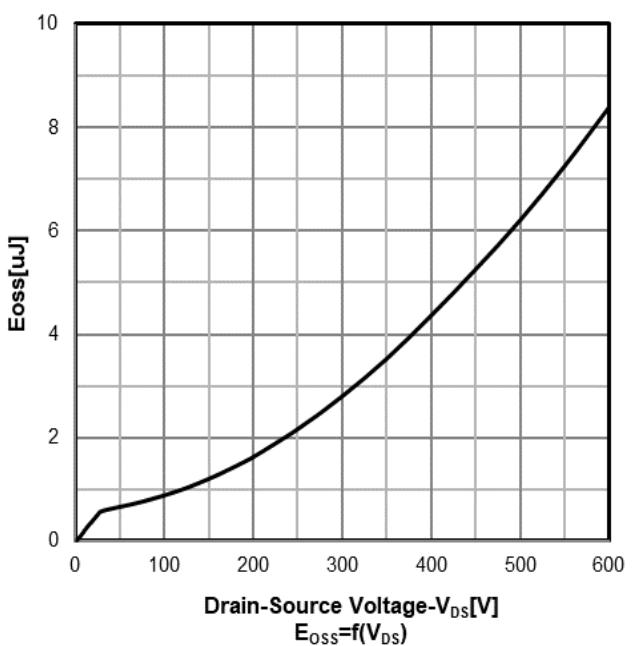


## Typical Performance Characteristics

Max. transient thermal impedance  
parameter:  $D = t_p/T$ ; Non-FullPAK



Cross stored energy





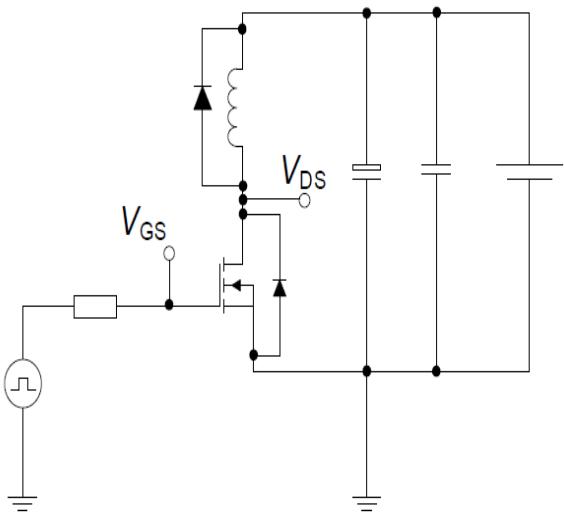
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## Test circuits

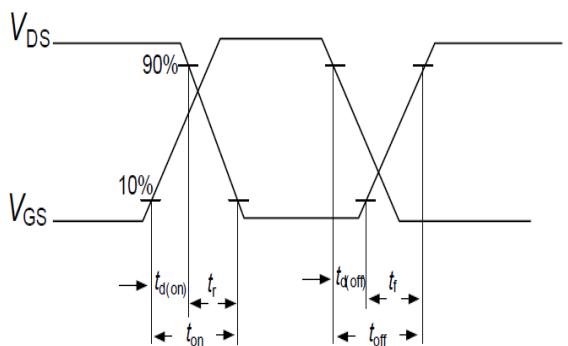
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Switching times test circuit and waveform for inductive load

Switching times test circuit for inductive load

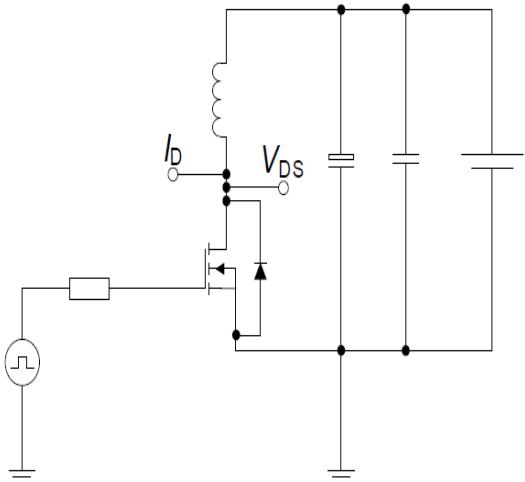


Switching time waveform

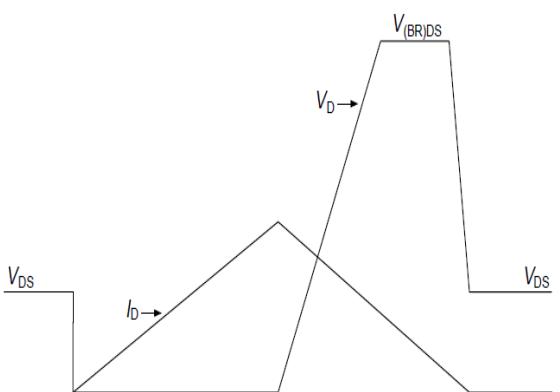


Unclamped inductive load test circuit and waveform

Unclamped inductive load test circuit



Unclamped inductive waveform





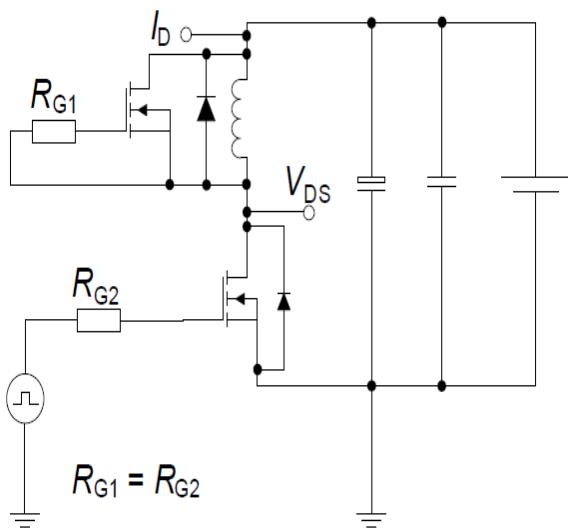
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## Test circuits

SSB90R240S2/SSW90R240S2 900V N-Channel Super-Junction MOSFET Gen-II

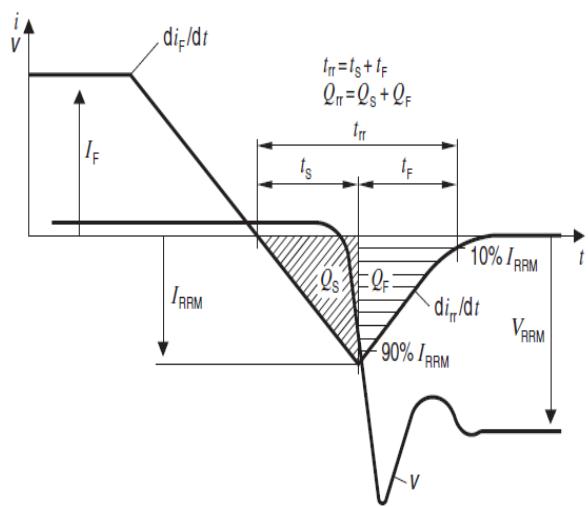
### Test circuit and waveform for diode characteristics

Test circuit for diode characteristics



$$R_{G1} = R_{G2}$$

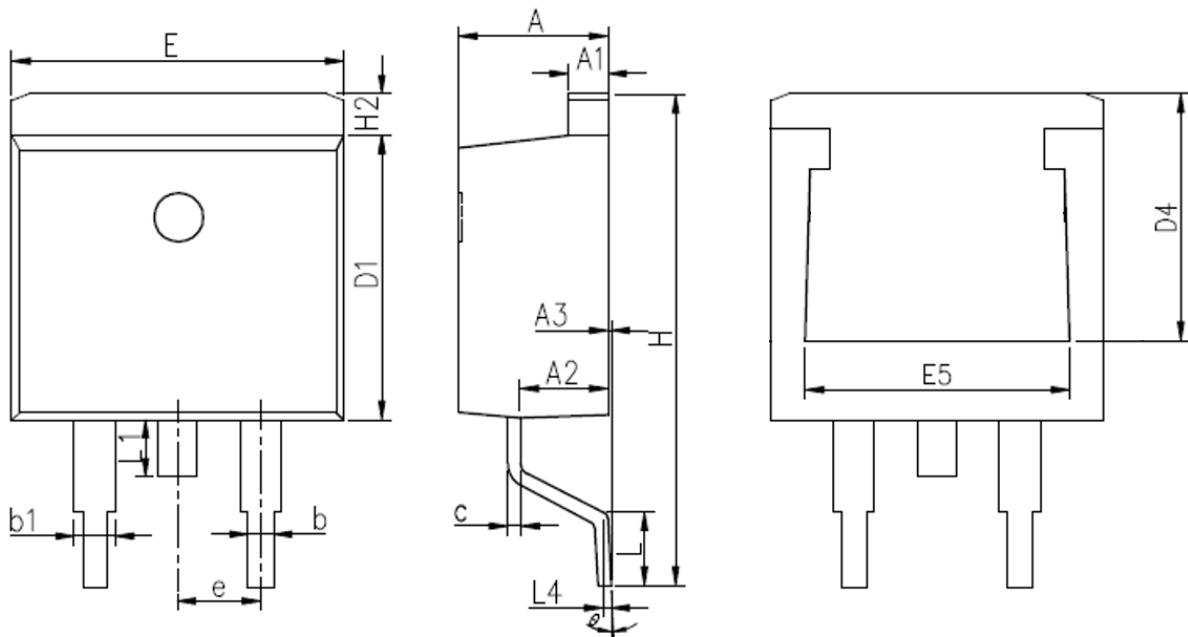
Diode recovery waveform





## Package Outline

TO-263



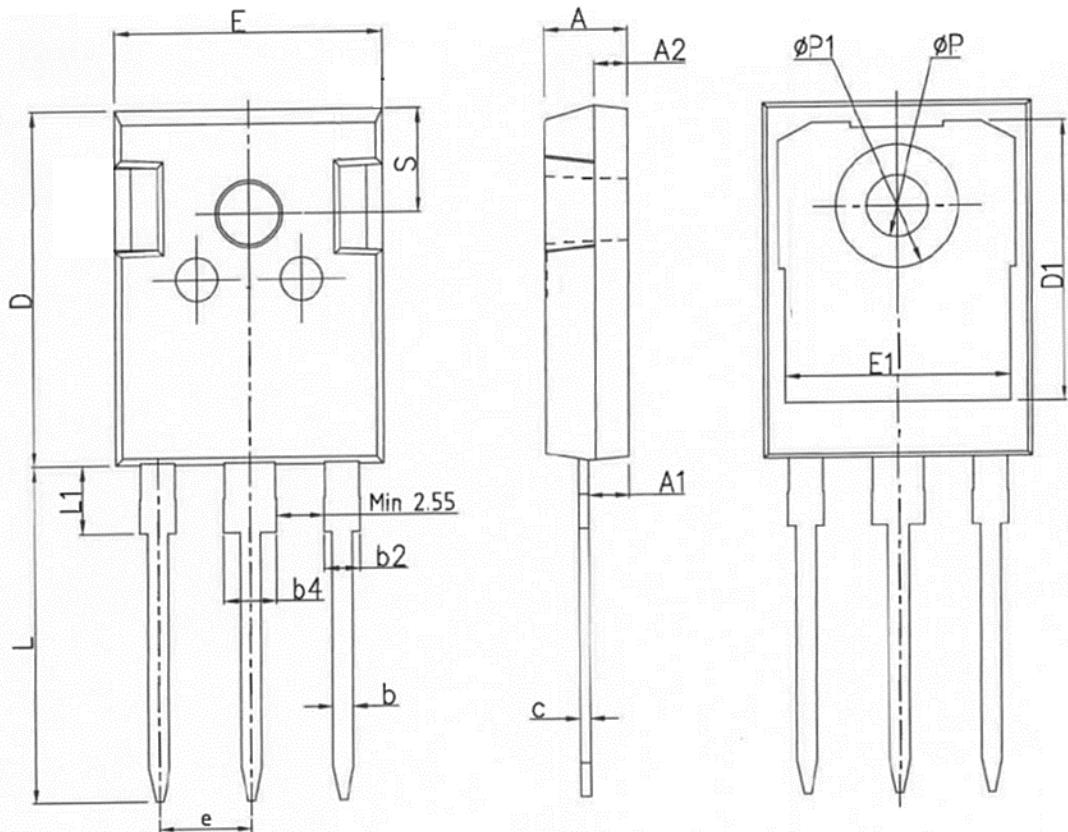
### COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
A3	0.00	0.13	0.25
b	0.70	0.81	0.96
b1	1.17	1.27	1.47
c	0.30	0.38	0.53
D1	8.50	8.70	8.90
D4	6.60	-	-
E	9.86	10.16	10.36
E5	7.06	-	-
e	2.54		BSC
H	14.70	15.10	15.50
H2	1.07	1.27	1.47
L	2.00	2.30	2.60
L1	1.40	1.55	1.70
L4	0.25		BSC
θ	0°	5°	9°

# Package Outline

TO-247

**SSB90R240S2/SSW90R240S2 900V N-Channel Super-Junction MOSFET Gen-II**



## COMMON DIMENSIONS

SYMBOL	UNIT(mm)		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.20	2.40	2.60
A2	1.85	2.00	2.15
b	1.10	1.20	1.35
b2	1.91	2.04	2.21
b4	2.91	3.04	3.21
c	0.50	0.60	0.75
D	20.70	21.00	21.30
D1	16.20	16.55	16.90
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
e	5.44BSC		
L	19.60	19.95	20.30
L1	-	-	4.30
$\Phi P$	3.40	3.60	3.80
$\Phi P1$	-	-	7.50
S	6.15BSC		



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