



SPN9926

N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN9926 is the Dual N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , notebook computer power management and other battery powered circuits where high-side switching .

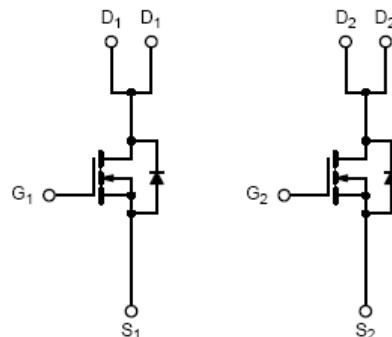
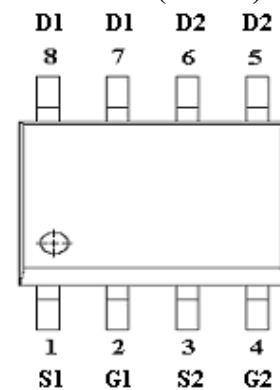
FEATURES

- ◆ 20V/4.0A,R_{DS(ON)}=55mΩ@V_{GS}=4.5V
- ◆ 20V/3.4A,R_{DS(ON)}=70mΩ@V_{GS}=2.5V
- ◆ 20V/2.8A,R_{DS(ON)}=90mΩ@V_{GS}=1.8V
- ◆ Super high density cell design for extremely low R_{DS(ON)}
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOP-8 package design

APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

PIN CONFIGURATION(SOP-8)



PART MARKING



A : Lot Code
B : Date Code



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PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN9926S8RGB	SOP-8	SPN9926
SPN9926S8TGB	SOP-8	SPN9926

※ SPN9926S8RGB : 13" Tape Reel ; Pb – Free ; Halogen – Free

※ SPN9926S8TGB : Tube ; Pb – Free ; Halogen – Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	20	V
Gate –Source Voltage	V _{GSS}	±20	V
Continuous Drain Current(T _J =150°C)	TA=25°C	4.0	A
	TA=70°C		
Pulsed Drain Current	I _{DM}	10	A
Continuous Source Current(Diode Conduction)	I _S	1.6	A
Power Dissipation	TA=25°C	1.25	W
	TA=70°C		
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	105	°C/W



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ELECTRICAL CHARACTERISTICS

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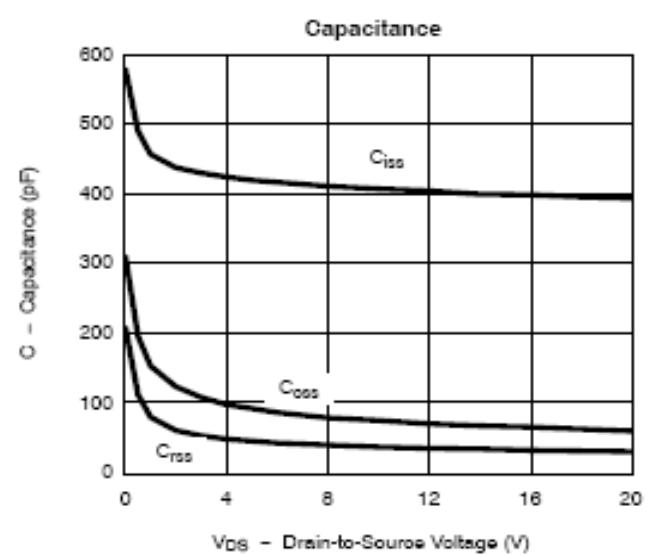
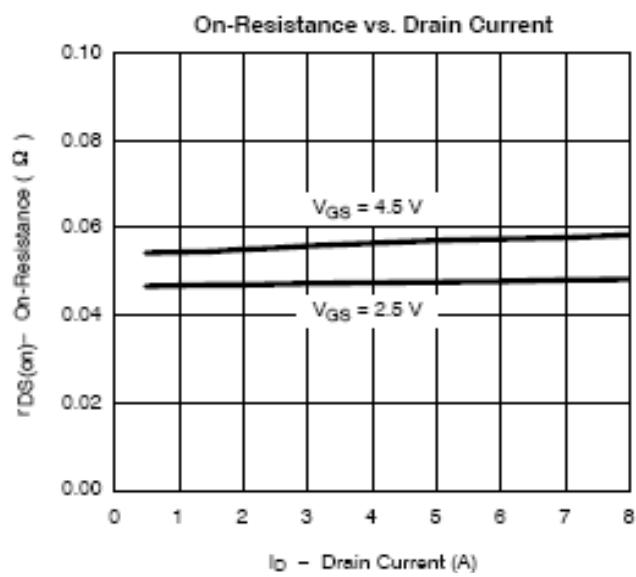
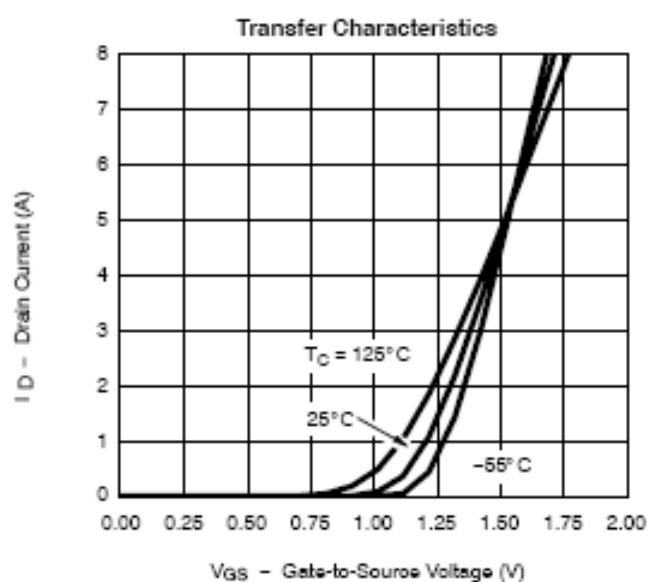
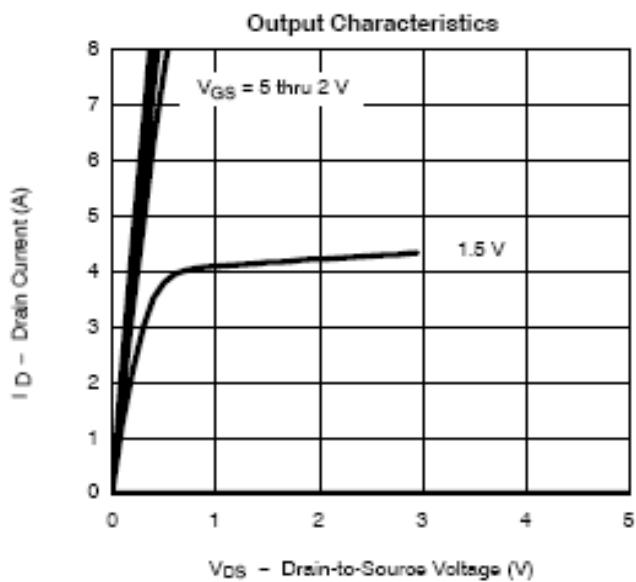
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=250uA	20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=250uA	0.4		1.0	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V			1	uA
		V _{DS} =20V, V _{GS} =0V T _J =55°C			5	
On-State Drain Current	I _{D(on)}	V _{DS} ≤5V, V _{GS} =4.5V	6			A
Drain-Source On-Resistance	R _{DSS(on)}	V _{GS} =4.5V, ID=4.0A		0.040	0.055	Ω
		V _{GS} =2.5V, ID=3.4A		0.055	0.070	
		V _{GS} =1.8V, ID=2.8A		0.075	0.090	
Forward Transconductance	g _f s	V _{DS} =5V, ID=-3.6A		10		S
Diode Forward Voltage	V _{SD}	I _S =1.6A, V _{GS} =0V		0.8	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =6V, V _{GS} =4.5V ID=2.8A		4.8	8	nC
Gate-Source Charge	Q _{gs}			1.0		
Gate-Drain Charge	Q _{gd}			1.0		
Input Capacitance	C _{iss}	V _{DS} =6V, V _{GS} =0V f=1MHz		485		pF
Output Capacitance	C _{oss}			85		
Reverse Transfer Capacitance	C _{rss}			40		
Turn-On Time	t _{d(on)}	V _{DD} =6V, R _L =6Ω ID=1.0A, V _{GEN} =4.5V R _G =6Ω		8	14	nS
	t _r			12	18	
Turn-Off Time	t _{d(off)}			30	35	
	t _f			12	16	



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TYPICAL CHARACTERISTICS

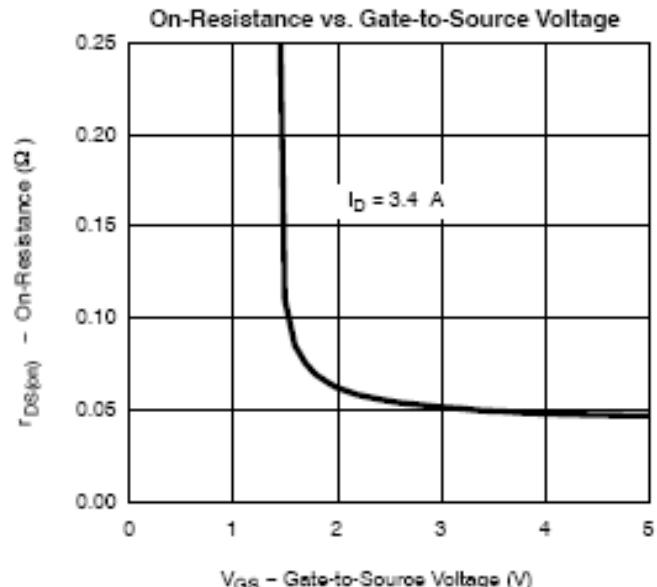
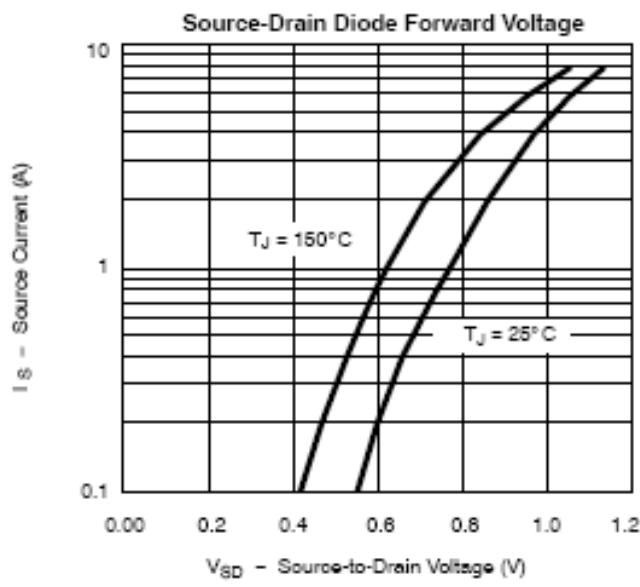
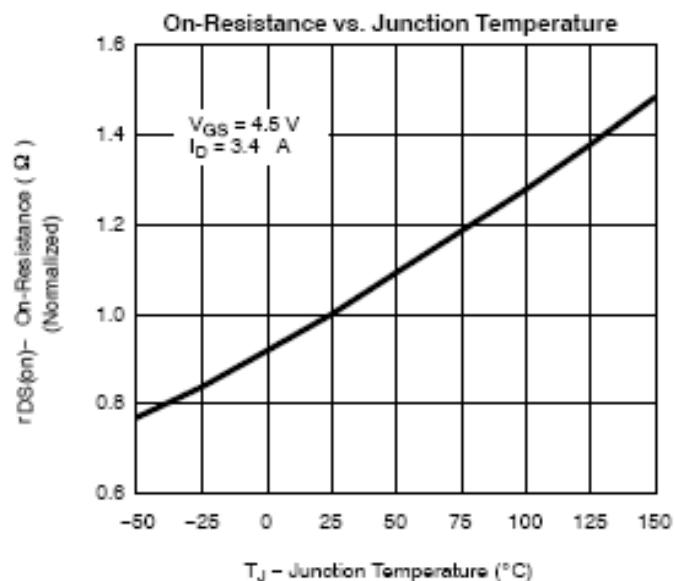
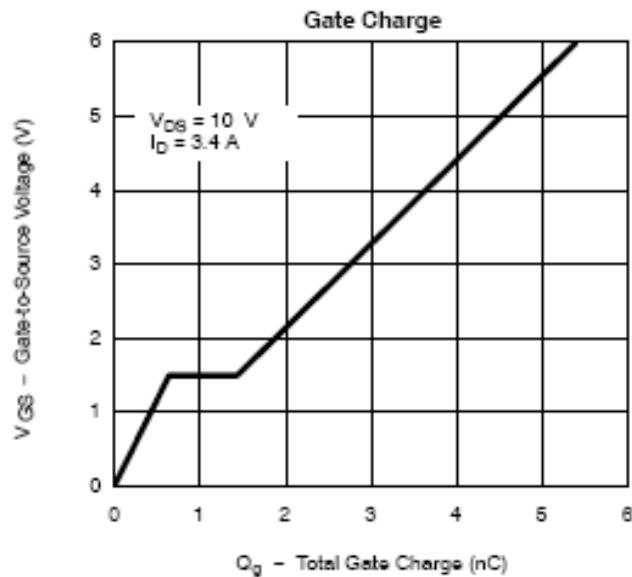




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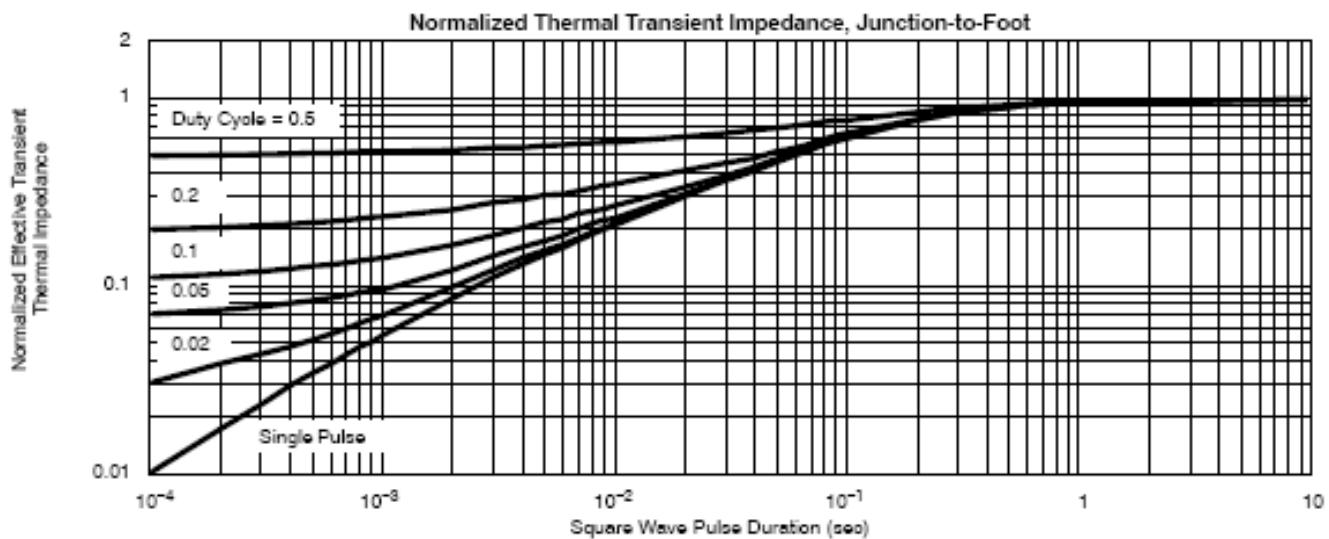
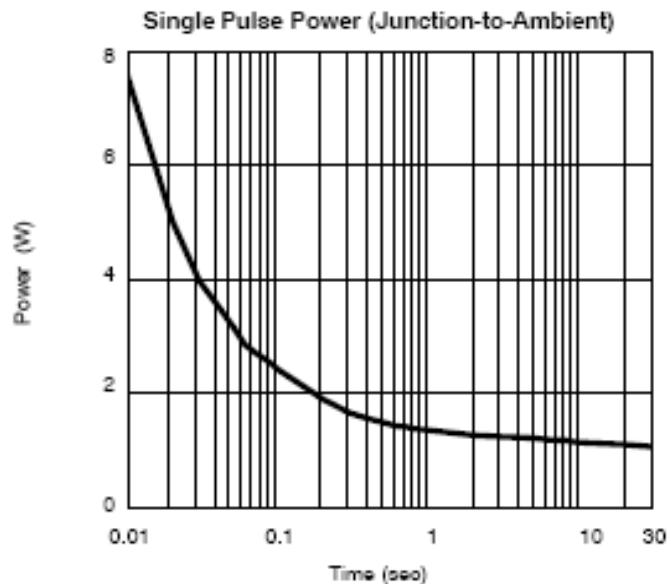
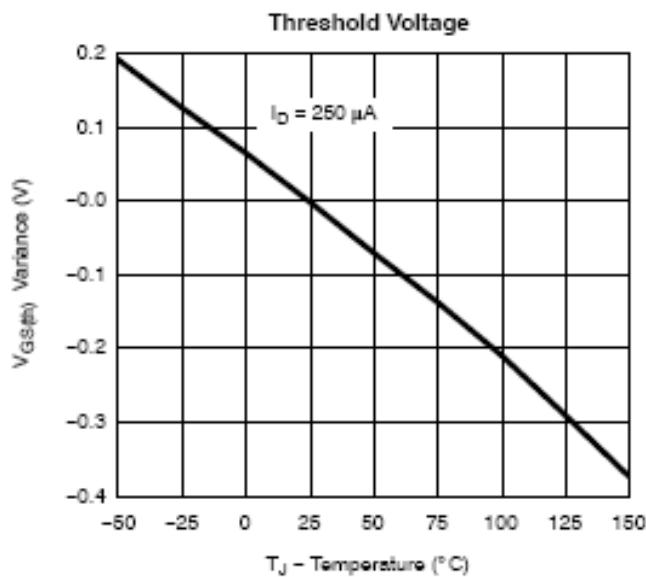




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TYPICAL CHARACTERISTICS





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