



SPN6562

Dual N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN6562 is the Dual N-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

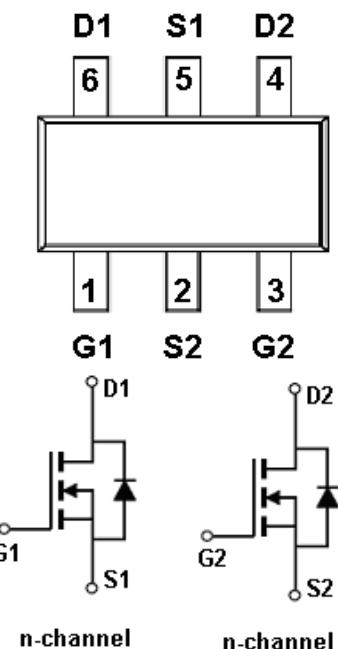
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

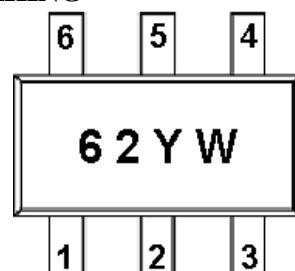
FEATURES

- ◆ N-Channel
30V/2.8A,R_{DS(ON)}=65mΩ@V_{GS}=10V
30V/2.3A,R_{DS(ON)}=75mΩ@V_{GS}=4.5V
30V/1.5A,R_{DS(ON)}=105mΩ@V_{GS}=2.5V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-23-6L package design

PIN CONFIGURATION(SOT-23-6L)



PART MARKING



Y : Year Code

W : Week Code



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PIN DESCRIPTION

Pin	Symbol	Description
1	G1	Gate 1
2	S2	Source 2
3	G2	Gate 2
4	D2	Drain 2
5	S1	Source 1
6	D1	Drain1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN6562S26RGB	SOT-23-6L	62

※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)

※ SPN6562S26RGB : Tape Reel ; Pb – Free; Halogen - Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	30	V
Gate –Source Voltage	V _{GSS}	±12	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	ID	2.8
	T _A =70°C		2.3
Pulsed Drain Current	I _{DM}	10	A
Continuous Source Current(Diode Conduction)	I _S	1.25	A
Power Dissipation	T _A =25°C	P _D	1.15
	T _A =70°C		0.75
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	T ≤ 10sec	R _{θJA}	50
	Steady State		100
			°C/W



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ELECTRICAL CHARACTERISTICS

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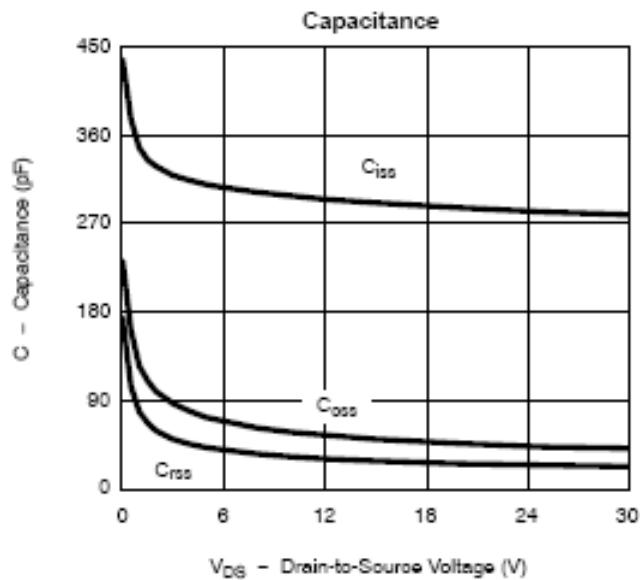
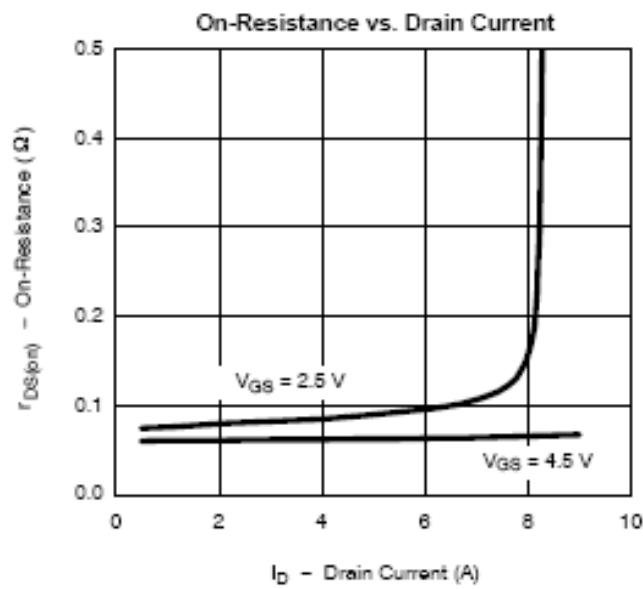
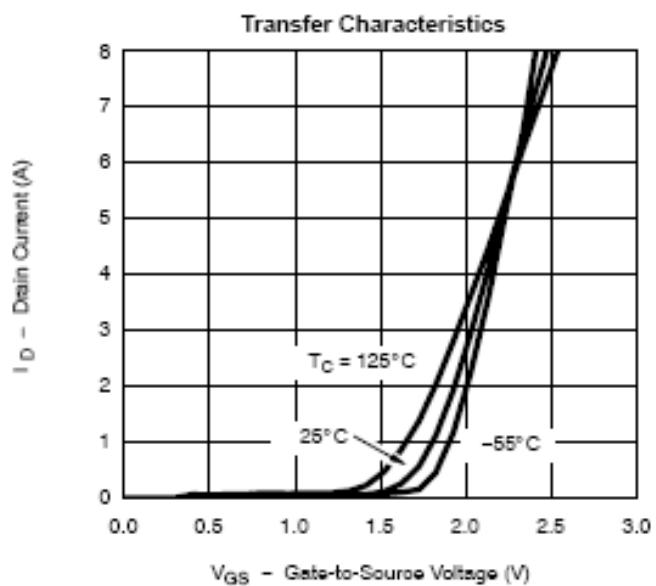
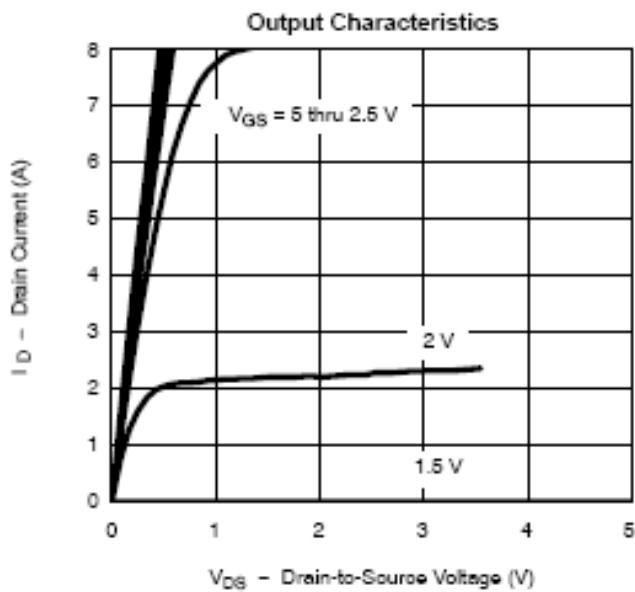
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=250uA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=250uA	0.5		1.6	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =1.0V			1	uA
		V _{DS} =24V, V _{GS} =0.0V T _J =55°C			10	
On-State Drain Current	I _{D(on)}	V _{DS} ≥4.5V, V _{GS} =10V	6			A
		V _{DS} ≥4.5V, V _{GS} =4.5V	4			
Drain-Source On-Resistance	R _{DSS(on)}	V _{GS} = 10V, ID=2.8A		0.055	0.065	Ω
		V _{GS} = 4.5V, ID=2.3A		0.065	0.075	
		V _{GS} = 2.5V, ID=1.5A		0.085	0.105	
Forward Transconductance	g _{fs}	V _{DS} =4.5V, ID=2.5A		4.6		S
Diode Forward Voltage	V _{SD}	I _S =1.25A, V _{GS} =0V		0.82	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =15, V _{GS} =4.5V ID=2.0A		4.2	6	nC
Gate-Source Charge	Q _{gs}			0.6		
Gate-Drain Charge	Q _{gd}			1.5		
Input Capacitance	C _{iss}	V _{DS} =15, V _{GS} =0V f=1MHz		350		pF
Output Capacitance	C _{oss}			55		
Reverse Transfer Capacitance	C _{rss}			41		
Turn-On Time	t _{d(on)}	V _{DD} =15, R _L =10Ω V _{GEN} =10V, R _G =3Ω		2.5		nS
	t _r			2.5		
Turn-Off Time	t _{d(off)}			20		
	t _f			4		



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TYPICAL CHARACTERISTICS

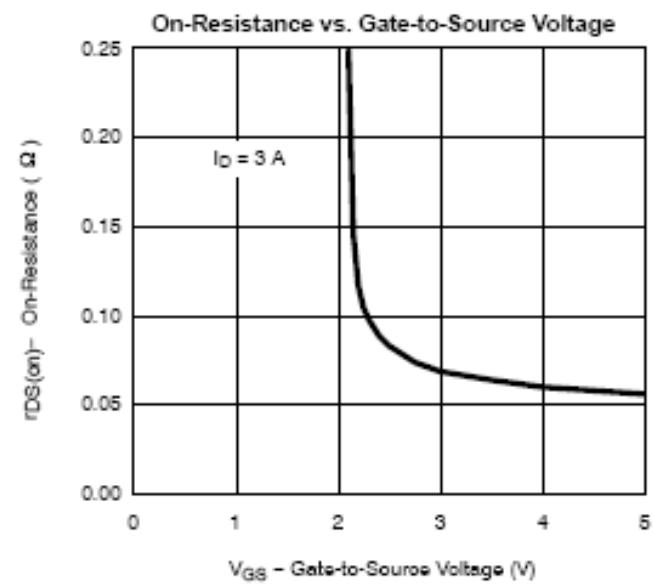
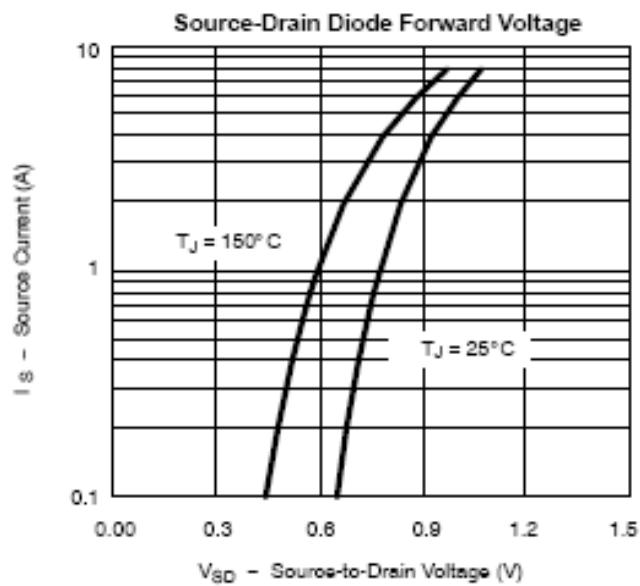
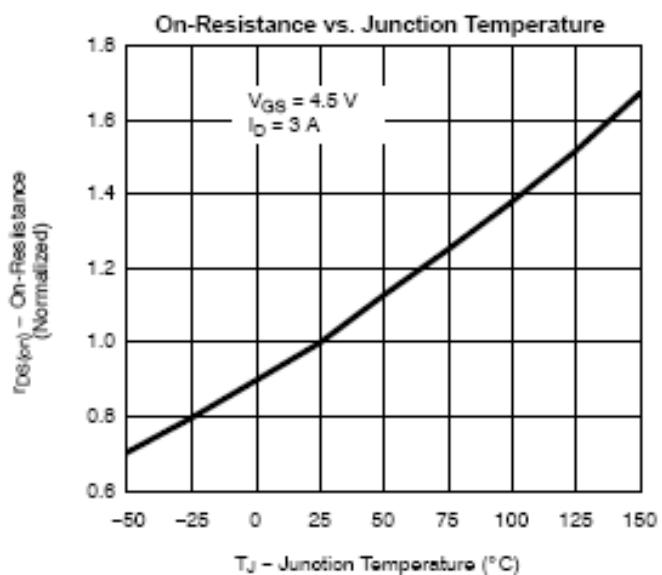
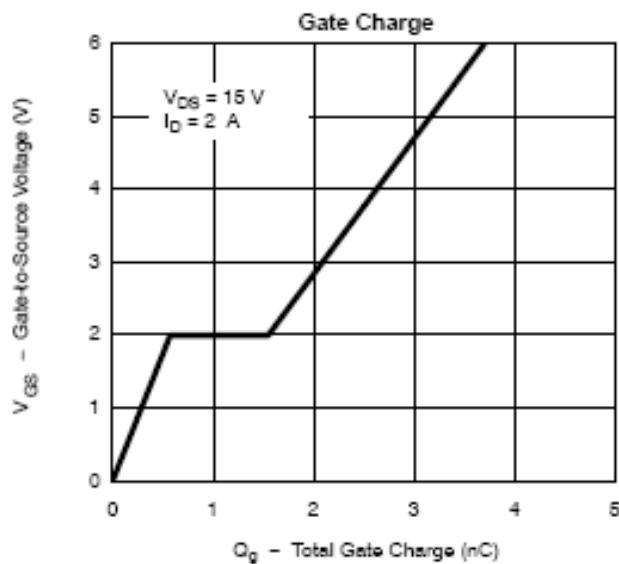




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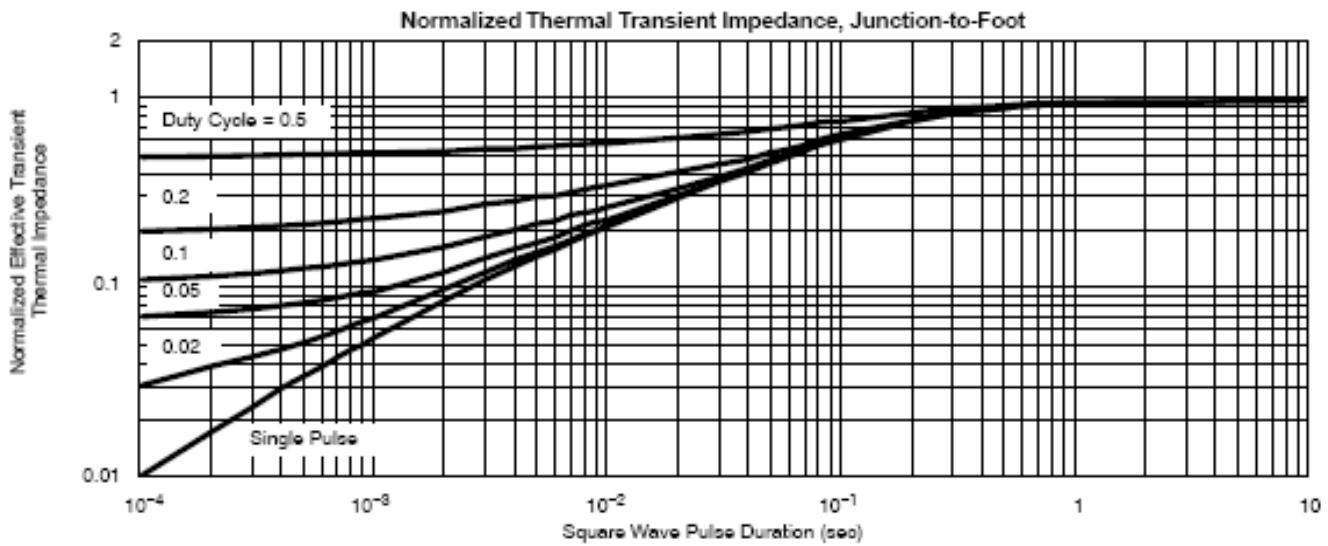
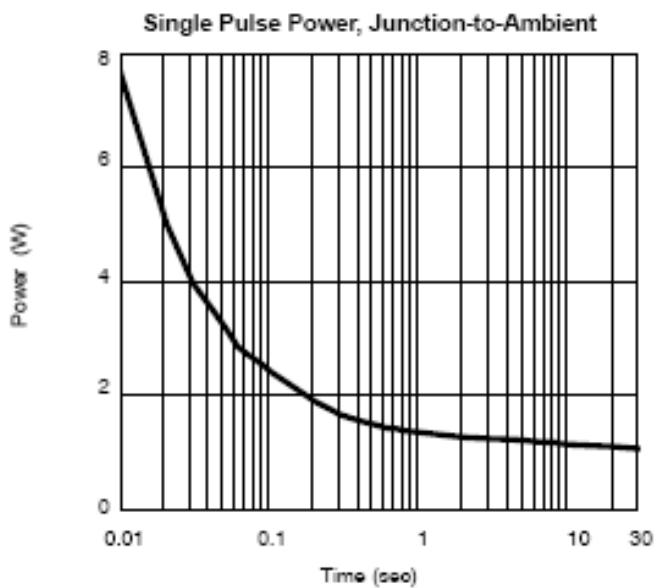
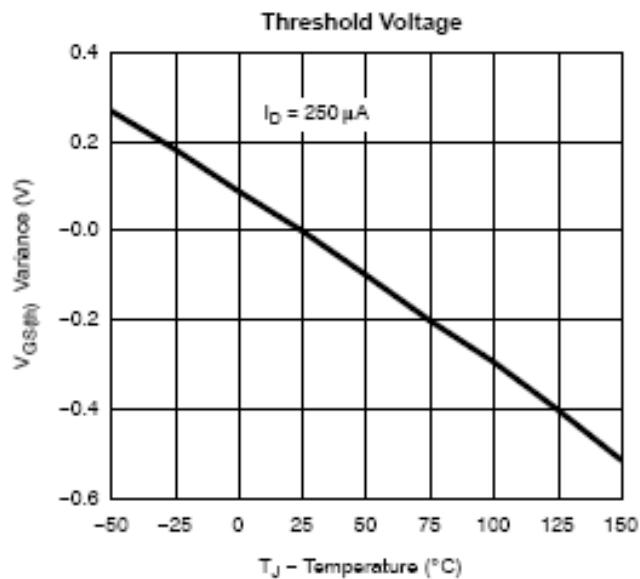




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TYPICAL CHARACTERISTICS





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