TECHNICAL DATA DATASHEET 4109, REV ENG-

# Three-Phase IGBT BRIDGE, With Gate Driver and Optical Isolation

DESCRIPTION: A 1200 VOLT, 250 AMP, THREE PHASE IGBT BRIDGE

ELECTRICAL CHARACTERISTICS PER IGBT DEVICE		(Tj=25°C UNLESS OTHERWISE SPECIFIED)					
PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT	
IGBT SPECIFICATIONS							
Collector to Emitter Breakdown Voltage		BV <sub>CES</sub>	1200	-	-	V	
$I_{\rm C} = 250 \ \mu \text{A}, \ V_{\rm GE} = 0 \text{V}$							
Continuous Collector Current	$T_{c} = 25 \ ^{O}C$	I <sub>C</sub>	-	-	250	А	
	$T_{c} = 90 {}^{O}C$				240		
Pulsed Collector Current, 1mS		I <sub>CM</sub>	-	-	600	А	
Gate to Emitter Voltage		V <sub>GE</sub>	-	-	+/-20	V	
Gate-Emitter Leakage Current , $V_{GE}$ = +/-20V		I <sub>GES</sub>	-	-	+/- 300	nA	
Gate Threshold Voltage, I <sub>c</sub> =2mA		V <sub>GE(TH)</sub>	3.0	-	6.0	V	
Zero Gate Voltage Collector Current		I <sub>CES</sub>	-	-			
$V_{CE} = 1200 \text{ V}, V_{GE} = 0 \text{ V} \text{ T}_{i} = 25^{\circ} \text{C}$					5	mA	
$V_{CE} = 900 \text{ V}, V_{GE} = 0 \text{ V} \text{ T}_{i} = 125^{\circ}\text{C}$					40	mA	
Collector to Emitter Saturation Voltage, $I_{C} = 200A, V_{GE} = 15V,$	$T_c = 25$ °C	$V_{CE(SAT)}$	-	2.5	2.8	V	
Maximum Thermal Resistance		R <sub>eJC</sub>	-	-	0.10	°C/W	
Brake IGBT					0.20	°C/W	
60A Maximum Current							
Brake IGBT SPECIFICATIONS							
Continuous Collector Current	$T_c = 25$ °C	I <sub>C</sub>	-	-	150	А	
	$T_{c} = 90 {}^{O}C$				120		
Pulsed Collector Current, 0.5mS		I <sub>CM</sub>	-	-	300	А	
Over-Temperature Shutdown							
Over-Temperature Shutdown		Tsd	100	110	120	°C	
Over-Temperature Shutdown Hysteresis				20		°C	
Over-Temperature Output		Tso		10		10mV/°C	

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ULTRAFAST DIODES RATING AND CHARACTERISTICS						
Diode Peak Inverse Voltage	PIV	1200	-	-	V	
Continuous Forward Current, $T_c = 90$ °C	l <sub>F</sub>	-	-	240	А	
Forward Surge Current, t <sub>p</sub> = 10 msec	I <sub>FSM</sub>	-	-	700	A	
Diode Forward Voltage, $I_F = 200A$	V <sub>F</sub>	-	2.0	2.3	V	
Diode Reverse Recovery Time (I <sub>F</sub> =200A, V <sub>RR</sub> =600V , di/dt=200 A/ $\mu$ s)	t <sub>rr</sub>	-	180	250	nsec	
Maximum Thermal Resistance	$R_{ ext{ heta}JC}$	-	-	0.15	°C/W	
Gate Driver						
Supply Voltage	VCC	10	15	20	V	
Input On Current	HIN, LIN	2		5.0	mA	
Opto-Isolator Logic High Input Threshold	l <sub>th</sub>	-	1.6	-	mA	
Input Reverse Breakdown Voltage	BV <sub>in</sub>	5.0	-	-	V	
Input Forward Voltage @ I <sub>in</sub> = 5mA	V <sub>F</sub>	-	1.5	1.7	V	
Under Voltage Lockout	VCCUV	7.0	-	9.7	V	
ITRIP Refernce Voltage (1)	Itrip-ref	1.45	1.5	1.55	V	
Desaturation Over-Current Protection Blanking time (2)		3	5	TBD	μsec	
Input-to-Output Turn On Delay	t <sub>ond</sub>	-		800	nsec	
Output Turn On Rise Time	t <sub>r</sub>	-		180		
Input-to-Output Turn Off Delay	t <sub>offd</sub>	-		1000		
	t <sub>f</sub>			160		
At VCC=300V, IC=50A, $T_c = 25$						
Input-Output Isolation Voltage	-	1000	-	-	V	
Hall Current Sensors Gain, at DC bus, Phase A, and Phase		TBD	TBD	TBD	V/A	

Maximum operating Junction Temperature	T <sub>jmax</sub>	-40	-	150	°C
Maximum Storage Junction Temperature	T <sub>jmax</sub>	-55	-	150	٥C

ITRIP Cycle-by cycle current limit is internally set to 200A peak. The set point can be lowered by connecting a resistor between ltrip-ref and Gnd. The set point can be increased by connecting a resistor between ltrip-ref and +5V ref

(2) Desaturation blanking maximum time is TBD and is only provided at the low-side IGBTs.

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## **Schematic Diagram:**



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## Package Drawing:



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