

Isc N-Channel MOSFET Transistor

SPA07N60C3

• FEATURES

- With TO-220F package
- Low input capacitance and gate charge
- Low gate input resistance
- Reduced switching and conduction losses
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

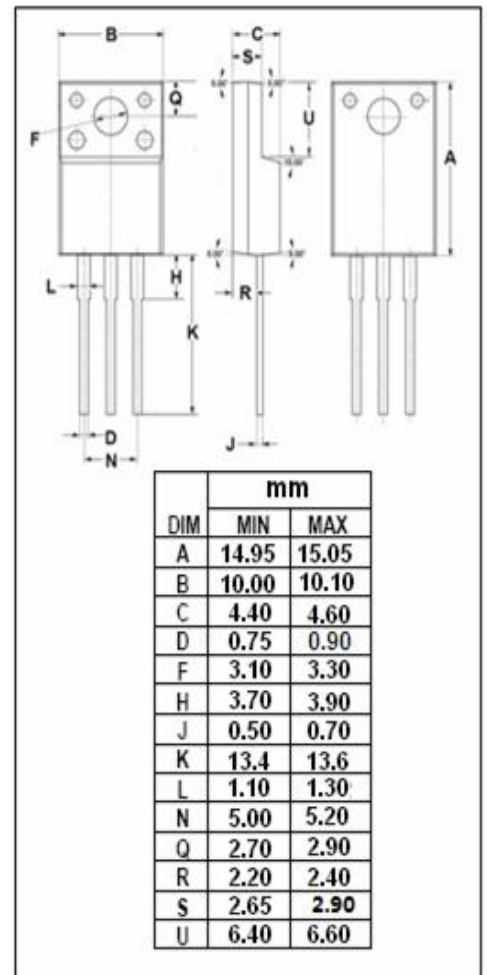
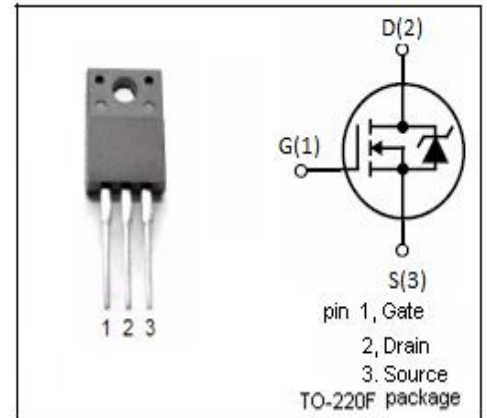
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous @ $T_c=25^{\circ}\text{C}$ (V_{GS} at 10V) $T_c=100^{\circ}\text{C}$	7.3 4.6	A
I_{DM}	Drain Current-Single Pulsed	21.9	A
P_D	Total Dissipation @ $T_c=25^{\circ}\text{C}$	32	W
T_j	Max. Operating Junction Temperature	-55~150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature	-55~150	$^{\circ}\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	3.9	$^{\circ}\text{C/W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	80	$^{\circ}\text{C/W}$



Isc N-Channel MOSFET Transistor**SPA07N60C3****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D =0.25mA	600			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} ; I _D =350uA	2.1	3	3.9	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = 10V; I _D =3.4A; T _j =25°C V _{GS} = 10V; I _D =3.4A; T _j =150°C		0.54 1.46	0.6	Ω
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±30V; V _{DS} = 0V			±100	nA
I _{DSS}	Drain-Source Leakage Current	V _{DS} = 600V; V _{GS} = 0V; T _j =25°C V _{DS} = 600V; V _{GS} = 0V; T _j =150°C	0.5	1 100		μA
V _{SDF}	Diode forward voltage	I _{SD} =7.3A, V _{GS} = 0 V		1	1.2	V

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