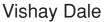
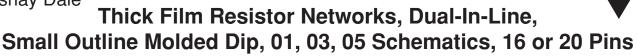
## SOGC 01, 03, 05









## FEATURES

- · 0.110" [2.79mm] maximum seated height
- Rugged, molded case construction
- 0.050" [1.27mm] lead spacing
- · Reduces total assembly costs
- · Compatible with automatic surface mounting equipment
- Uniform performance characteristics
- Meets EIA PDP 100, SOGN-0003 outline dimensions
- · Available in tube pack or tape and reel pack

## STANDARD ELECTRICAL SPECIFICATIONS

MODEL	SCHEMATIC	RESISTOR CIRCUIT W @ 70°C	PACKAGE POWER W @ 70°C	TOLERANCE ±%	RESISTANCE RANGE Ω	OPERATING VOLTAGE VDC	TEMPERATURE COEFFICIENT ppm/°C
SOGC-16	01	0.1	1.6	2 (1, 5*)	10-1M0	50 max	100
	03	0.19	1.6	2 (1, 5*)	10-1M0	50 max	100
	05	0.1	1.6	2 (5*)	10-1M0	50 max	100
SOGC-20	01	0.1	2.0	2 (1, 5*)	10-1M0	50 max	100
	03	0.19	2.0	2 (1, 5*)	10-1M0	50 max	100
	05	0.1	2.0	2 (5*)	10-1M0	50 max	100

\* Tolerances in brackets available upon request.

100 milliohm maximum on zero ohm jumper

TECHNICAL SPECIFICATIONS				
PARAMETER	UNIT	S0GC-16 / -20		
Package Power Rating (max. at + 70°C)	W	1.6 / 2.0		
TC Tracking (- 55°C to + 125°C)	ppm/°C	± 50		
Voltage Coefficient of Resistance:	ppm/V	< 50 typical.		
Maximum Operating Voltage:	VDC	50		
Operating Temperature Range:	°C	- 55 to + 125.		
Storage Temperature Range:	°C	- 55 to + 150		

MECHANICAL SPECIFICATIONS					
Marking:	Model number, schematic number, value tolerance, pin 1 indicator, date code.				
Marking Resistance to Solvents:	Permanency testing per MIL-STD-202, Method 215.				
Maximum Solder Reflow Temperature:	+ 255ºC				
Solderability:	Per MIL-STD-202, Method 208E.				
Terminals:	Copper alloy. 60/40 solder dipped terminal.				
Body:	Molded epoxy.				

ORDERING INFORMATION							
01, 03 Schematic SOGC	16 20	01 03	xxx or xxxx	G			
MODEL	NUMBER OF	SCHEMATIC	R₁ VALUE	TOLERANCE			
	LEADS		First 2 digits (3 for F tolerance) are significant figures. Last digit specifies number of zeros to follow.	F = ± 1% G= ± 2% J = ± 5%			
05 Schematic			XXX XXX				
SOGC MODEL	16 20 NUMBER OF	05 SCHEMATIC	or or xxxx xxxx R <sub>1</sub> VALUE R <sub>2</sub> VALUE	<b>G</b> TOLERANCE			
	LEADS		First 2 digits (3 for F tolerance) are significant figures. Last digit specifies number of zeros to follow.	$\begin{array}{l} F = \pm \ 1\% \\ G = \pm \ 2\% \\ J = \pm \ 5\% \end{array}$			

For Technical Questions, contact: ff2aresistors@vishay.com

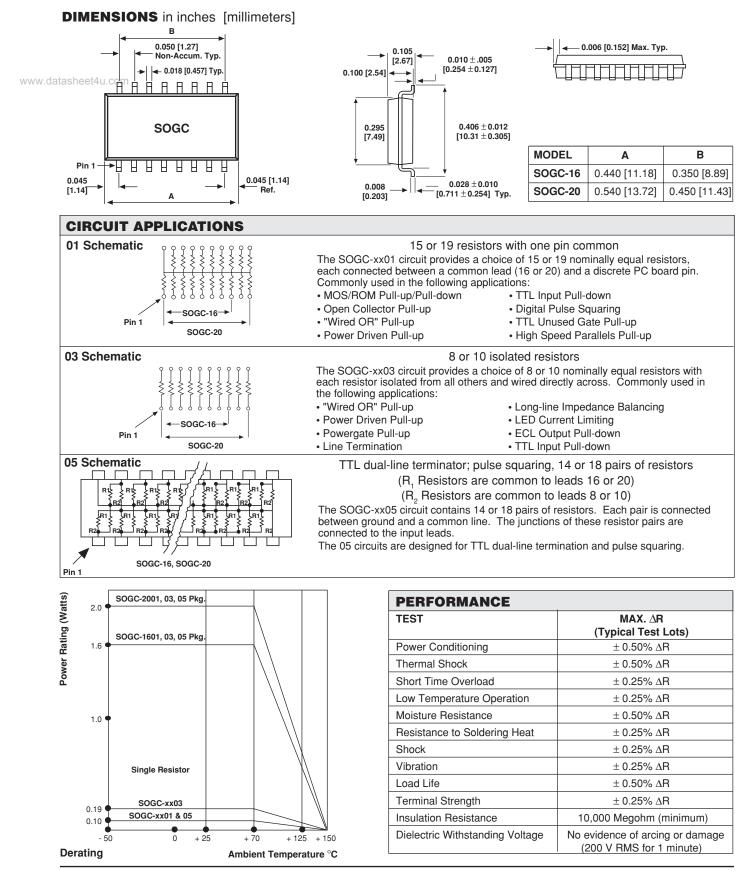
Document Number: 31506



## SOGC 01, 03, 05

Thick Film Resistor Networks, Small Outlline, Molded, DIP

Vishay Dale



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