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TY-OHM ELECTRONIC WORKS CO., LTD.

**SMW POWER WIRE WOUND CHIP RESISTORS
RESISTOR SPECIFICATION**

Version: 2003.B

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SMW POWER WIRE WOUND CHIP RESISTORS

1. Applicable Scope:

This SMW standard specification is for use in consumer electronics, computers, telecommunications, control instruments...etc.

2. Part Number:

It is composed by Type, Rated Wattage, Nominal Resistance and Tolerance. e.g.

SMW	1W	10Ω	J
Type	Rated Wattage	Nominal Resistance	Tolerance

2.1 Type :

Power Wire Wound Chip Resistors are called "SMW".

2.2 Rated Wattage:

Shown by "W", such as 1W, 2W, 3W.

2.3 Nominal Resistance:

Ω are its unit, which be in accordance with JIS-C6409 article 6 (EIA RS-196A) series.

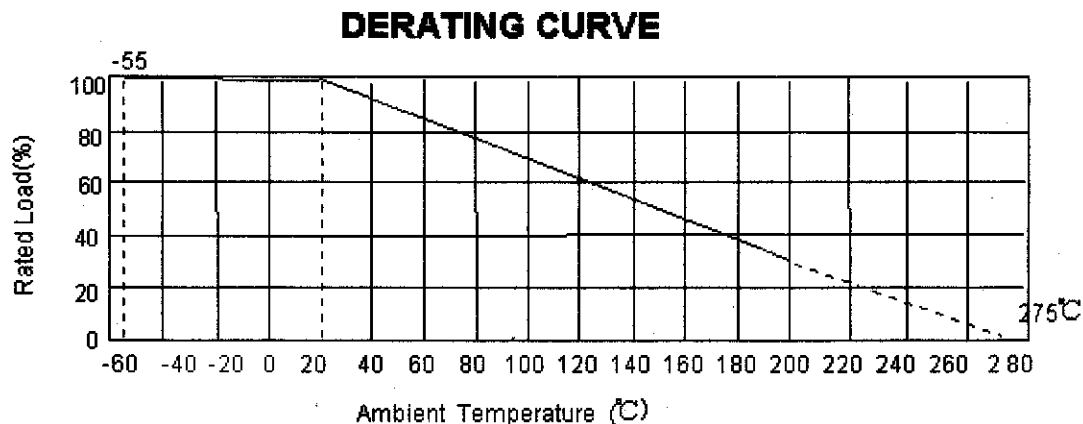
2.4 Tolerance:

It is measured by Bridge-method at room temperature and expressed by a capital letter.

J= ±5% , F= ±1%

3. Rated Power:

Rated power is the value of Max load voltage specified at the ambient temperature of 20°C, and shall meet the functions of electrical and mechanical performance. When the ambient temperature surpasses above mentioned temperature, the value declines as per following DERATING CURVE.



3.1 Rated Voltage:

It is calculated through the following formula:

$$E = \sqrt{PXR}$$

where E: rated voltage (V)

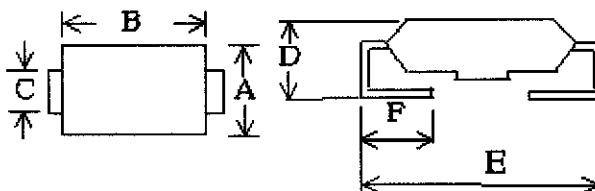
P: rated power (W)

R: total nominal resistance (Ω)

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4. Dimension and structure:

4.1 Dimension



TYPE	Dimension (mm)						Resistance Range	Resistance Tolerance
SMW	A±0.3	B±0.3	C±0.3	D±0.3	E max.	F±0.3		
1W	3.3	4.5	1.2	2.7	6.0	1.1	0.1Ω~22Ω	F=±1% J=±5%
2W	4.0	6.7	1.4	3.55	7.9	1.5	0.1Ω~47Ω	
3W	5.5	10.5	1.7	5	12	2.3	0.1Ω~100Ω	

Note:

1.The resistance ranges listed above are the one wound by auto machines.

2.Higher or lower values can be produced by hand-handing wire wound process.

4.2 S STRUCTURE

4.2.1 Material:

Terminal is to be firmly connected with resistors element, both electrically and mechanically, and allow easy soldering.

4.2.2 Coating:

Flame retardant molded package, resistant to heat, humidity & insulation.

4.2.3 Marking:

Marking is made on the surface with Rated Wattage, Nominal Resistance, Tolerance and Maker's trade mark (TY-OHM).

5. Operating Temperature Range: -55°C ~ 200°C

6. Electrical Performance

6.1 Resistance Temperature Coefficient:

It shall be within : ± 200ppm/°C

$$T.C \text{ (ppm/°C)} = [(R2 - R1) \div R1] \times [1 \div (T2 - T1)] \times 10$$

where

R1: resistance value at reference temperature

R2: resistance value at test temp.

T1: reference temp. (usu. 25°C)

T2: test temp. (about 75°C)

6.2 Temperature Cycle:

Following temp. cycles are to be made 5 times and then put at room temp. for one hour, the resistance value change rate between pre-and-post test shall be within ± 1%.

Steps	Temperature(°C)	Time (minutes)
1 st step	-55±3	30
2 nd step	Room temp.	2~3
3 rd step	200±3	30
4 th step	Room temp.	2~3

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6.3 Short Time Over Load:

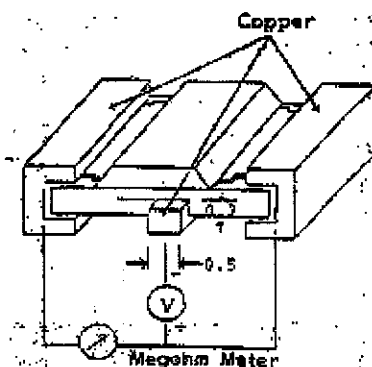
When the resistors are applied 5 times as much as rated wattage for 5 seconds continuously, it shows no evidence of arc, flame...etc. Removing the voltage and place the resistors to the normal condition for 30 minutes, the resistance value change rate between pre-and-post test shall be within $\pm 1\%$.

6.4 Insulation Character :

Resistors are located in a jig and applying DC500V, measuring the Insulation Resistance which shall be over 10000M Ω .

6.5 Voltage Withstanding:

Resistors are located in a jig and applying DC500V for 1 minute and should find no physical damage to the resistors, such as arc, char...etc.



6.6 Load Life:

The resistors arrayed are sent into the 70°C oven, applying rated voltage at the cycle of 1.5 hours ON, 0.5 hour OFF for 1000^{+48}_{-0} hours in total. Then, after removing the voltage, take the resistors out of the oven and left under normal temp. for about one hour for cooling. The resistance value change rate between pre-and-post test shall be within $\pm 2\%$.

6.7 Moisture-proof Load Life:

The resistors arrayed are placed into a constant temp./humidity oven at the temp. of $40 \pm 2^\circ\text{C}$ and the humidity of 90~95%, then rated power is applied for 1.5 hours and cut off for 0.5 hour. The similar cycle will be repeated for 500^{+24}_{-0} hours in total (including cut-off time). Then remove the voltage, taking the resistors out of the oven and leaving them at room temp. for one hour. The resistance value change rate between pre-and-post test shall be within $\pm 2\%$. There also shall be no evidence of remarkable change on appearance, and the marking shall not be illegible.

6.8 Solder-ability:

Immerse the resistors in the solder pot at $235 \pm 5^\circ\text{C}$ for 2 seconds. At least 95% solder coverage on the termination.