CERF	amate	TYPE	SMV1812M□□□KRR	MODEL		PAGE	1/9
CITA					DATE	Feb. 03, 2	2009
SUBJ	ECT		Structure	REV.	C01		
1.	STRUCT	URE					-
NO.	ITE	EM	DESCI	RIPTION			ļ
1.1	Main M	laterial	Zinc Oxide				
1.2	End terr	nination	Ag/Ni/Sn				
1.3	Pack	aging	Reel				
1.4	-	es with dard	EC61000-4-5				
1.5	-	with RoHS dard	Yes				
1.6	Lead o	ontent	< 1000ppm				
1.7	tempe	der profile erature nmend)	<b>250</b> ℃				
				L	4.50	± 0.20	
			a	1 W	3.20	± 0.20	
				T(max.)	2	2.00	
1.8	Dimor	nsions		а	0.5+	0.2/-0.1	
1.0	Dimer	611016	W. (Unit : m	m)			

CERAMATE	TYPE	SMV1812M□□□KRR	MODEL		PAGE	2/9
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SUBJECT	E	Electrical Characteristic	S	REV.	C01	

# 2. ELECTRICAL CHARACTERISTICS

N0.	ITEM	PERFORMANCE	TEST METHODS
2.0	Standard Conditions		Unless otherwise specified, all tests are made under environmental conditions as given below: Temperature: 5~35°C Relative humidity: 45~85 % RH
2.1	Maximum Allowable Voltage	AC : <b>*(1)</b> V <sub>rms</sub> DC : <b>*(1)</b> V	Maximum continuous sine wave(RMS) or DC voltage which may be applied.
2.2	Varistor Voltage	V <sub>0.1mA</sub> : <b>*(1)</b> V	Voltage across the varistor measured at $C_{mA}$ DC.
2.3	Varistor Voltage Temperature Coefficient	0 ~ –0.05 %/°C	<u>VemAaleste – VemAaleste</u> X <u>1</u> VemAaleste – X <u>60</u> × 100
2.4	Max. Clamping Voltage	*(1) Vat 1 A	Peak voltage across the varistor with a specified peak impulse current of 8x20µs waveform.
2.5	Withstanding Surge Current	* <b>(1)</b> A	The max. current within the varistor voltage change of less than $\pm 10\%$ when one impulse current (8x20µs) applied.
2.6	Energy	* <b>(1)</b> Joule	The max. energy absorbed with a varistor voltage change of less than $\pm 10\%$ when one impulse (10x1000 $\mu$ s) is applied.
2.7	Response time	T <sub>rise</sub> < 1 nS	
2.8	Leakage current	Ι <sub>νν</sub> <50μΑ	at V1mA×80%
	_	Ι <sub>νν</sub> <200μΑ	at V1mA×80% (After Reliability Test)
2.9	Capacitance	* <b>(1)</b> pF.	Capacitance shall be measured at 1 kHz±10%,1 Vrms max.(1 MHz below 100 pF) 0V bias and $20\pm2^{\circ}C$

#### \*(1) See table 2.1 Electrical Characteristics

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SUBJECT		<b>Electrical Characteristics</b>		REV.	C01	

#### Table 2.1 Electrical Characteristics

Part Number	Maximum Allowable Voltage		Varistor Voltage	Max. Clamping Voltage	Withstanding Surge Current		Capacitance
Symbol	AC	DC	V(1mA)	V <sub>(*2)</sub>	A <sub>(* 3)</sub>	J	PF <sub>(*1)</sub> (kHz)
1812M180KRR	11	14	18 (15.3~20.7 )	< 30	500	>1.4	3500
1812M240KRR	14	18	24( 21.6~26.4 )	< 38	500	>1.7	2000
1812M560KRR	35	45	56( 50.4~61.6 )	<90	500	>4.2	1000
1812M680KRR	40	56	68( 61.2~74.8 )	<110	500	>4.8	700

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Ceramate	TYPE	SMV1812M□□□KRR	MODEL		PAGE	4/9
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SUBJECT		Reliability		REV.	C01	

# **3. ENVIRONMENTAL CHARACTERISTICS**

No.	Characteristic		Test m	ethod and descriptior	ı				
3.1	High Temperature Storage	The specimen shall be subjected to $150 \pm 2^{\circ}$ C for $1000 \pm 12$ hours in a thermostatic bath vithout load and then stored at room temperature and humidity for 1 to 2 hours. The change of varistor voltage shall be within 10%.							
		The temperature cycle of specified temperature shall be repeated five times	Step 1	Temperature -40±3℃	Period 30Min±3				
3.2	P Temperature Cycle	and then stored at room temperature and humidity for one or two hours. The	2	Room Temperature	1~2 hours				
		change of varistor voltage shall be within	3	<b>125±2</b> ℃	30Min±3				
		10% and mechanical damage shall be	4	Room Temperature	1~2 hours				
3.3	High Temperature Load	After being continuously applied the maxim hours, the specimen shall be stored at roo hours, the change of varistor voltage shall	m temp	erature and humidity fo					
3.4	Damp Heat Load/ Humidity Load	The specimen should be subjected to 40 ± maximum allowable voltage applied for 10 and humidity for one or two hours. The cha	00 hour	s, then stored at room	temperature				
3.5	Low Temperature Storage	The enaloge of validation voltage shall be writing 10.0. The specimen should be subjected to -40 $\pm 2^{\circ}$ C, without load for 500 hours and then tored at room temperature for one or two hours. The change of varistor voltage shall be within 10%							

#### **4. TECHNICAL TERM**

No	. Item	Specifications	Description
4.	1 Operating Temperature Range	-40°C to +85°C	Operating temperature range without derating.
4.2	2 Storage Temperature Range	-40℃ to +125℃	Storage temperature range without voltage applied.
<b>4</b> . wDat	Transient Response	< 50 ns	Time lag between application of surge and varistor's "turn-on" conduction action.

CERAMAT	εT	YPE	SMV181	2M□□□KRR	MODEL		PAGE	5/9
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SUBJECT			Sol	dering		REV.	C01	
5.1 Recommende	_	C 2.8~3.6	D 1.3~2.2 Jnit : mm)		▲ ► B		-	

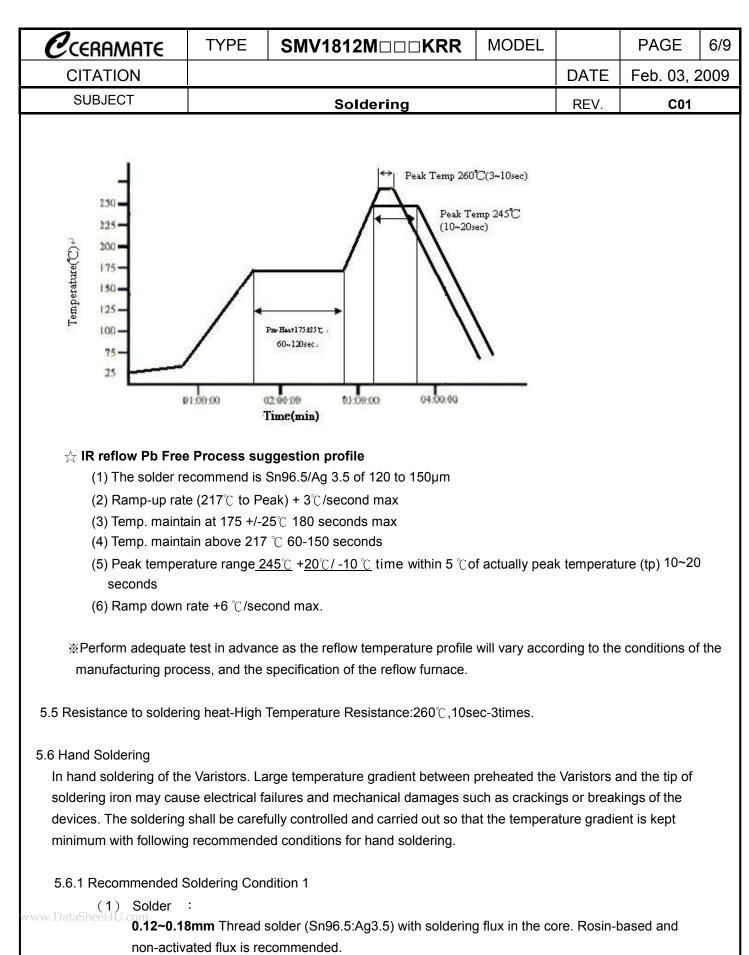
5.2 The SIR test of the solder paste shall be done (Based on JIS-Z-3284)

#### 5.3 Steel plate and foot distance printing

Foot distance printing (mm)	Steel Plate thickness (mm)			
> 0.65mm	0.18mm			
0.65mm~0.5mm	0.15mm			
0.50mm~0.40mm	0.12mm			
<=0.40 mm	0.10mm			

5.4 IR Soldering

Rapid heating, partial heating or rapid cooling will easily cause defect of the component. So preheating and gradual cooling process is suggested. IR soldering has the highest yields due to controlled heating rates and solder liquids times. Make sure that the element is not 2.4 The IR reflow and temperature of Soldering for Pb Free subjected to a thermal gradient steeper than 4 degrees per second. 2 degrees per second is the ideal gradient. During the soldering process, pre- heating to within 100 degrees of the solders peak temperature is essential to minimize thermal shock.



(2) Preheating

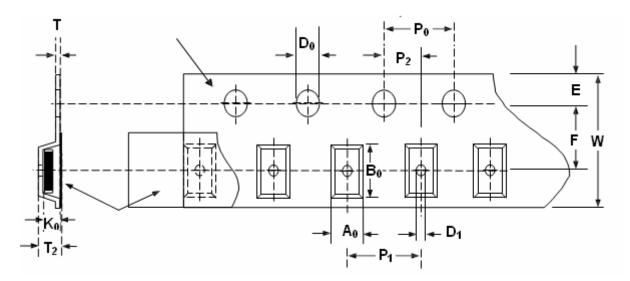
The Varistors shall be preheated so that Temperature Gradient between the devices and the tip of soldering iron is  $150^{\circ}$ C or below.

CITATION SUBJECT (3) Soldering In Rated Pow		Soldering		DATE	Feb. 03, 2	2009
(3) Soldering I		Soldering				
				REV.	C01	
Temperatu in advance (4) Cooling	wer of 20w m ure of solderi e on the sold	nax with 3mm soldering tip in diame ng iron tip 380℃max,3-5sec ( The r ering tip.) ristors shall be cooled gradually at r	equired amou			ted
5 6 2 Recommended S	oldering Cor	ndition 2 (Without preheating)				
	•	directly touch to ceramic dielectrics				
(2) Solder iron	•	ully preheated before soldering whi		on tip to the	e external	
5.7 Post Soldering Clean	ing					
electrical charac mounted on the affected by the a 5.7.2. When an ultraso	teristic and the board. It shat applied cleaning or preventing ed by the ultr by 29MHz mat Power 20w/l	is applied to the mounted Varistor failures or damages of the devices rasonic waves.	tance)of the V ic and the relia s on PC Boar	aristors wh ability of the ds. Followi	ich have beer e devices are ing conditions	n not s are

CERAMATE	TYPE	SMV1812M□□□KRR	MODEL		PAGE	8/9
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SUBJECT	Pa	ckaging Specificati	ion	REV.	C01	

### 6. PACKAGING SPECIFICATION

- 6.1 Carrier tape and transparent cover tape should be heat-sealed to carry the products, and the reel should be used to reel the carrier tape.
- 6.2 The adhesion of the heat-sealed cover tape shall be 40 +20/ -15grams.
- 6.3 Both the head and the end portion of the taping shall be empty for reel package and SMT auto-pickup machine. And a normal paper tape shall be connected in the head of taping for the operator to handle



Symbo	A0 ±0.10	B0 ±0.10	K0 ±0.10	T ±0.05	T2 ±0.05	D0 +0.10 -0.00	D1 ±0.05	P1 ±0.10	P2 ±0.05	P0 ±0.05	W ±0.20	E ±0.10	F ±0.05
mm	3.66	4.95	1.74	0.25	1.99	1.50	1.50	8.00	2.00	4.00	12.00	1.75	5.50

CERAMA	TE 1	YPE	SMV181	2M 🗆 🗆 🗆	KRR	MODEL		PAGE	9/9
CITATION							DATE	Feb. 03,	2009
SUBJECT	REV.	C01							
			E	D D	В		. W1		
Symbol	A	В	А С	D	E	• w	<b>W</b> <sub>1</sub>		
Unit(mm)	178±1	60±0.5	13.5±0.1	21±0.2	2.0±0.5	13.6±0.2	1.5±0.1	5	