

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	
I _S	Diode Continuous Forward Current	T _A =25°C	2	A
I _D	Continuous Drain Current	T _A =25°C	2.7	A
		T _A =70°C	2.1	
I _{DM} ^a	Pulsed Drain Current	T _A =25°C	10.8	A
P _D	Maximum Power Dissipation	T _A =25°C	2.5	W
		T _A =70°C	1.6	
R _{θJA} ^c	Thermal Resistance-Junction to Ambient	t ≤ 10s	50	°C/W
		Steady State	95	°C/W
I _{AS} ^b	Avalanche Current, Single pulse	L=0.5mH	5	A
E _{AS} ^b	Avalanche Energy, Single pulse	L=0.5mH	6.25	mJ

Note a : Pulse width limited by max. junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J = 25^\circ\text{C}$).

Note c : Surface mounted on 1in^2 pad area.

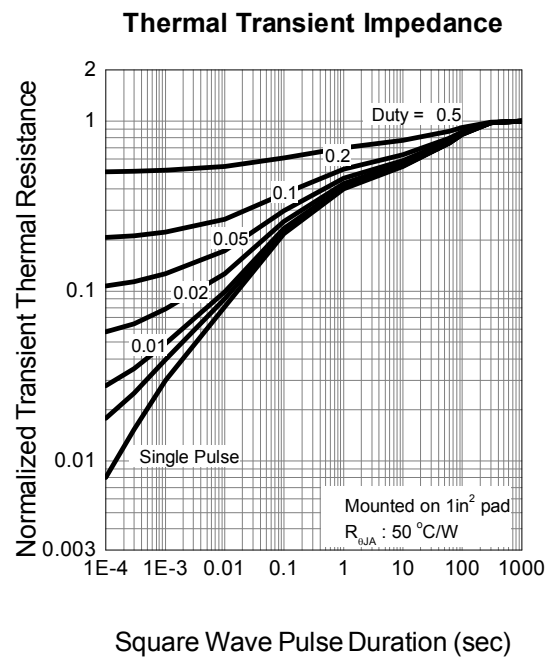
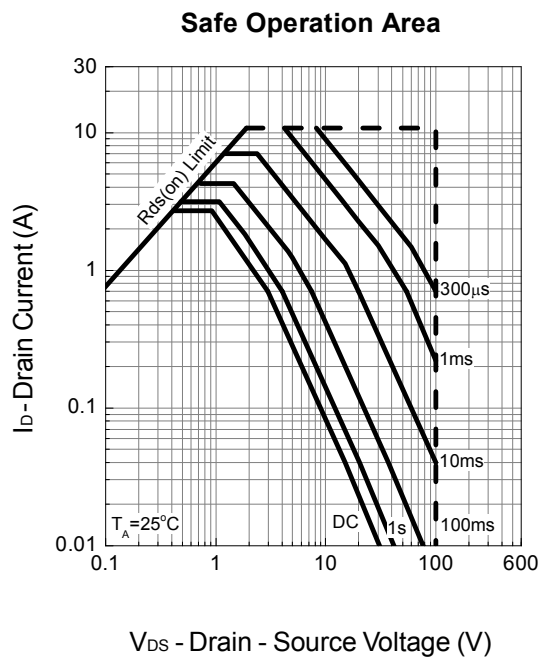
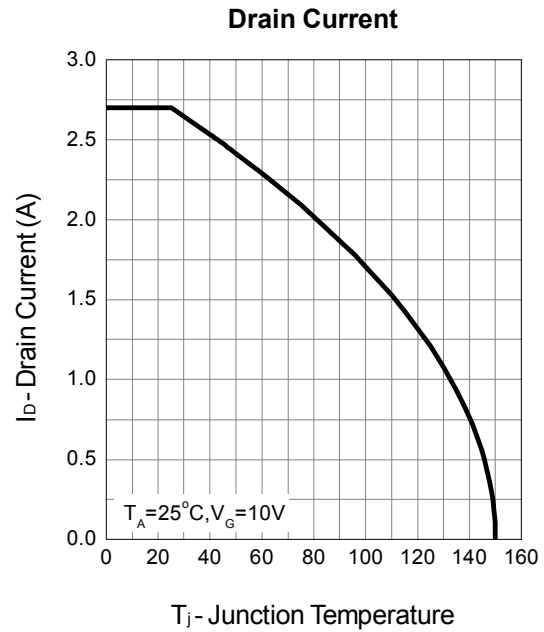
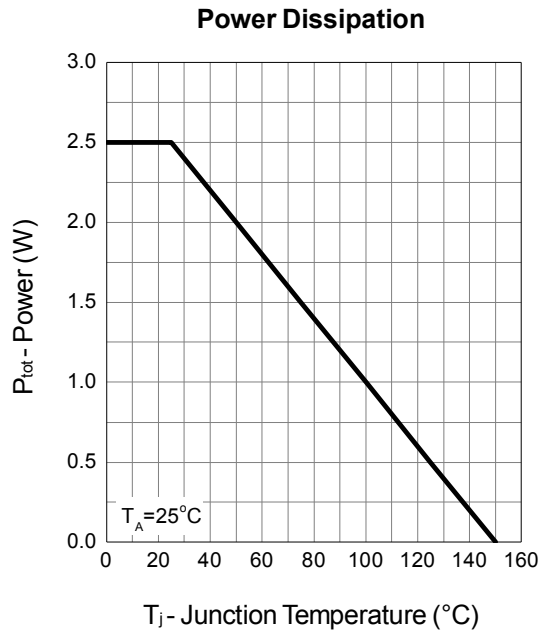
Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V T _J =85°C	-	-	1 30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1	2	3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
R _{DS(ON)} ^d	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =2.5A V _{GS} =4.5V, I _{DS} =2A	-	130 135	156 176	mΩ
Diode Characteristics						
V _{SD} ^d	Diode Forward Voltage	I _{SD} =2A, V _{GS} =0V	-	0.8	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =2.5A, dI _{SD} /dt=100A/μs	-	24	-	ns
Q _{rr}	Reverse Recovery Charge		-	31	-	nC
Dynamic Characteristics ^e						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	2.5	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, Frequency=1.0MHz	-	440	570	pF
C _{oss}	Output Capacitance		-	30	-	
C _{rss}	Reverse Transfer Capacitance		-	16	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	9	17	ns
t _r	Turn-on Rise Time		-	7	13	
t _{d(OFF)}	Turn-off Delay Time		-	18	33	
t _f	Turn-off Fall Time		-	4	8	
Gate Charge Characteristics ^e						
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} =4.5V, I _{DS} =2.5A	-	4.8	-	nC
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} =10V, I _{DS} =2.5A	-	10	14	
Q _{gs}	Gate-Source Charge		-	1.8	-	
Q _{gd}	Gate-Drain Charge		-	1.9	-	

Note d : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

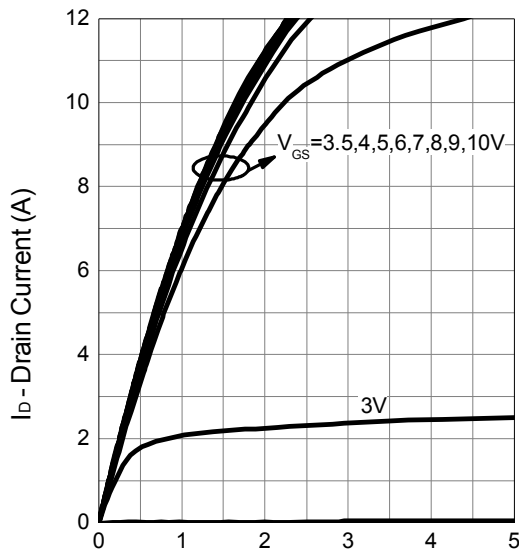
Note e : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics



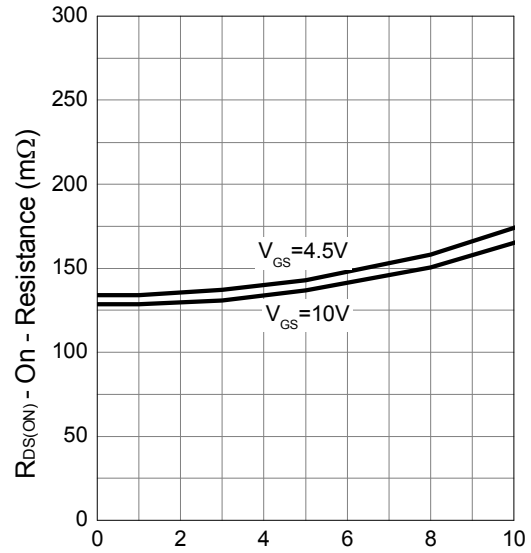
Typical Operating Characteristics (Cont.)

Output Characteristics



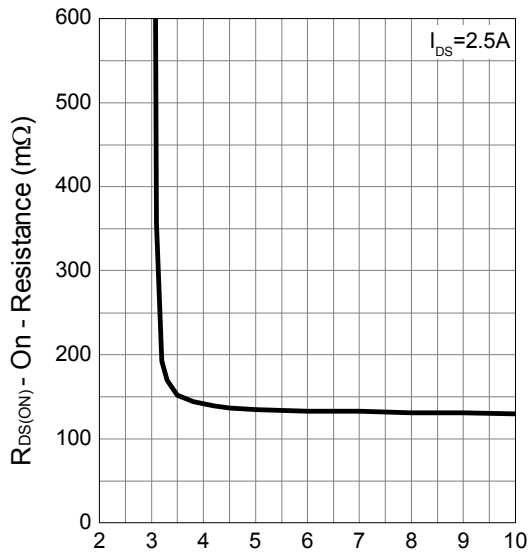
V_{DS} - Drain - Source Voltage (V)

Drain-Source On Resistance



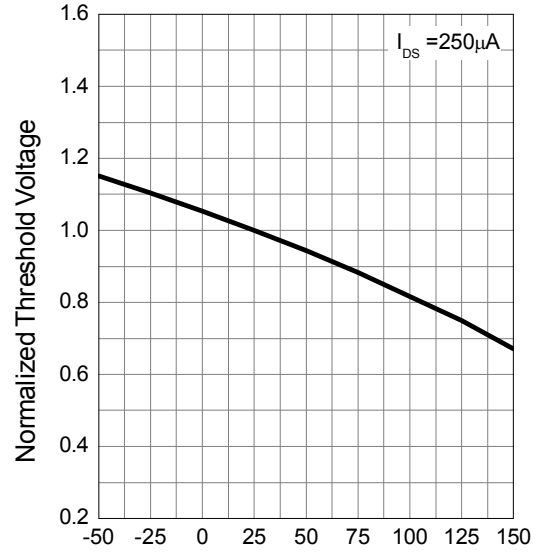
I_D - Drain Current (A)

Gate-Source On Resistance



V_{GS} - Gate - Source Voltage (V)

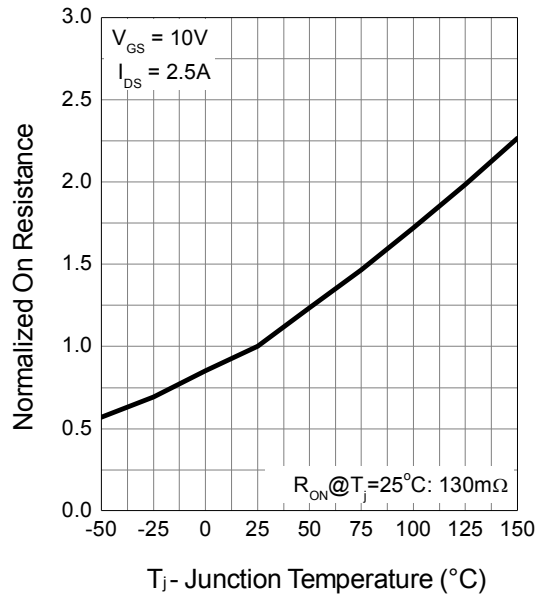
Gate Threshold Voltage



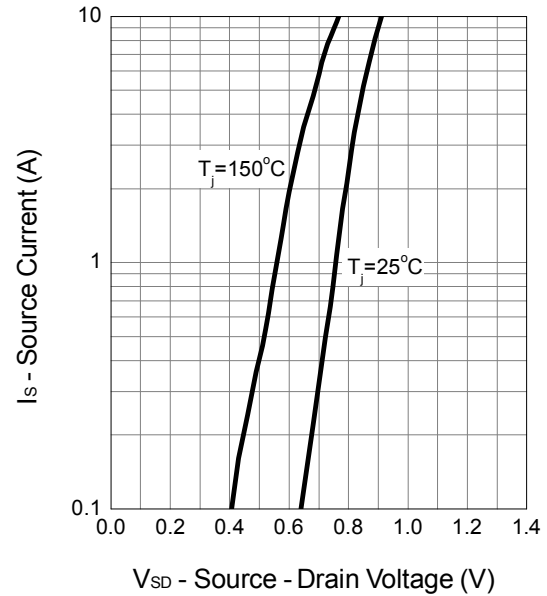
T_J - Junction Temperature ($^{\circ}C$)

Typical Operating Characteristics (Cont.)

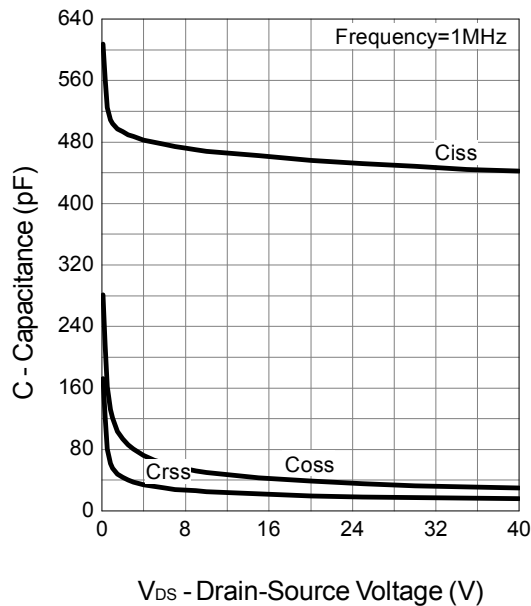
Drain-Source On Resistance



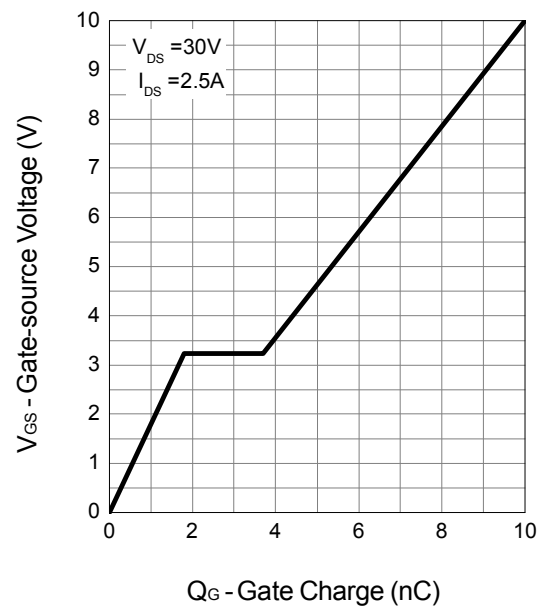
Source-Drain Diode Forward



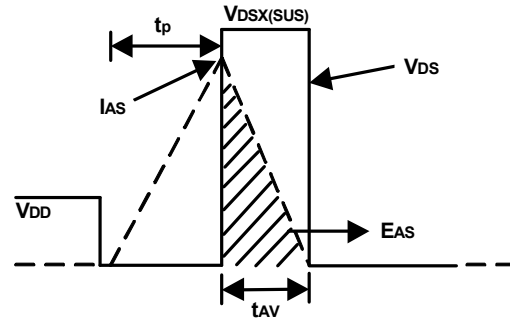
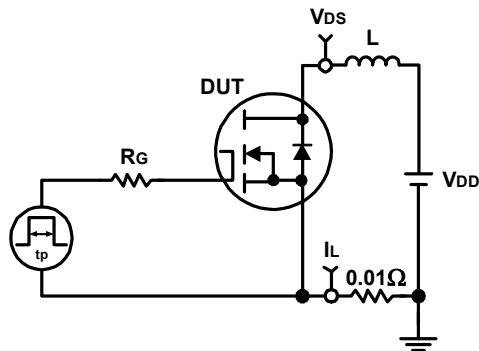
Capacitance



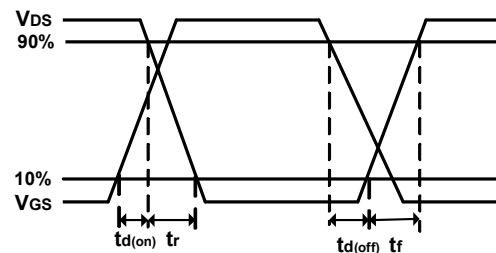
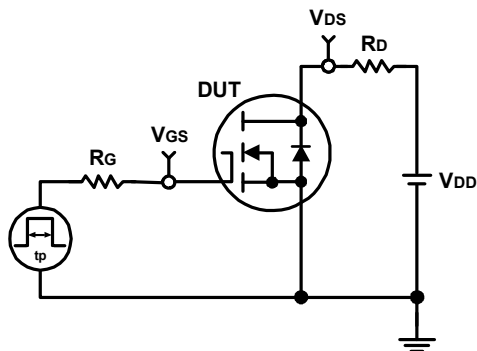
Gate Charge



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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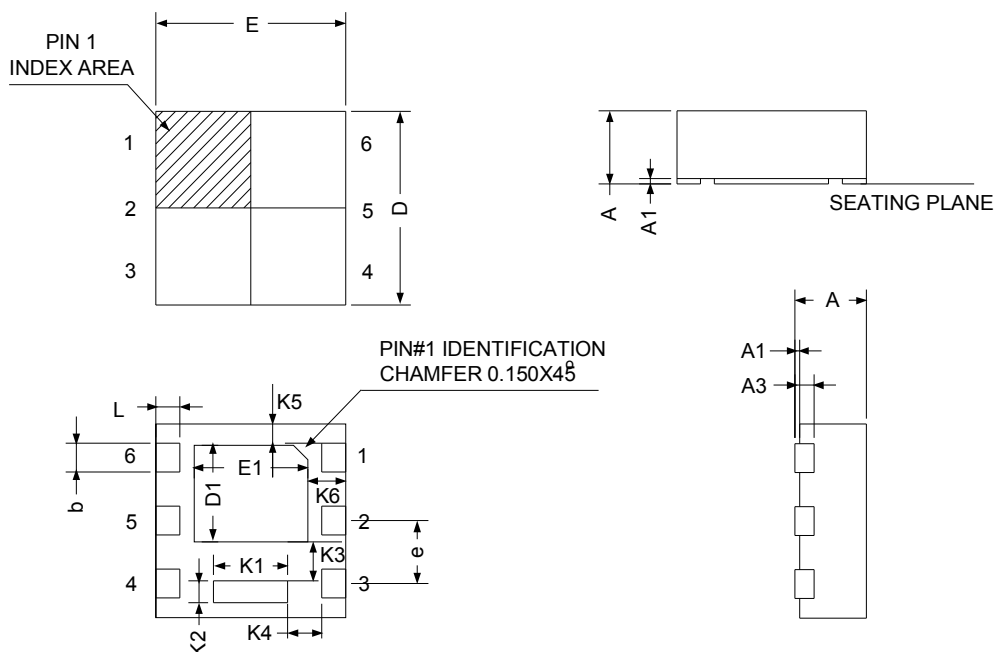
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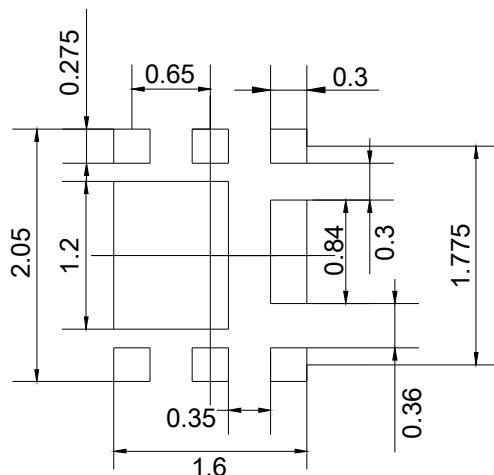
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Package Information



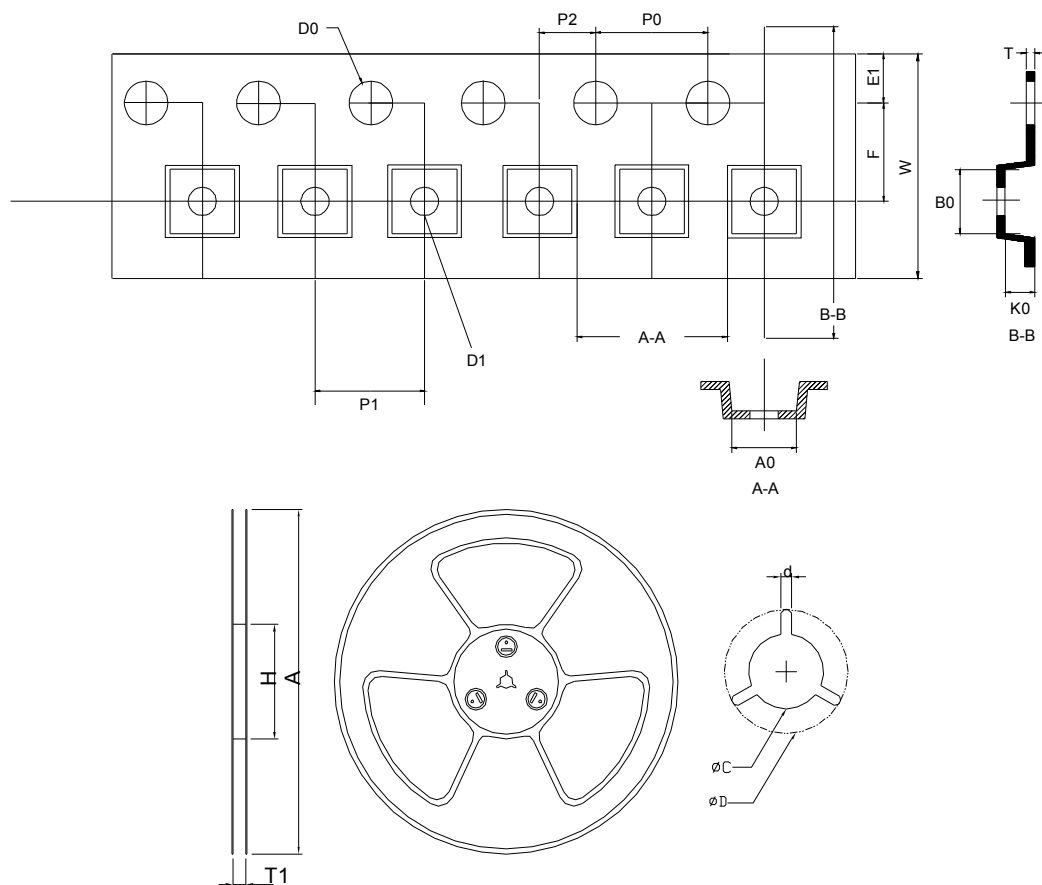
SYMBOL	DFN2x2A-6_EP			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	0.80	0.028	0.031
A1	0.00	0.05	0.000	0.002
A3	0.200 REF		0.008 REF	
b	0.25	0.35	0.010	0.014
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
D1	0.90	1.10	0.035	0.043
E1	0.90	1.10	0.035	0.043
e	0.65 BSC		0.026 BSC	
L	0.20	0.30	0.008	0.012
K1	0.65	0.85	0.026	0.033
K2	0.20	-	0.008	-
K3	0.20	-	0.008	-
K4	0.32	-	0.013	-
K5	0.20	0.26	0.008	0.010
K6	0.45	0.55	0.018	0.022

RECOMMENDED LAND PATTERN



UNIT: mm

Carrier Tape & Reel Dimensions

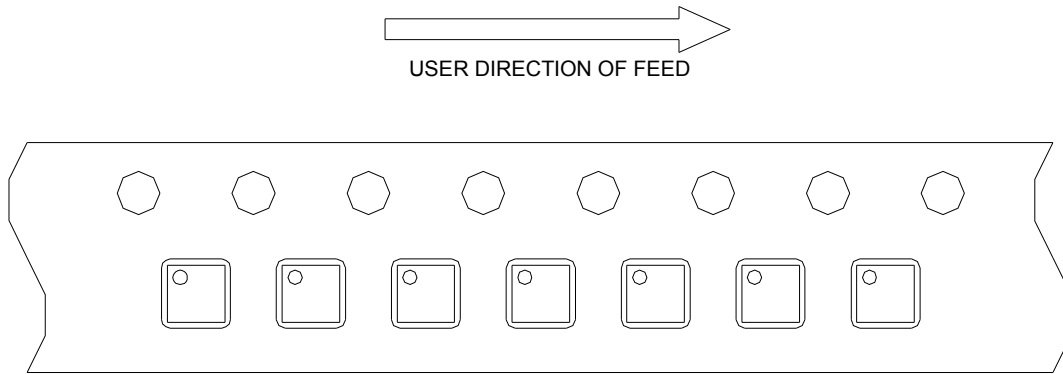


Application	A	H	T1	C	d	D	W	E1	F
DFN2x2A -6_EP	178.0±2.00	60.0±0.50	9.0±2.00	13.0+0.50 -0.20	2.0±0.20	21.0±0.50	8.0+0.30 -0.10	1.75±0.10	3.5±0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0±0.10	4.0±0.10	2.0±0.05	1.5+0.10 -0.00	1.0 MIN.	0.3±0.05	2.3±0.10	2.3±0.10	1.05±0.10

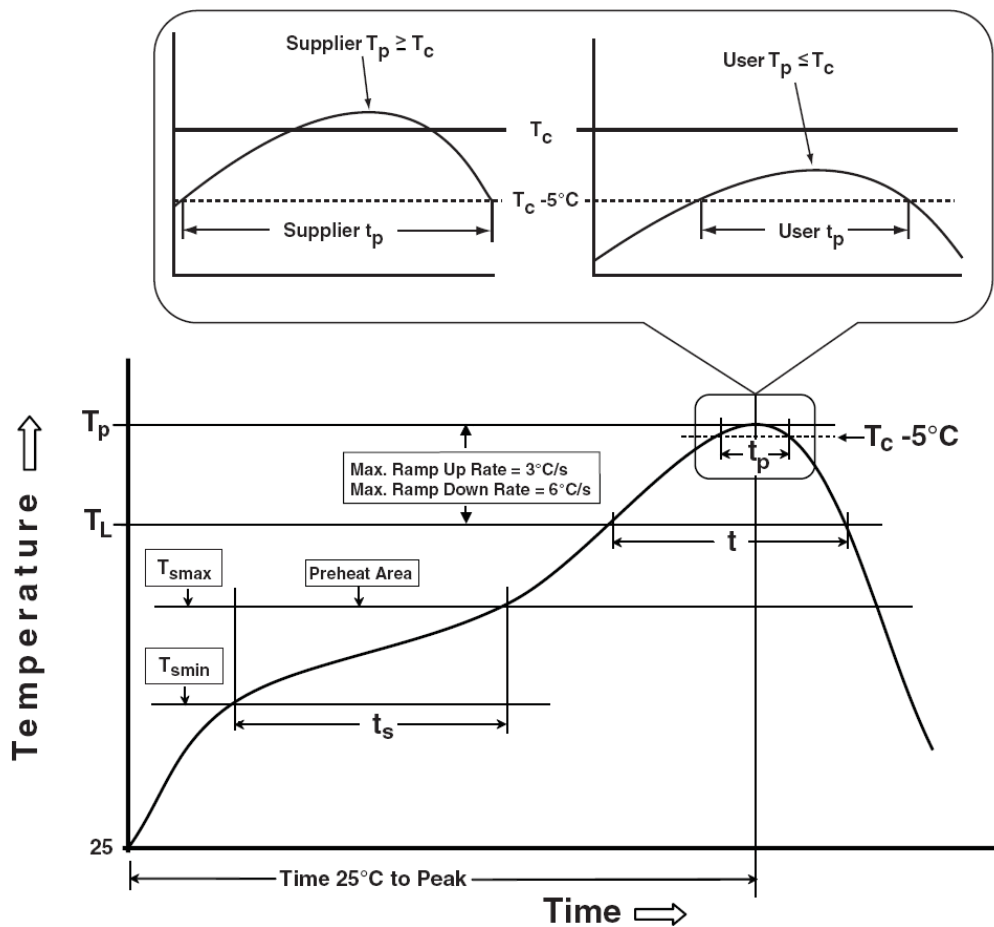
(mm)

Taping Direction Information

DFN2x2A-6_EP



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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