# SM1A57NHG



N-Channel Enhancement Mode MOSFET

**Pin Description** 

TO-263-2

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# **Features**

- 100V/120A
  - $R_{DS(ON)}$ =4.8m $\Omega$ (max.)@V<sub>GS</sub>=10V
- 100% UIS + R<sub>g</sub> Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- Moisture Sensitivity Level MSL1 (per JEDEC J-STD-020D)

# Applications

- High Efficiency Synchronous Rectification in SMPS.
- Hard Switched and High Frequency Circuits.



# **Ordering and Marking Information**



Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the leadfree requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.



#### Absolute Maximum Ratings (T<sub>A</sub>=25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit		
Common	Ratings				
V <sub>DSS</sub>	Drain-Source Voltage	100	V		
$V_{GSS}$	Gate-Source Voltage		±20	v	
TJ	Maximum Junction Temperature		150	- °C	
T <sub>STG</sub>	Storage Temperature Range		-55 to 150		
ls	Diode Continuous Forward Current	T <sub>c</sub> =25°C	70	A	
	Continuous Drain Current	T <sub>c</sub> =25°C	120 <sup>a</sup>		
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> =100°C	86		
I <sub>DM</sub> <sup>b</sup>	Pulsed Drain Current	T <sub>c</sub> =25°C	400		
P	Marian Barrow Disatesting	T <sub>c</sub> =25°C	192	- w	
$P_{D}$	Maximum Power Dissipation	T <sub>c</sub> =100°C	76		
$R_{_{ ext{ heta}JC}}$	Thermal Resistance-Junction to Case		0.65	°C/W	
I	Continuous Drain Current	T <sub>A</sub> =25°C	16.1		
I <sub>D</sub>		T <sub>A</sub> =70°C	12.9	— A	
P <sub>D</sub>	Maximum Power Dissipation	T <sub>A</sub> =25°C	2.5	10/	
		T <sub>A</sub> =70°C	1.6	— W	
$R_{_{\theta JA}}{}^{d}$	Thermal Resistance-Junction to Ambient		50	°C/W	
I <sub>AS</sub> <sup>c</sup>	Avalanche Current, Single pulse	L=0.5mH	42	A	
E <sub>AS</sub> <sup>c</sup>	Avalanche Energy, Single pulse	L=0.5mH	441	mJ	

Note a : Calculated continuous current based on maximum allowable junction temperature. Bonding wire limitation current is 120A.

Note b : Pulse width limited by max. junction temperature.

Note c : UIS tested and pulse width limited by maximum junction temperature  $150^{\circ}$ C (initial temperature  $T_i=25^{\circ}$ C).

Note d : Surface Mounted on 1in<sup>2</sup> pad area.

#### Electrical Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Static Ch	aracteristics					
$BV_{DSS}$	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	100	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	-	-	1	μΑ
		T <sub>J</sub> =85°C	-	-	30	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	2	3	4	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
R <sub>DS(ON)</sub> <sup>e</sup>	Drain-Source On-state Resistance	V <sub>GS</sub> =10V, I <sub>DS</sub> =40A	-	4	4.8	mΩ
Diode Ch	aracteristics					
$V_{\text{SD}}^{e}$	Diode Forward Voltage	I <sub>SD</sub> =40A, V <sub>GS</sub> =0V	-	0.8	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>sp</sub> =40A, dI <sub>sp</sub> /dt=100A/μs	-	65	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$I_{SD}$ - 40A, $U_{SD}$ / $U_{C}$ - 100A/ $\mu$ S	-	135	-	nC
Dynamic	Characteristics <sup>f</sup>					
$R_{G}$	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	2	-	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V,	-	4600	6000	
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =50V,	-	720	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	Frequency=1.0MHz	-	50	-	
t <sub>d(ON)</sub>	Turn-on Delay Time		-	30	54	
t <sub>r</sub>	Turn-on Rise Time	$V_{DD}$ =30V, R <sub>L</sub> =30 $\Omega$ ,	-	16	29	
$t_{d(OFF)}$	Turn-off Delay Time	—I <sub>DS</sub> =1A, V <sub>GEN</sub> =10V, R <sub>G</sub> =6Ω	-	74	134	ns
t <sub>f</sub>	Turn-off Fall Time		-	118	213	
Gate Cha	rge Characteristics <sup>f</sup>					
Qg	Total Gate Charge		-	77	108	
$Q_{gs}$	Gate-Source Charge	──V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, ──I <sub>DS</sub> =40A	-	20	-	nC
$Q_{gd}$	Gate-Drain Charge		-	18	-	

Note e : Pulse test ; pulse width $\leq$ 300µs, duty cycle $\leq$ 2%.

Note f : Guaranteed by design, not subject to production testing.



## **Typical Operating Characteristics**





**Drain Current** 

T<sub>j</sub> - Junction Temperature (°C)



**Thermal Transient Impedance** 



Square Wave Pulse Duration (sec)



## Typical Operating Characteristics(Cont.)



#### **Output Characteristics**

**Drain-Source On Resistance** 



I<sub>D</sub> - Drain Current (A)





# Typical Operating Characteristics(Cont.)





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Source-Drain Diode Forward

V<sub>sD</sub> - Source - Drain Voltage (V)



Capacitance

**Gate Charge** 



# Typical Operating Characteristics(Cont.)



#### V<sub>gs</sub> - Gate-Source Voltage (V)

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# **Avalanche Test Circuit and Waveforms**



# Switching Time Test Circuit and Waveforms





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# **Classification Profile**





## **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak Temperature min $(T_{smin})$ Temperature max $(T_{smax})$ Time $(T_{smin}$ to $T_{smax})$ $(t_s)$	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds	
Average ramp-up rate $(T_{smax}$ to $T_{P})$	3 °C/second max.	3°C/second max.	
Liquidous temperature $(T_L)$ Time at liquidous $(t_L)$	183 °C 60-150 seconds	217 °C 60-150 seconds	
Peak package body Temperature $(T_p)^*$	See Classification Temp in table 1	See Classification Temp in table 2	
Time $(t_P)^{**}$ within 5°C of the specified classification temperature $(T_c)$	20** seconds	30** seconds	
Average ramp-down rate $(T_p \text{ to } T_{smax})$	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	

\* Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum. \*\* Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process - Classification Temperatures (Tc)

Package Thickness	Volume mm <sup>³</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>³</sup> >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## **Reliability Test Program**

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ Tjmax
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ Tjmax
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
ТСТ	JESD-22, A104	500 Cycles, -65°C~150°C

# **Customer Service**

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