

# SEMITRANS® 10

### **IGBT4** Modules

### SKM1400GAR12P4

### Features\*

- · Symmetrical current sharing
- Low-inductive module design
- High mechanical robustness
- UL recognized, file no. E63532

### **Typical Applications**

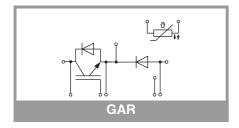
- · Brake chopper
- Windturbines

#### **Remarks**

Recommended  $T_{jop} = -40 \dots +150^{\circ}C$ 

Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
IGBT	•						
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		1200	V			
Ic	T <sub>i</sub> = 175 °C	T <sub>c</sub> = 25 °C	2165	Α			
	- 1 j = 175 C	T <sub>c</sub> = 100 °C	1453	Α			
I <sub>Cnom</sub>			1400	Α			
I <sub>CRM</sub>			2800	Α			
$V_{GES}$			-20 20	V			
t <sub>psc</sub>	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T <sub>j</sub> = 150 °C	10	μs			
Tj			-40 175	°C			
Inverse d	iode			•			
V <sub>RRM</sub>	T <sub>j</sub> = 25 °C		1200	V			
I <sub>F</sub>	T <sub>i</sub> = 175 °C	T <sub>c</sub> = 25 °C	1849	Α			
	1 <sub>j</sub> = 1/5 °C	T <sub>c</sub> = 100 °C	1181	Α			
I <sub>FRM</sub>			2800	Α			
I <sub>FSM</sub>	$t_p = 10 \text{ ms, sin } 18$	80°, T <sub>j</sub> = 25 °C	7296	Α			
Tj			-40 175	°C			
Freewhee	eling diode						
$V_{RRM}$	T <sub>j</sub> = 25 °C		1200	V			
I <sub>F</sub>	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	1849	Α			
		T <sub>c</sub> = 100 °C	1181	Α			
I <sub>FRM</sub>			2800	Α			
I <sub>FSM</sub>	$t_p = 10 \text{ ms, sin } 18$	30°, T <sub>j</sub> = 25 °C	7296	Α			
Tj			-40 175	°C			
Module							
T <sub>stg</sub>			-40 150	°C			
V <sub>isol</sub>	AC sinus 50 Hz,	t = 1 min	4000	V			

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
IGBT						•	
V <sub>CE(sat)</sub>	I <sub>C</sub> = 1400 A	T <sub>j</sub> = 25 °C		1.75	2.07	V	
	V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 150 °C		2.18	2.44	V	
$V_{CE0}$	chiplevel	T <sub>j</sub> = 25 °C		0.80	0.90	V	
		T <sub>j</sub> = 150 °C		0.70	0.80	V	
r <sub>CE</sub>	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25 °C		0.68	0.83	mΩ	
	chiplevel	T <sub>j</sub> = 150 °C		1.06	1.17	mΩ	
$V_{GE(th)}$	V <sub>GE</sub> =V <sub>CE</sub> , I <sub>C</sub> = 49.2 mA		5.1	5.8	6.4	V	
I <sub>CES</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_j = 25 ^{\circ}\text{C}$				5	mA	
C <sub>ies</sub>	V <sub>CE</sub> = 25 V V <sub>GE</sub> = 0 V	f = 1 MHz		81.6		nF	
Coes		f = 1 MHz		5.28		nF	
C <sub>res</sub>		f = 1 MHz		4.50		nF	
$Q_{G}$	V <sub>GE</sub> = - 8 V+ 15 V			7500		nC	
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			0.6		Ω	





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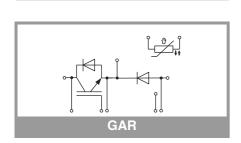
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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
t <sub>d(on)</sub>	V <sub>CC</sub> = 600 V	T <sub>j</sub> = 150 °C		340		ns
t <sub>r</sub>	I <sub>C</sub> = 1400 A	T <sub>j</sub> = 150 °C		125		ns
E <sub>on</sub>	$V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1 \Omega$	T <sub>j</sub> = 150 °C		150		mJ
t <sub>d(off)</sub>	$R_{G \text{ off}} = 1 \Omega$	T <sub>j</sub> = 150 °C		765		ns
t <sub>f</sub>	di/dt <sub>on</sub> = 11 kA/μs	T <sub>j</sub> = 150 °C		180		ns
E <sub>off</sub>	$\begin{array}{l} \text{di/dt}_{\text{off}} = 7 \text{ kA/}\mu\text{s} \\ \text{dv/dt} = 2950 \text{ V/}\mu\text{s} \\ \text{L}_{\text{s}} = 25 \text{ nH} \end{array}$	T <sub>j</sub> = 150 °C		290		mJ
$R_{\text{th(j-c)}}$	per IGBT				0.02	K/W
R <sub>th(c-s)</sub>	per IGBT (λ <sub>grease</sub> =0	.81 W/(m*K))		0.008		K/W
Inverse d			_			_
$V_F = V_{EC}$	I <sub>F</sub> = 1400 A	T <sub>j</sub> = 25 °C		2.07	2.38	V
	V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 150 °C		1.98	2.28	V
V <sub>F0</sub>		T <sub>j</sub> = 25 °C	1	1.30	1.50	V
	chiplevel	T <sub>i</sub> = 150 °C		0.90	1.10	V
r <sub>F</sub>	ahinlaval	T <sub>j</sub> = 25 °C		0.55	0.63	mΩ
	chiplevel	T <sub>j</sub> = 150 °C		0.77	0.84	mΩ
I <sub>RRM</sub>	I <sub>F</sub> = 1400 A	T <sub>j</sub> = 150 °C		1050		Α
Q <sub>rr</sub>	V <sub>GE</sub> = -15 V - di/dt <sub>off</sub> = 11 kA/μs	T <sub>j</sub> = 150 °C		275		μC
E <sub>rr</sub>	$V_R = 600 \text{ V}$	T <sub>j</sub> = 150 °C		118		mJ
R <sub>th(j-c)</sub>	per diode				0.033	K/W
R <sub>th(c-s)</sub>	per diode (λ <sub>grease</sub> =0	).81 W/(m*K))		0.01		K/W
Freewhee	eling diode		1			•
$V_F = V_{EC}$	I <sub>F</sub> = 1400 A	T <sub>j</sub> = 25 °C		2.07	2.38	V
	V <sub>GE</sub> = 0 V level = chiplevel	T <sub>j</sub> = 150 °C		1.98	2.28	V
$V_{F0}$	- chiplevel	T <sub>j</sub> = 25 °C		1.30	1.50	V
	op.ovo.	T <sub>j</sub> = 150 °C		0.90	1.10	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.55	0.63	mΩ
	1 1100 1	T <sub>j</sub> = 150 °C		0.77	0.84	mΩ
I <sub>RRM</sub>	l <sub>F</sub> = 1400 A di/dt <sub>off</sub> = 11 kA/μs	T <sub>j</sub> = 150 °C		1050		A
Q <sub>rr</sub>	$V_{GE} = -15 \text{ V}$	T <sub>j</sub> = 150 °C		275		μC
E <sub>rr</sub>	V <sub>R</sub> = 600 V	T <sub>j</sub> = 150 °C		118		mJ
R <sub>th(j-c)</sub>	per diode				0.033	K/W
R <sub>th(c-s)</sub>	per diode ( $\lambda_{grease}$ =0	).81 W/(m*K))		0.010		K/W
Module			•			
L <sub>CE</sub>				10		nH
R <sub>CC'+EE'</sub>	measured per swith			0.2		mΩ
R <sub>th(c-s)1</sub>	calculated without thermal coupling (λ <sub>grease</sub> =0.81 W/(m*K))			0.004		K/W
$R_{\text{th(c-s)2}}$	including thermal coupling, $T_s$ underneath module $(\lambda_{grease}=0.81 \text{ W/(m}^*\text{K)})$			0.004		K/W
Ms	to heat sink M5		4		6	Nm
Mt		to terminals M8	8		10	Nm
		to terminals M4	1.8		2.1	Nm
W			<u> </u>		1250	g



Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Temperature Sensor							
R <sub>100</sub>	T <sub>c</sub> =100°C (R <sub>25</sub> =5 kΩ)	493 ± 5%		Ω			
B <sub>100/125</sub>	$R_{(T)}=R_{100}exp[B_{100/125}(1/T-1/T_{100})];T[K];$	3550 ±2%		K			

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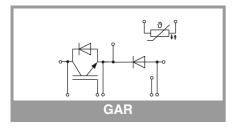
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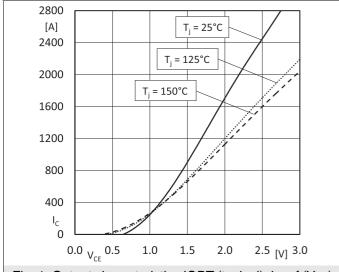


Fig. 1: Output characteristics IGBT (typical);  $I_C = f(V_{CE})$ ; V<sub>GE</sub> = 15V; (chiplevel)

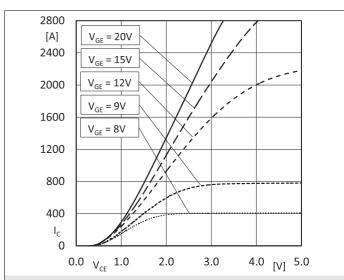


Fig. 2: Output characteristics IGBT (typical);  $I_C = f(V_{CE})$ ;  $T_i = 150 \,^{\circ}\text{C}$ ; (chiplevel)

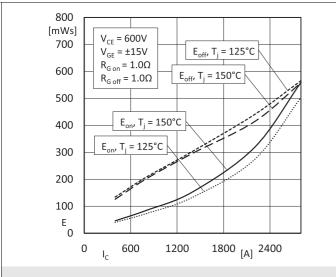


Fig. 3: Switching losses IGBT (typical); E=f(I<sub>C</sub>)

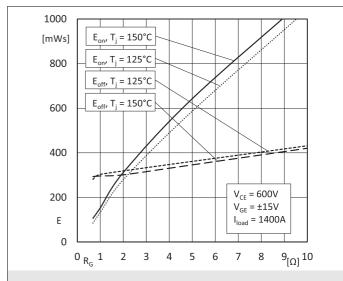
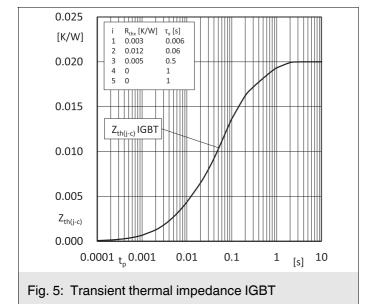
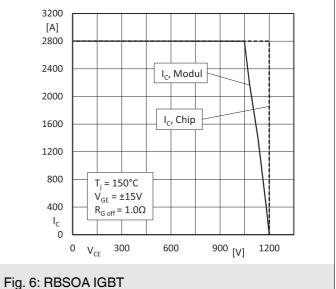
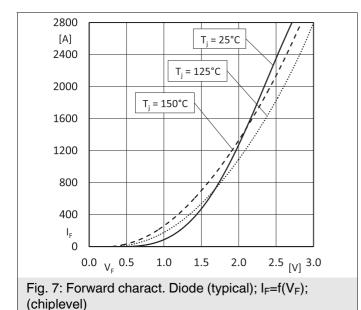
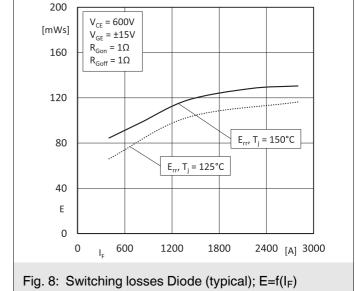


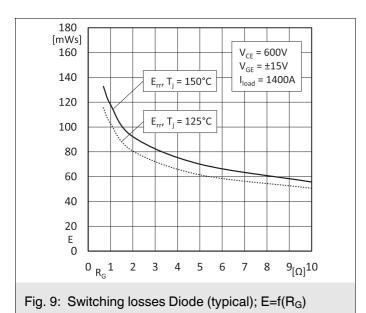
Fig. 4: Switching losses IGBT (typical); E=f(R<sub>G</sub>)

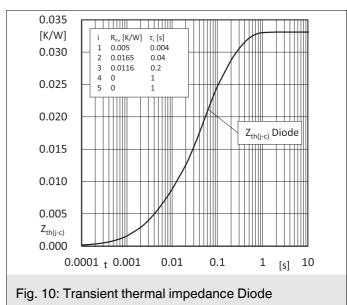


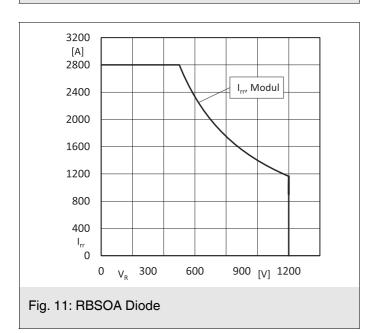


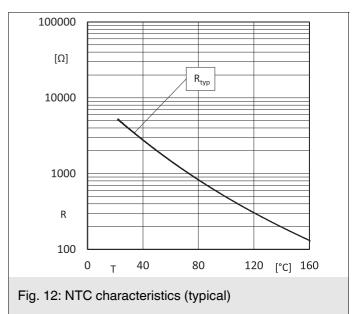


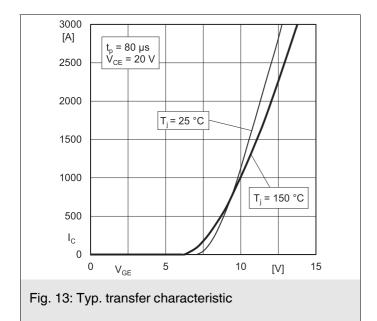












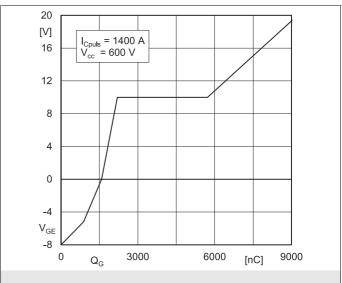
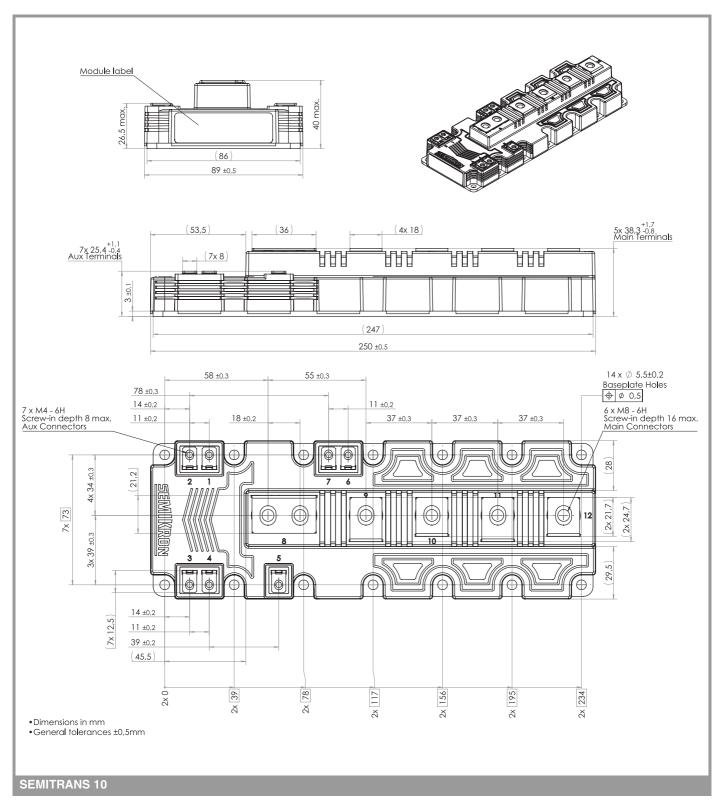
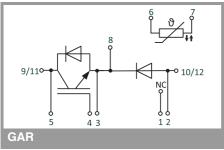


Fig. 14: Typ. gate charge characteristic





This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

#### \*IMPORTANT INFORMATION AND WARNINGS

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