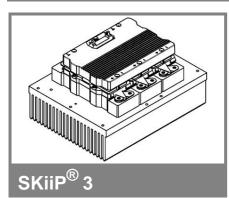
SKiiP 613GD123-3DUL



6-pack-integrated intelligent Power System

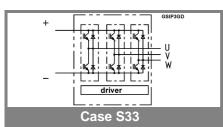
Power section

SKiiP 613GD123-3DUL

Data

Power section features

- SKiiP technology inside
- Trench IGBTs .
- CAL HD diode technology
- Integrated current sensor •
- Integrated temperature sensor •
- Integrated heat sink
- IEC 60721-3-3 (humidity) class . 3K3/IE32 (SKiiP[®] 3 System) IEC 60068-1 (climate) 40/125/56
- UL recognized file no. E63532
- 1) with assembly of suitable MKP capacitor per terminal



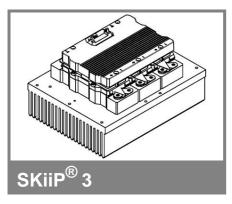
Absolute	e Maximum Ratings	T _s = 25°C unless otherwise specified						
Symbol	Conditions	Values	Units					
IGBT								
V _{CES}		1200	V					
V _{CC} ¹⁾	Operating DC link voltage	900	V					
V _{GES}		± 20	V					
I _C	T _s = 25 (70) °C	600 (450)	А					
Inverse o	Inverse diode							
I _F = - I _C	T _s = 25 (70) °C	470 (350)	А					
I _{FSM}	T _j = 150 °C, t _p = 10 ms; sin	3500	А					
I²t (Diode)	Diode, T _j = 150 °C, 10 ms	61	kA²s					
T _j , (T _{stg})		- 40 + 150 (125)	°C					
V _{isol}	rms, AC, 1 min, main terminals to heat sink	3000	V					
I _{AC-terminal}	per AC terminal, rms, T _s = 70 °C,	400	А					
	T _{terminal} < 115 °C							

Charact	eristics	l _s = 25	T_s = 25 °C unless otherwise specified				
Symbol Conditions		min.	typ.	max.	Units		
IGBT		•					
V _{CEsat}	I_{C} = 300 A, T_{j} = 25 (125) °C; measured at terminal		1,7 (1,9)	2,1	V		
V _{CEO}	T _i = 25 (125) °C; at terminal		0,9 (0,8)	1,1 (1)	v		
r _{CE}	T _i = 25 (125) °C; at terminal		2,6 (3,7)	3,3 (4,4)	mΩ		
I _{CES}	$V_{GE} = 0 V, V_{CE} = V_{CES},$ T _i = 25 (125) °C		1,2 (36)		mA		
E _{on} + E _{off}			110		mJ		
	T _j = 125 °C, V _{CC} = 900 V		195		mJ		
R _{CC+EE} ′	terminal chip, T _i = 25 °C		0,5		mΩ		
L _{CE}	top, bottom		12		nH		
C _{CHC}	per phase, AC-side		1,7		nF		
Inverse	diode						
V _F = V _{EC}	$I_F = 300 \text{ A}, T_j = 25 (125) ^{\circ}\text{C}$ measured at terminal		1,5 (1,5)	1,8	V		
V _{TO}	T _i = 25 (125) °C		0,9 (0,7)	1,1 (0,9)	v		
r _T	T _j = 25 (125) °C T _j = 25 (125) °C		2 (2,7)	2,3 (3)	mΩ		
E _{rr}	$I_{\rm C}$ = 300 A, V _{CC} = 600 V		21		mJ		
	T _j = 125 °C, V _{CC} = 900 V		28		mJ		
Mechan	ical data				•		
M _{dc}	DC terminals, SI Units	6		8	Nm		
M _{ac}	AC terminals, SI Units	13		15	Nm		
w	SKiiP [®] 3 System w/o heat sink		2,4		kg		
w	heat sink		7,5		kg		
Thermal characteristics (PX16 heat sink with fan SKF16B-230-1); "s" reference to heat sink; "r" reference to built-in temperature sensor (acc.IEC 60747-15)							
R _{th(j-s)l}	per IGBT	1		0,059	K/W		

00/4/-1	5)								
R _{th(j-s)I}	per IGB	per IGBT				0,059 K/W			
R _{th(j-s)D}	per diod	per diode				0,115			
Z _{th}	R _i (mK/V	R _i (mK/W) (max. values)				tau _i (s)			
	1	2	3	4	1	2	3	4	
Z _{th(j-r)I}	10,2	28,8	21	0	363	0,18	0,04	1	
Z _{th(j-r)D}	36	36	54	60	30	5	0,25	0,04	
Z _{th(r-a)}	2,1	20	5,5	1,4	210	85	11	0,4	

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SKiiP 613GD123-3DUL



6-pack-integrated intelligent Power System

6-pack integrated gate driver SKiiP 613GD123-3DUL

Data

Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlock of top/bottom switch
- Isolation by transformer
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute	Maximum Ratings	$T_a = 25$ °C unless otherwise specified			
Symbol	Conditions	Values	Units		
V _{S2}	unstabilized 24 V power supply	30	V		
V _i	input signal voltage (high)	15 + 0,3	V		
dv/dt	secondary to primary side	75	kV/µs		
V _{isollO}	input / output (AC, rms, 2 s)	3000	V		
VisoIPD	partial discharge extinction voltage, rms, $Q_{PD} \le 10 \text{ pC}$;	1170	V		
V _{isol12}	output 1 / output 2 (AC, rms, 2 s)	1500	V		
f _{sw}	switching frequency	15	kHz		
f _{out}	output frequency for I _{peak(1)} =I _C	15	kHz		
T _{op} (T _{stg})	operating / storage temperature	- 40 + 85	°C		

Characte	ristics	(T _a			= 25 °C)
Symbol	Conditions	itions min. typ. max.			Units
V _{S2}	supply voltage non stabilized	13	24	30	V
I _{S2}	V _{S2} = 24 V	365+37*f	/kHz+0,001	11*(I _{AC} /A) ²	mA
V _{iT+}	input threshold voltage (High)			12,3	V
V _{iT-}	input threshold voltage (Low)	4,6			V
R _{IN}	input resistance		10		kΩ
C _{IN}	input capacitance		1		nF
t _{d(on)IO}	input-output turn-on propagation time		1,3		μs
t _{d(off)IO}	input-output turn-off propagation time		1,3		μs
t _{pERRRESET}	error memory reset time		9		μs
t _{TD}	top / bottom switch interlock time		3		μs
I _{analogOUT}	max. 5 mA; 8 V corresponds to 15 V supply voltage for external components		600		A
I _{s1out}	max. load current			50	mA
I _{TRIPSC}	over current trip level		750		•
-	(I _{analog} OUT = 10 V)	110	750	400	A °O
T _{tp}	over temperature protection	110	000	120	°C
UDCTRIP	U _{DC} -protection (U _{analog OUT} = 9 V);		900		V
	(option for GB types)				

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