

IGBT module

SK35GD12T4ETE1

- Features*
 Low inductive design
- Press-Fit contact technology
- Rugged mounting due to integrated mounting clamps
- Heat transfer and insulation through direct copper bonded aluminium oxide ceramic (DBC)
- Trench4 IGBT technology
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

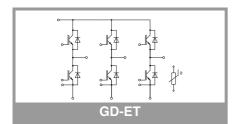
Typical Applications

- · Motor drives
- Servo drives
- · Air conditioning
- Auxiliary Inverters
- UPS

Absolute Maximum Ratings						
Symbol	Conditions		Values	Unit		
IGBT 1	•		•	•		
V_{CES}	T _j = 25 °C		1200	V		
Ic	λ _{paste} =0.8 W/(mK)	T _s = 25 °C	49	Α		
	T _j = 175 °C	T _s = 70 °C	40	Α		
Ic	λ _{paste} =2.5 W/(mK)	T _s = 25 °C	60	Α		
	T _j = 175 °C	T _s = 70 °C	49	Α		
I _{Cnom}			35	Α		
I _{CRM}	$I_{CRM} = 3 \times I_{Cnom}$		105	Α		
V_{GES}			-20 20	V		
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 150 °C	10	μs		
Tj			-40 175	°C		

Absolute Maximum Ratings						
Symbol	Conditions		Values	Unit		
Diode 1						
V_{RRM}	T _j = 25 °C		1200	V		
l _F	λ _{paste} =0.8 W/(mK)	T _s = 25 °C	41	Α		
T _j = 175 °C	T _j = 175 °C	T _s = 70 °C	33	Α		
l _F	λ _{paste} =2.5 W/(mK)	T _s = 25 °C	49	Α		
	T _j = 175 °C	T _s = 70 °C	40	Α		
I _{Fnom}			35	Α		
I _{FRM}	I _{FRM} = 2 x I _{Fnom}		70	Α		
I _{FSM}	10 ms	T _j = 25 °C	170	Α		
sin 180°	sin 180°	T _j = 150 °C	170	Α		
T _i		1	-40 175	°C		

Absolute Maximum Ratings					
Symbol Conditions Values			Unit		
Module					
I _{t(RMS)}	ΔT _{terminal} at PCB joint = 30 K, per pin	30	Α		
T _{stg}		-40 125	°C		
V _{isol}	AC, sinusoidal, t = 1 min	2500	V		





SEMITOP®E1

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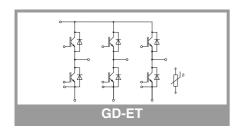
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Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
IGBT 1							
V _{CE(sat)}	I _C = 35 A	T _j = 25 °C		1.85	2.10	V	
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.25	2.45	V	
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V	
	Criipievei	T _j = 150 °C		0.70	0.80	V	
r _{CE}	$V_{GE} = 15 \text{ V}$	T _j = 25 °C		30	34	mΩ	
	chiplevel	T _j = 150 °C		44	47	mΩ	
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_{C} = 1.2$	mA	5	5.8	6.5	V	
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	200 V, $T_j = 25 ^{\circ}\text{C}$			1	mA	
C _{ies}	V 05 V	f = 1 MHz		1.95		nF	
Coes	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		0.155		nF	
C _{res}	THE THE	f = 1 MHz		0.115		nF	
Q_{G}	V _{GE} = -15V +15V	<i>i</i>		258		nC	
R _{Gint}	T _j = 25 °C			0		Ω	
t _{d(on)}	V _{CC} = 600 V	T _j = 150 °C		17		ns	
t _r	$I_C = 35 \text{ A}$ $V_{GF} = +15/-15 \text{ V}$	T _j = 150 °C		30		ns	
Eon	$R_{Gon} = 8 \Omega$	T _j = 150 °C		2.61		mJ	
t _{d(off)}	$R_{G \text{ off}} = 8 \Omega$	T _j = 150 °C		232		ns	
t _f	$di/dt_{on} = 825 A/\mu s$	T _j = 150 °C		69		ns	
E _{off}	$di/dt_{off} = 438 \text{ A/}\mu s$ $dv/dt = 4865 \text{ V/}\mu s$	T _j = 150 °C		2.85		mJ	
R _{th(j-s)}	per IGBT, λ _{paste} =0.	per IGBT, λ _{paste} =0.8 W/(mK)		0.96		K/W	
R _{th(j-s)}	per IGBT, λ _{paste} =2.5 W/(mK)			0.67		K/W	

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Diode 1							
V _F	I _F = 35 A	T _j = 25 °C		2.30	2.62	V	
	chiplevel	T _j = 150 °C		2.29	2.62	V	
V_{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V	
		T _j = 150 °C		0.90	1.10	V	
r _F	chiplevel	T _j = 25 °C		29	32	mΩ	
	Criipievei	T _j = 150 °C		40	43	mΩ	
I _{RRM}	$I_F = 35 A$	T _j = 150 °C		25		Α	
Q _{rr}	di/dt _{off} = 825 A/μs V _{GE} = -15 V	T _j = 150 °C		5.5		μC	
Err	$V_{GE} = -15 \text{ V}$ $V_{CC} = 600 \text{ V}$	T _j = 150 °C		2.27		mJ	
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			1.34		K/W	
R _{th(j-s)}	per Diode, λ _{paste} =2.	.5 W/(mK)		1		K/W	





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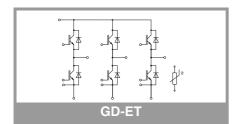
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Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Module						
Ms	to heatsink	1.6		2.3	Nm	
W	weight		25		g	

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Temperature Sensor							
R ₁₀₀	T _r = 100 °C		493 ± 5%		Ω		
B _{100/125}	$R_{(T)}=R_{100}exp[B_{100/125}(1/T-1/T_{100})];T[K];$		3550 ±2%		К		



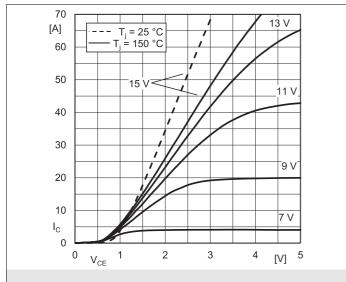


Fig. 1: Typ. IGBT output characteristic, incl. R_{CC'+ EE'}

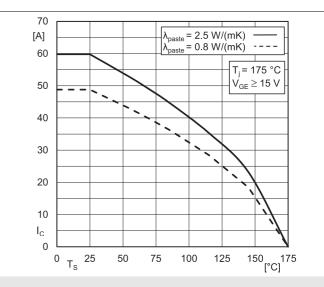


Fig. 2: IGBT rated current vs. temperature I_c=f(T_s)

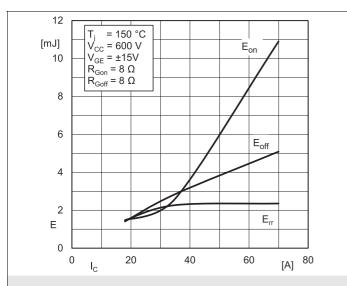


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

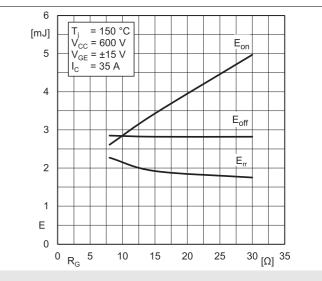


Fig. 4: Typ. turn-on /-off energy = f (R_G)

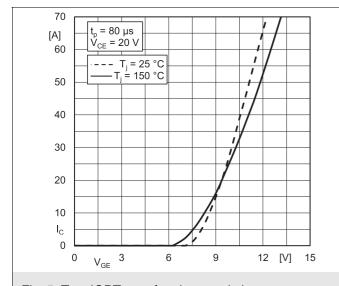


Fig. 5: Typ. IGBT transfer characteristic

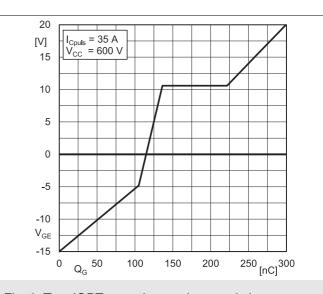


Fig. 6: Typ. IGBT gate charge characteristic

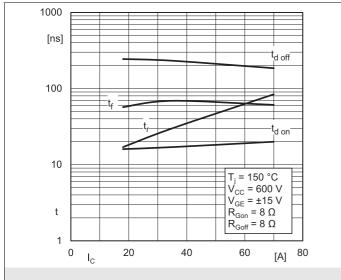


Fig. 7: Typ. switching times = $f(I_C)$

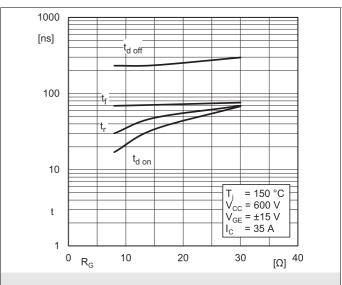


Fig. 8: Typ. switching times = $f(R_G)$

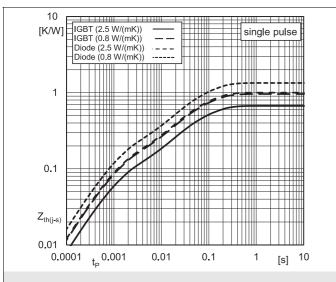


Fig. 9: Typ. transient thermal impedance

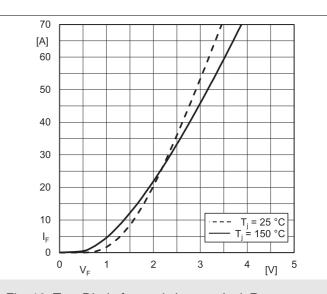


Fig. 10: Typ. Diode forward charact., incl. $R_{CC'+\; EE'}$

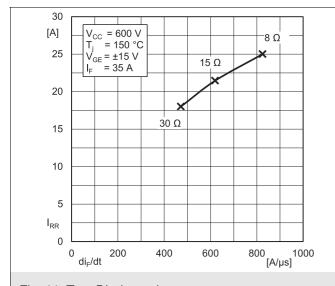


Fig. 11: Typ. Diode peak reverse recovery current

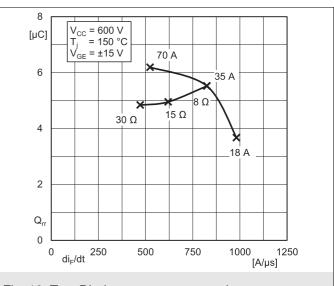
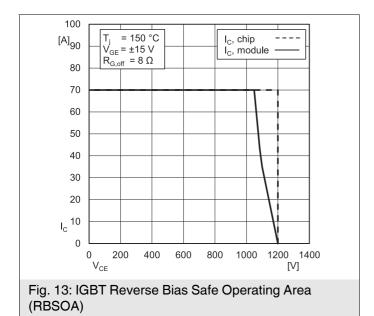
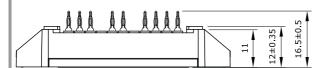
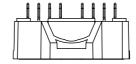


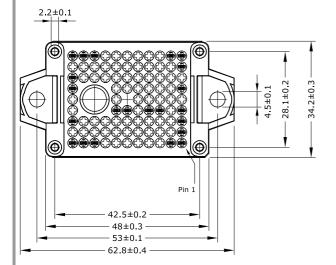
Fig. 12: Typ. Diode reverse recovery charge

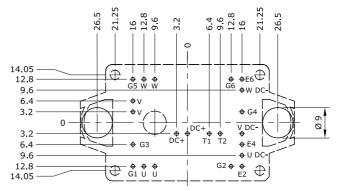




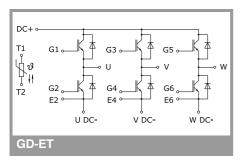


- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern ⊕ 0.025
- Diameters of drill $\not \odot$ 1.15mm
- Copper thickness in hole 25 50 μm
- Hole specification for contacts: refer to SEMITOP E1, E2 mounting instructions





SEMITOP®E1



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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