

# SK 120 GB 12F4 T



SEMITOP® 3

## IGBT module

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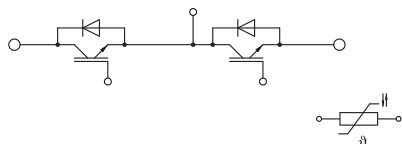
#### Target Data

#### Features

- Compact design
- One screw mounting module
- Optimum heat transfer and isolation through AlN direct copper bonding (DBC)
- Trench4 Fast IGBT technology
- CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized, file no. E 63 532

#### Typical Applications\*

- Switching (not for linear use)
- Inverter
- Switched mode power supplies
- UPS



GB-T

#### Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
Inverter - IGBT				
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		1200	V
I <sub>C</sub>	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 25 °C	174	A
		T <sub>s</sub> = 70 °C	143	A
I <sub>Cnom</sub>			120	A
I <sub>CRM</sub>	I <sub>CRM</sub> = 3 x I <sub>Cnom</sub>		360	A
V <sub>GES</sub>			-20 ... 20	V
t <sub>psc</sub>	V <sub>CC</sub> = 800 V V <sub>GE</sub> ≤ 15 V V <sub>CES</sub> ≤ 1200 V	T <sub>j</sub> = 150 °C	10	μs
T <sub>j</sub>			-40 ... 175	°C
Inverse - Diode				
I <sub>F</sub>	T <sub>j</sub> = 175 °C	T <sub>s</sub> = 25 °C	29	A
		T <sub>s</sub> = 70 °C	24	A
I <sub>Fnom</sub>			15	A
I <sub>FRM</sub>	I <sub>FRM</sub> = 3 x I <sub>Fnom</sub>		45	A
I <sub>FSM</sub>	10 ms, sin 180°, T <sub>j</sub> = 150 °C		65	A
T <sub>j</sub>			-40 ... 175	°C
Module				
I <sub>t(RMS)</sub>	,			A
T <sub>stg</sub>			-40 ... 125	°C
V <sub>isol</sub>	AC, sinusoidal, t = 1 min		2500	V

#### Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
<b>Inverter - IGBT</b>					
$V_{CE(sat)}$	$I_C = 120\text{ A}$ $V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25\text{ °C}$	2.05	2.40	V
		$T_j = 150\text{ °C}$	2.50	2.85	V
$V_{CE0}$	chipelevel	$T_j = 25\text{ °C}$	0.80	0.90	V
		$T_j = 150\text{ °C}$	0.70	0.80	V
$r_{CE}$	$V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25\text{ °C}$	10	13	mΩ
		$T_j = 150\text{ °C}$	15	17	mΩ
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 4.5\text{ mA}$	5.2	5.8	6.4	V
$I_{CES}$	$V_{GE} = 0\text{ V}$ $V_{CE} = 1200\text{ V}$	$T_j = 25\text{ °C}$		1.6	mA
					mA
$C_{ies}$	$V_{CE} = 25\text{ V}$	$f = 1\text{ MHz}$	6.9		nF
$C_{oes}$	$V_{GE} = 0\text{ V}$	$f = 1\text{ MHz}$	0.555		nF
$C_{res}$		$f = 1\text{ MHz}$	0.405		nF
$Q_G$	- 15 V...+ 15 V		430		nC
$R_{Gint}$	$T_j = 25\text{ °C}$		2.7		Ω
$t_{d(on)}$	$V_{CC} = 600\text{ V}$	$T_j = 150\text{ °C}$	156		ns
$t_r$	$I_C = 120\text{ A}$	$T_j = 150\text{ °C}$	51		ns
$E_{on}$	$R_{G on} = 2.2\text{ Ω}$	$T_j = 150\text{ °C}$	8.8		mJ
$t_{d(off)}$	$R_{G off} = 2.2\text{ Ω}$	$T_j = 150\text{ °C}$	346		ns
$t_f$	$di/dt_{on} = 2354\text{ A}/\mu\text{s}$	$T_j = 150\text{ °C}$	42		ns
$E_{off}$	$di/dt_{off} = 2264\text{ A}/\mu\text{s}$	$T_j = 150\text{ °C}$	7.47		mJ
$R_{th(j-s)}$	per IGBT		0.22	0.25	K/W

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#### Target Data

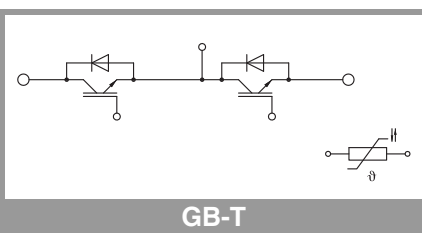
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Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse - Diode						
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 15 A	T <sub>j</sub> = 25 °C		2.38	2.71	V
	chiplevel	T <sub>j</sub> = 150 °C		2.44	2.77	V
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.30	1.50	V
		T <sub>j</sub> = 150 °C		0.90	1.10	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		72	81	mΩ
		T <sub>j</sub> = 150 °C		103	111	mΩ
I <sub>RRM</sub>	I <sub>F</sub> = 120 A	T <sub>j</sub> = 150 °C		43.4		A
Q <sub>rr</sub>	di/dt <sub>off</sub> = 1948 A/μs	T <sub>j</sub> = 150 °C		5.7		μC
E <sub>rr</sub>	V <sub>GE</sub> = -15 V	T <sub>j</sub> = 150 °C		2.04		mJ
	V <sub>CC</sub> = 600 V					
R <sub>th(j-s)</sub>	per Diode			1.25	1.34	K/W
Module						
L <sub>CE</sub>						nH
M <sub>s</sub>	to heatsink		2.25		2.5	Nm
w				29		g
Temperature Sensor						
R <sub>100</sub>	T <sub>c</sub> =100°C (R <sub>25</sub> =5 kΩ)			493 ± 5%		Ω
B <sub>100/125</sub>	R(T)=R <sub>100</sub> exp[B <sub>100/125</sub> (1/T-1/T <sub>100</sub> )]; T[K];			3550 ±2%		K



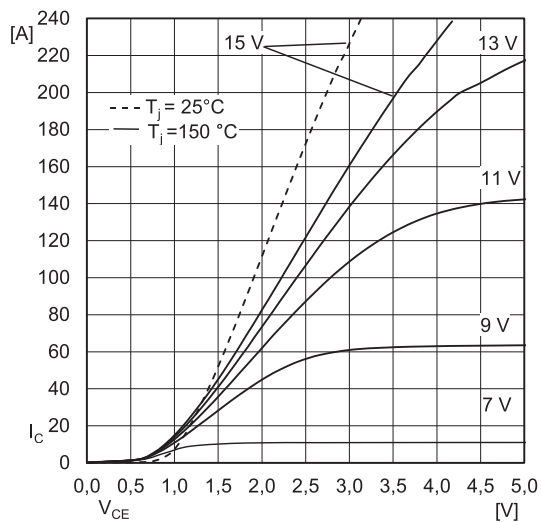


Fig. 1: Typ. output characteristic, inclusive  $R_{CC'+EE'}$

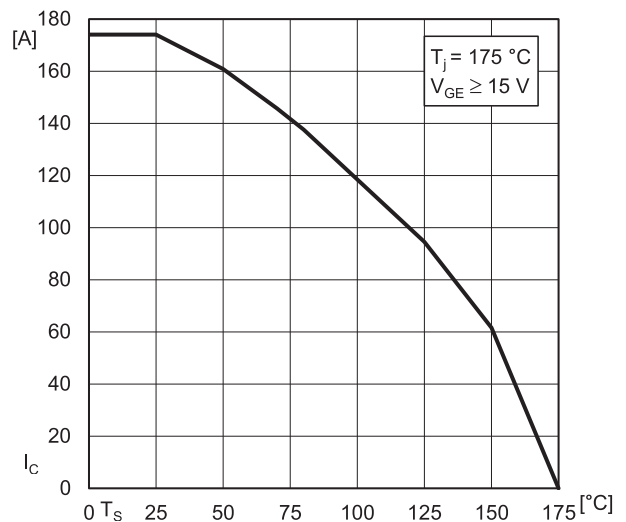


Fig. 2: Typ. rated current vs. temperature  $I_C = f(T_s)$

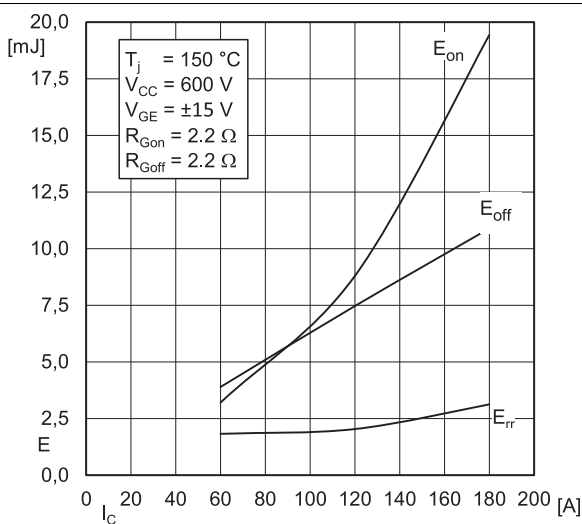


Fig. 3: Typ. turn-on /-off energy =  $f(I_C)$

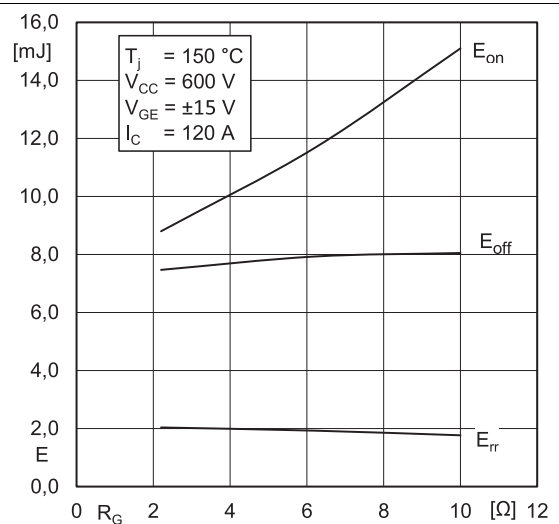


Fig. 4: Typ. turn-on /-off energy =  $f(R_G)$

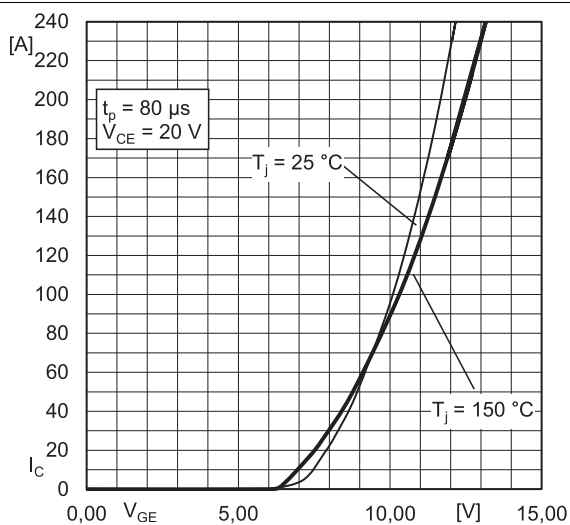


Fig. 5: Typ. transfer characteristic

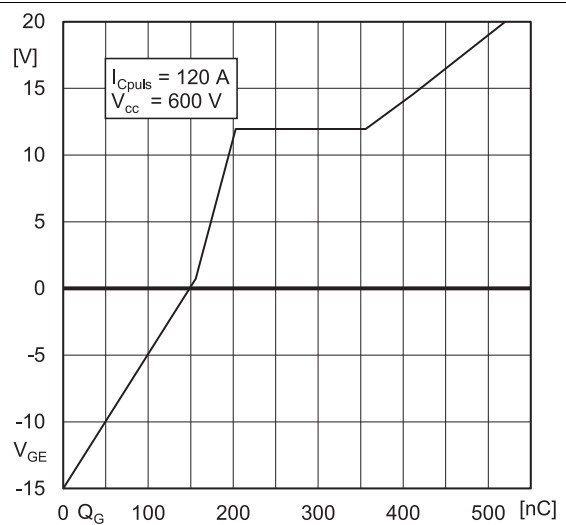
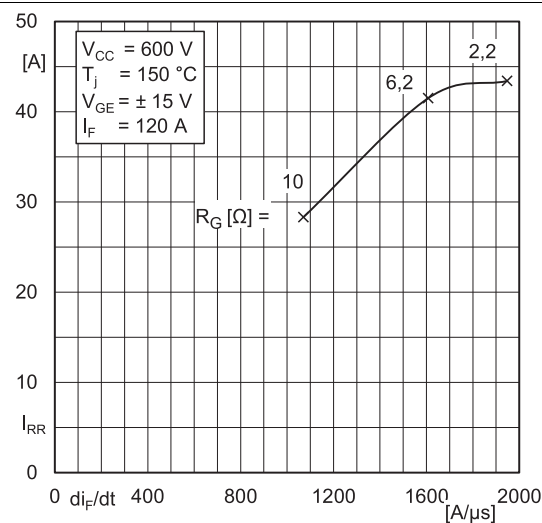
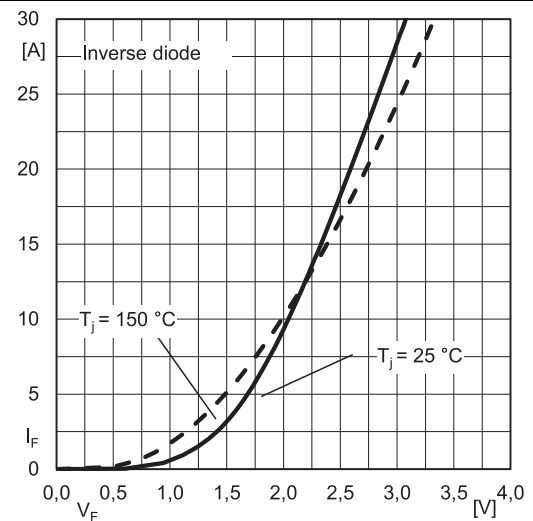
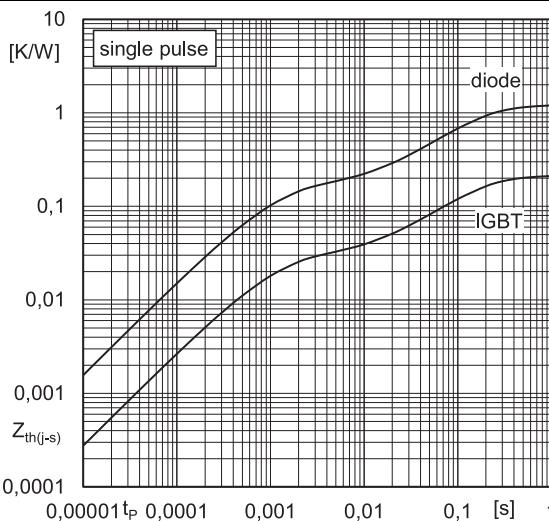
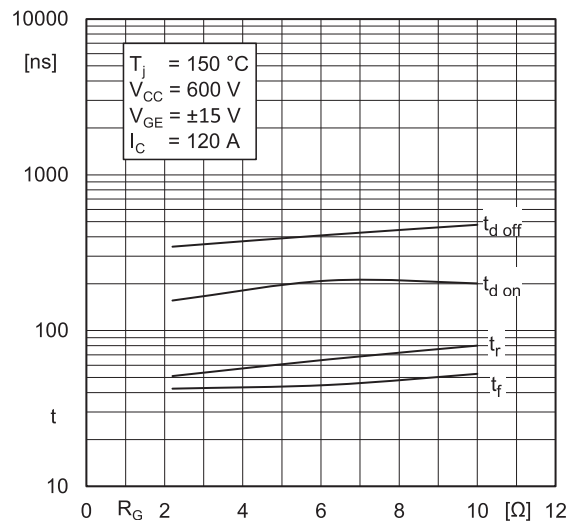
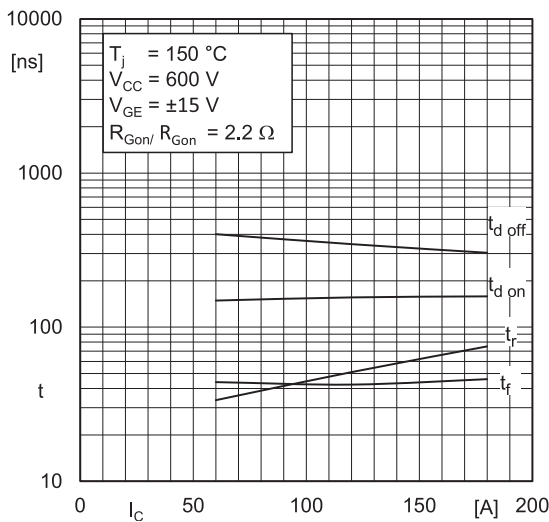
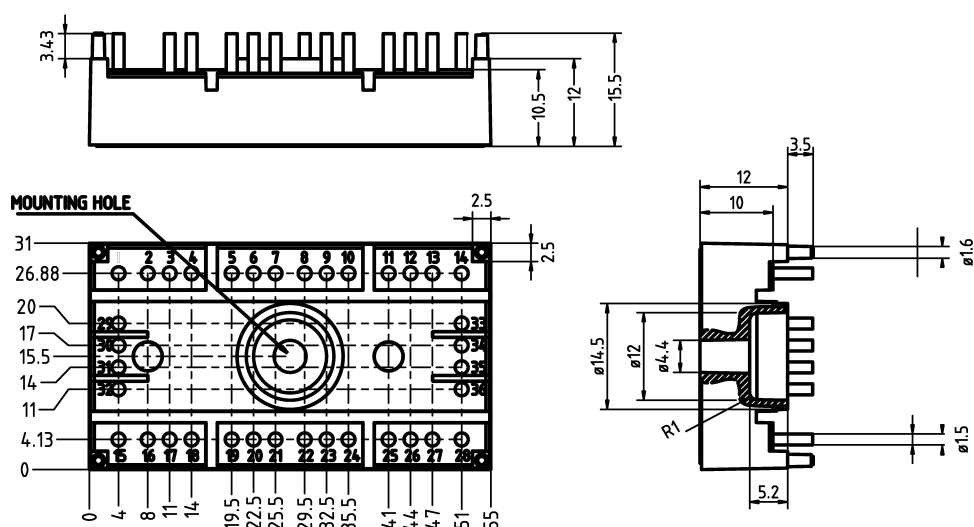


Fig. 6: Typ. gate charge characteristic



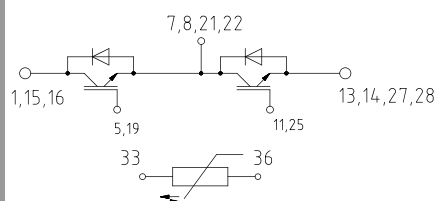
dimensions in mm  
tolerance system: ISO 2768-m



Suggested hole diameter, in the PCB, for solder pins and mounting plastic pins: 2mm

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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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