

Micro Commercial Components



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Features

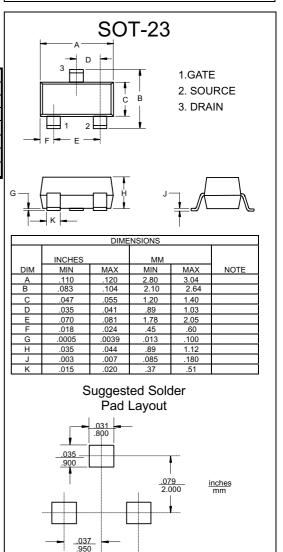
- Halogen free available upon request by adding suffix "-HF"
- 20V,7.0A, R_{DS(ON)}=22m Ω @V_{GS}=4.5V R_{DS(ON)}=26m Ω @V_{GS}=2.5V
- Low R_{DS(ON)}
- Rugged and reliable
- **ESD** Protected Gate
- SOT-23 Package
- Marking Code: 8810 Epoxy meets UL 94 V-0 flammability rating

Moisture Sensitivity Level 1 Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit	
V _{DS}	Drain-source Voltage	20	V	
ID	Drain Current-Continuous	7	A	
I _{DM}	Drain Current-Pulsed ^a	30	Α	
V _{GS}	Gate-source Voltage	±12	V	
R $_{\rm HJA}$	Thermal Resistance Junction to Ambient ^b	417	°C/W	
ΤJ	Operating Junction Temperature	-55 to +150	°C	
T _{STG}	Storage Temperature	-55 to +150	°C	

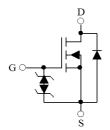
SI8810

N-Channel Enhancement Mode Field Effect Transistor



<u>.037</u> .950

Internal Block Diagram



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Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
STATIC PARAMETERS			•	•		
Drain-source breakdown voltage	V (BR) DSS	Vgs = 0V, Id =250µA	20			V
Zero gate voltage drain current	IDSS	V _{DS} =16V,V _{GS} = 0V			1	μA
Cata badu laakaga gurrant	lgss	Vgs =±4.5V, Vds = 0V			±1	μA
Gate-body leakage current		Vgs =±8V, Vds = 0V			±10	μA
Gate threshold voltage (note 1)	VGS(th)	V _{DS} =V _{GS} , I _D =250µA	0.4		1	V
		Vgs =10V, Id =7A			20	mΩ
		Vgs =4.5V, Id =6.6A			22	mΩ
Drain-source on-resistance (note 1)	RDS(on)	Vgs =3.8V, Id =6A			24	mΩ
		Vgs =2.5V, Id =5.5A			26	mΩ
		Vgs =1.8V, Id =5A			35	mΩ
Forward tranconductance (note 1)	g fs	Vds =5V, Id =7A	9			S
Diode forward voltage(note 1)	V _{SD}	I _S =1A, V _{GS} = 0V			1	V
DYNAMIC PARAMETERS (note 2)	·					
Input Capacitance	C _{iss}			1150		pF
Output Capacitance	C _{oss}	V _{DS} =10V,V _{GS} =0V,f =1MHz		185		pF
Reverse Transfer Capacitance	C _{rss}			145		pF
Total gate charge	Qg			15		nC
Gate-source charge	Q _{gs}	Vps =10V,Vgs =4.5V,Ip =7A		0.8		nC
Gate-drain charge	Q _{gd}			3.2		nC
SWITCHING PARAMETERS(note 2)			•			
Turn-on delay time	td(on)			6		ns
Turn-on rise time	tr	V _{GS} =5V,V _{DD} =10V,		13		ns
Turn-off delay time	td(off)	R_L =1.35 Ω , R_{GEN} =3 Ω		52		ns
Turn-off fall time	tr			16		ns

Notes :

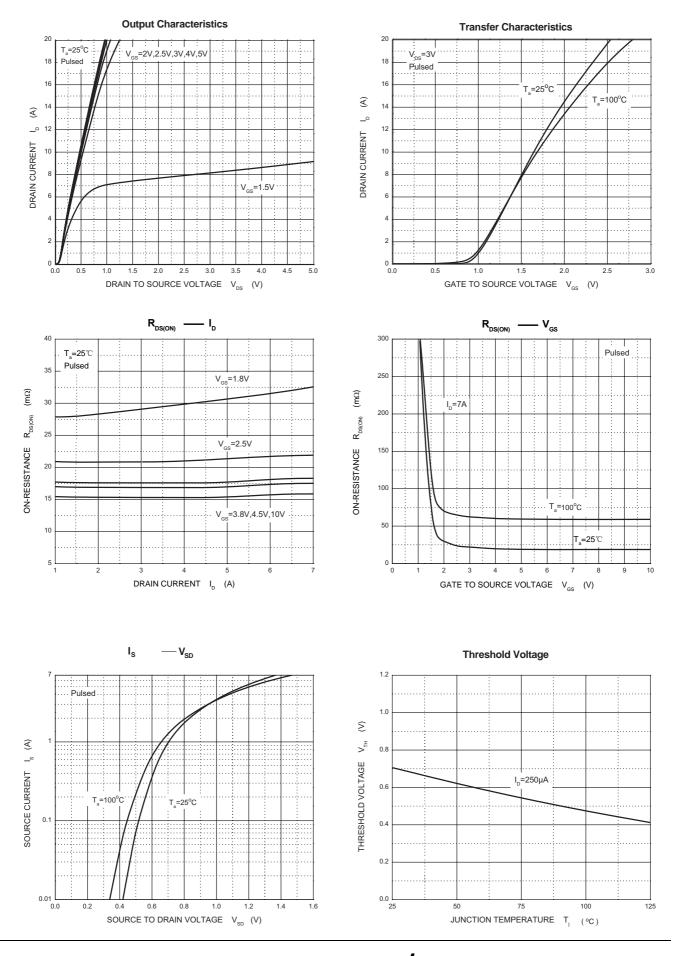
1. Pulse Test : Pulse width \leq 300µs, duty cycle \leq 0.5%.

2. Guaranteed by design, not subject to production testing.

SI8810

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Ordering Information :

Device	Packing		
Part Number-TP	Tape&Reel: 3Kpcs/Reel		

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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