



# N-Channel Reduced $Q_g$ , Fast Switching MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
30	0.00975 at V <sub>GS</sub> = 10 V	12.5	
	0.01375 at V <sub>GS</sub> = 4.5 V	10.0	

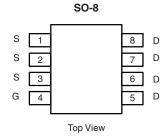
#### **FEATURES**

- $\bullet \quad \text{Extremely Low } \mathsf{Q}_{\mathsf{gd}} \text{ for Low Switching Losses} \\$
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested



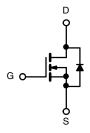
#### **APPLICATIONS**

- High-Side DC/DC Conversion
  - Notebook
  - Server



Ordering Information: Si4392DY-T1

Si4392DY-T1-E3 (Lead (Pb)-free)



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ess otherwise no	ited) <sup>a</sup>		
Parameter		Symbol	Limits	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30		
Gate-Source Voltage		$V_{GS}$	± 20	V	
Continuous Drain Current (T = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	1	12.5		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C	I <sub>D</sub>	10		
Pulsed Drain Current		I <sub>DM</sub>	50	Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.7		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	30		
Avalanche Energy	L = U. I IIII	E <sub>AS</sub>	45	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	В	3.0	W	
Maximum rower Dissipation	T <sub>A</sub> = 70 °C	P <sub>D</sub>	1.9	VV	
Operating Junction and Storage Temperature Range		T <sub>.I</sub> , T <sub>sta</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS <sup>a</sup>				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient	R <sub>thJA</sub>	33	42	°C/W
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	16	20	C/VV

#### Notes:

a. Surface mounted on 1" x 1" FR4 board,  $t \le 10 \text{ s.}$ 

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply.

## Vishay Siliconix



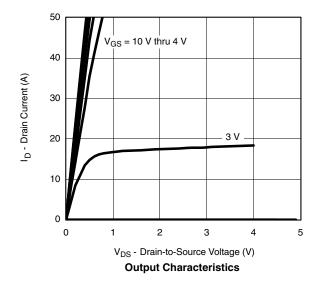
<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		3.0	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zoro Gato Voltago Drain Current	1	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, $T_J$ = 55 °C			5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = 10 \text{ V}, I_D = 12.5 \text{ A}$		0.008	0.00975	Ω	
Diain-Source On-State nesistance	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 10.0 \text{ A}$		0.011	0.01375		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 12.5 \text{ A}$		40		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 2.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.73	1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			10	15		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 12.5 \text{ A}$		3.5		nC	
Gate-Drain Charge	$Q_{gd}$			2.6			
Gate Resistance	$R_g$		0.5	1.6	2.7	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			15	25		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		5	10		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		45	70	ns	
Fall Time	t <sub>f</sub>			8	15		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.7 A, dI/dt = 100 A/μs		30	60		

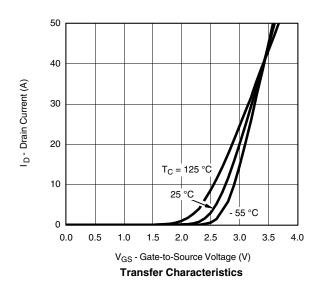
#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

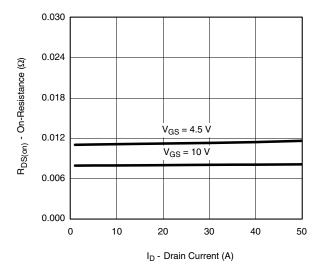




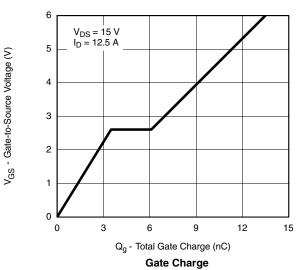




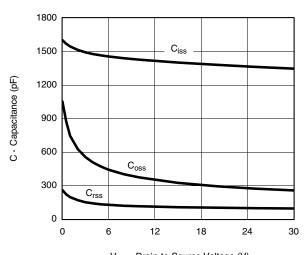
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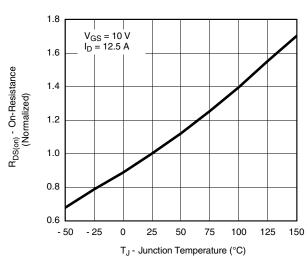
On-Resistance vs. Drain Current



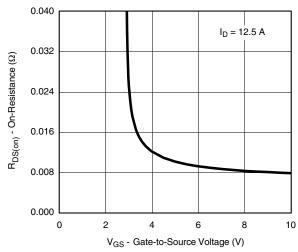
Source-Drain Diode Forward Voltage



V<sub>DS</sub> - Drain-to-Source Voltage (V) **Capacitance** 



On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

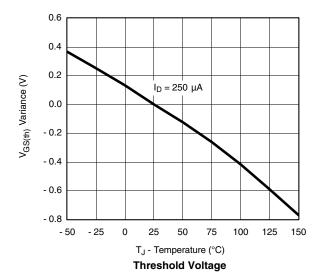
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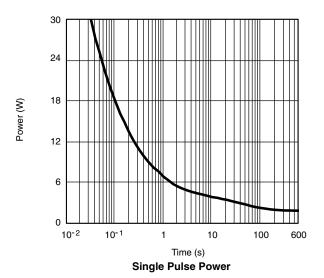
Is - Source Current (A)

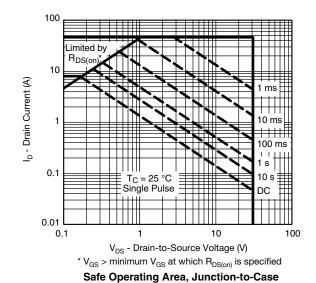
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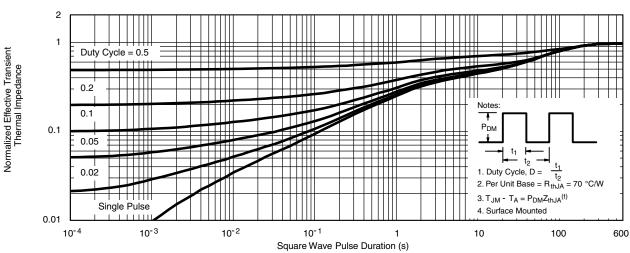
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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



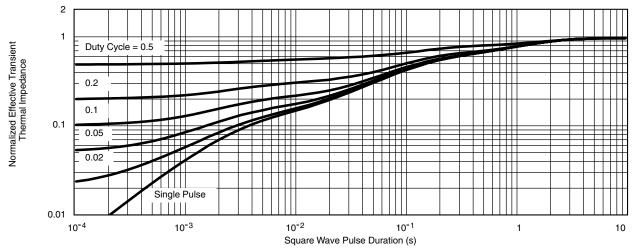








#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?72151">www.vishay.com/ppg?72151</a>



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