

Vishay Siliconix

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^b		
- 30	0.072 at V _{GS} = - 10 V	- 2.8		
- 30	0.120 at V _{GS} = - 4.5 V	- 2.0		

FEATURES

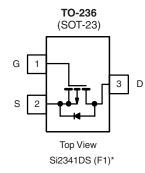
- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFETS
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switch
- PA Switch



ROHS COMPLIANT HALOGEN FREE Available



* Marking Code

Ordering Information: Si2341DS-T1-E3 (Lead (Pb)-free) Si2341DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		DS - 30	v
Gate-Source Voltage		V _{GS}	± 20		v	
Continuous Drain Current (T 150 °C) ^b	T _A = 25 °C	- I _D	- 2.8	- 2.5	•	
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^b$	T _A = 70 °C		- 2.2	- 2.0		
Pulsed Drain Current ^a		I _{DM}	- 12		A	
Continuous Source Current (Diode Conduction) ^b		۱ _S	- 0.75	- 0.6		
Power Dissipation ^b	T _A = 25 °C	- P _D	0.9	0.71	W	
	T _A = 70 °C		0.57	0.45		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^b	R _{thJA}	115	140	°C/W		
Maximum Junction-to-Ambient ^c		140	175			
Maximum Junction-to-Foot (Drain)	R _{thJF}	60	75			

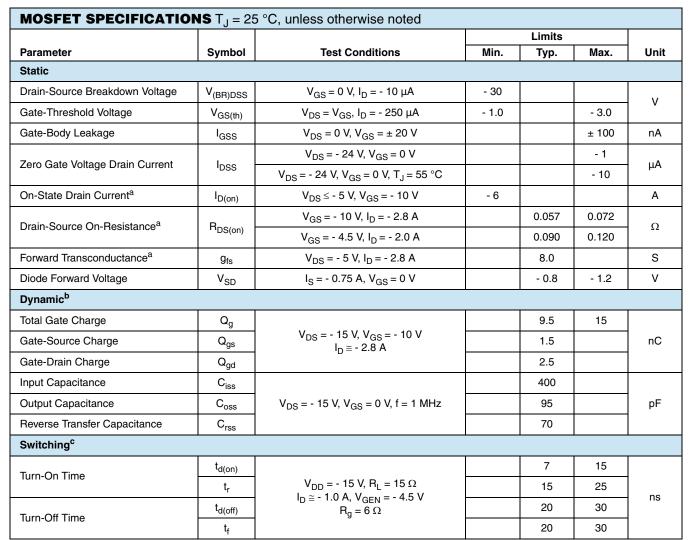
Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface mounted on FR4 board, $\dot{t} \le 5$ s.

c. Surface mounted on FR4 board.

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Notes:

a. Pulse test: PW \leq 300 μ s, duty cycle \leq 2 %.

b. For DESIGN AID ONLY, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

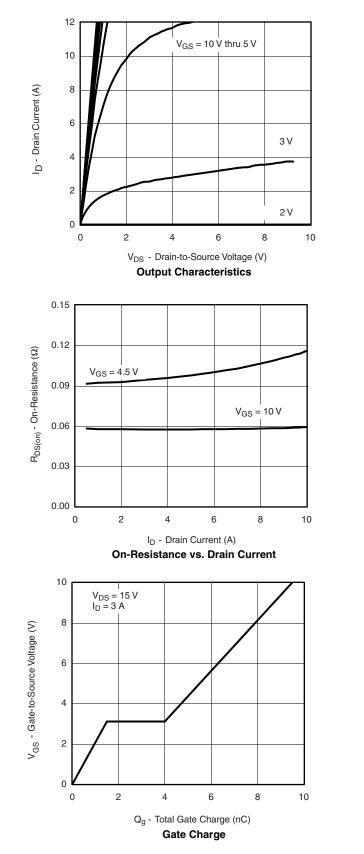
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

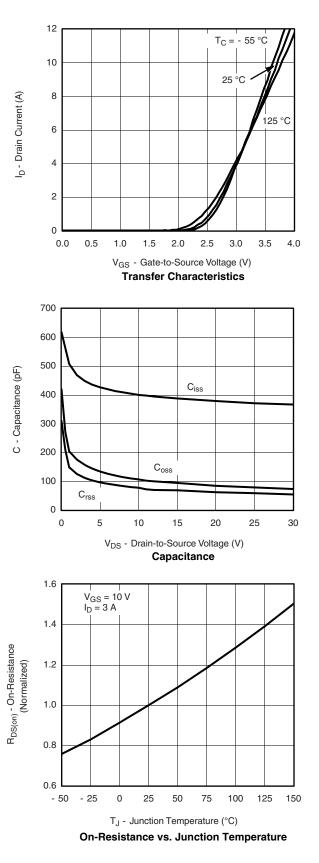




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TYPICAL CHARACTERISTICS 25 °C unless noted



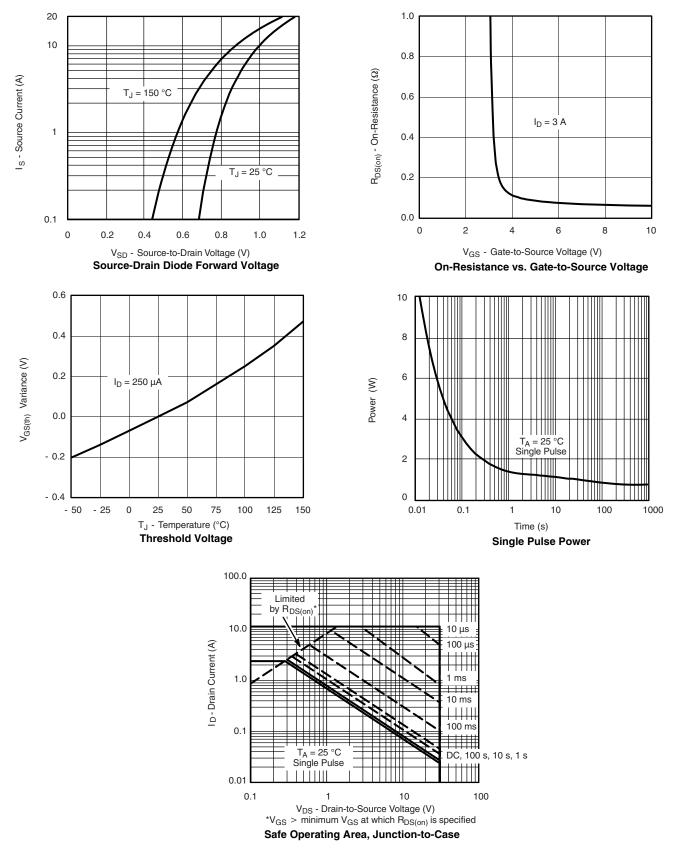


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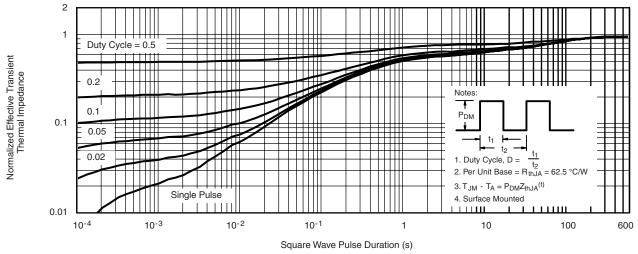
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TYPICAL CHARACTERISTICS 25 °C unless noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72263.



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