

TECHNICAL DATA DATA SHEET 4592, REV. -

HERMETIC SCHOTTKY RECTIFIER Low Forward Voltage Drop

Features:

- Soft Reverse Recovery at Low and High Temperature
- Low Forward Voltage Drop
- Low Power Loss, High Efficiency
- High Surge Capacity
- Guard Ring for Enhanced Durability and Long Term Reliability
- Guaranteed Reverse Avalanche Characteristics

Maximum Ratings

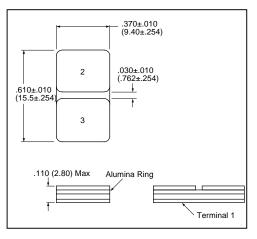
Characteristics	Symbol	Condition	Max.	Units
Peak Inverse Voltage	V_{RWM}	-	100	V
Max. Average Forward Current	$I_{F(AV)}$	50% duty cycle, rectangular wave form (Single)	15	А
Max. Average Forward Current	I _{F(AV)}	50% duty cycle, rectangular wave form (Common Cathode)	30	Α
Max. Peak One Cycle Non- Repetitive Surge Current	I _{FSM}	8.3 ms, half Sine wave (per leg)	280	А
Non-Repetitive Avalanche Energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3.0 \text{A}, \\ L = 4.4 \text{mH (per leg)}$	20	mJ
Repetitive Avalanche Current	I _{AR}	I_{AS} decay linearly to 0 in 1 μ s f limited by T_J max V_A =1.5 V_R	3.0	А
Maximum Thermal Resistance	$R_{ heta JC}$	DC operation	0.40	°C/W
Max. Junction Temperature	T_J	-	-65 to +200	°C
Max. Storage Temperature	T_{stg}	-	-65 to +200	°C

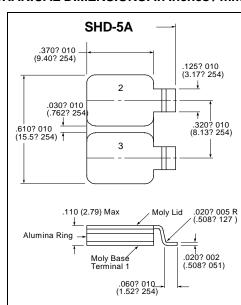
Electrical Characteristics

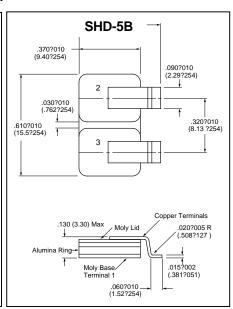
Characteristics	Symbol	Condition	Max.	Units
Max. Forward Voltage Drop	V_{F1}	@ 15A, Pulse, T _J = 25 °C	0.84	\ \
(per leg)	V_{F2}	@ 15A, Pulse, T _J = 125 °C	0.68	V
Max. Reverse Current	I _{R1}	@V _R = 100V, Pulse,	0.35	mA
		T _J = 25 °C		
(per leg)	I _{R2}	@V _R = 100V, Pulse,	8.0	mA
		T _J = 125 °C		
Max. Junction Capacitance	C _T	$@V_R = 5V, T_C = 25 ^{\circ}C$	500	pF
(per leg)		$f_{SIG} = 1MHz,$		
		$V_{SIG} = 50 \text{mV (p-p)}$		

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MECHANICAL DIMENSIONS: In Inches / mm





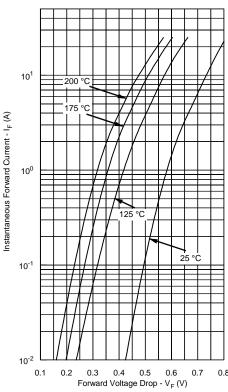


1 2 3 PINOUT TABLE

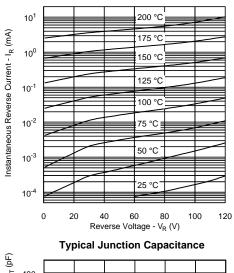
DEVICE TYPEPIN 1PIN 2PIN 3DUAL RECTIFIER, COMMON CATHODE (P)COMMON CATHODEANODEANODE

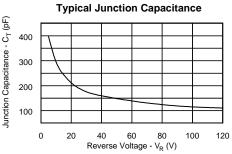
Note: The V_f curves shown are for the SD125SC100 unpackaged die only.





Typical Reverse Characteristics







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TECHNICAL DATA

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