

SANYO Semiconductors DATA SHEET

SFT1423 — General-Purpose Switching Device Applications

Features

- · Low ON-resistance.
- · 4V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|------------------|------------------------|-------------|------|
| Drain-to-Source Voltage | V _{DSS} | | 500 | V |
| Gate-to-Source Voltage | VGSS | | ±20 | V |
| Drain Current (DC) | ID | | 2 | А |
| Drain Current (PW≤10μs) | IDP | PW≤10μs, duty cycle≤1% | 10 | А |
| Allowable Power Dissipation | Do | | 1.0 | W |
| | PD | Tc=25°C | 20 | W |
| Channel Temperature | Tch | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

Electrical Characteristics at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | | | Unit |
|--|-----------------------|--|---------|-----|-----|-------|
| | | | min | typ | max | Offic |
| Drain-to-Source Breakdown Voltage | V(BR)DSS | ID=10mA, VGS=0V | 500 | | | V |
| Zero-Gate Voltage Drain Current | IDSS | V _{DS} =400V, V _{GS} =0V | | | 100 | μΑ |
| Gate-to-Source Leakage Current | IGSS | V _{GS} =±16V, V _{DS} =0V | | | ±10 | μΑ |
| Cutoff Voltage | VGS(off) | V _{DS} =10V, I _D =1mA | 1.2 | | 2.6 | V |
| Forward Transfer Admittance | yfs | V _{DS} =10V, I _D =1A | 1.1 | 1.9 | | S |
| Static Drain-to-Source On-State Resistance | R _{DS} (on)1 | I _D =1A, V _G S=10V | | 3.8 | 4.9 | Ω |
| | R _{DS} (on)2 | I _D =0.5A, V _G S=4V | | 3.9 | 5 | Ω |

Marking: T1423 Continued on next page.

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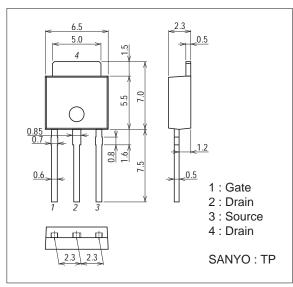
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| Parameter | Symbol | Conditions | Ratings | | | Unit |
|-------------------------------|----------------------|---|---------|-----|-----|-------|
| | | | min | typ | max | Uillt |
| Input Capacitance | Ciss | V _{DS} =30V, f=1MHz | | 175 | | pF |
| Output Capacitance | Coss | V _{DS} =30V, f=1MHz | | 32 | | pF |
| Reverse Transfer Capacitance | Crss | V _{DS} =30V, f=1MHz | | 6 | | pF |
| Turn-ON Delay Time | t _d (on) | See specified Test Circuit. | | 7.4 | | ns |
| Rise Time | t _r | See specified Test Circuit. | | 8.8 | | ns |
| Turn-OFF Delay Time | t _d (off) | See specified Test Circuit. | | 42 | | ns |
| Fall Time | tf | See specified Test Circuit. | | 27 | | ns |
| Total Gate Charge | Qg | V _{DS} =200V, V _{GS} =10V, I _D =2A | | 8.7 | | nC |
| Gate-to-Source Charge | Qgs | V _{DS} =200V, V _{GS} =10V, I _D =2A | | 1.1 | | nC |
| Gate-to-Drain "Miller" Charge | Qgd | V _{DS} =200V, V _{GS} =10V, I _D =2A | | 2.9 | | nC |
| Diode Forward Voltage | VSD | IS=2A, VGS=0V | | 0.9 | 1.2 | V |

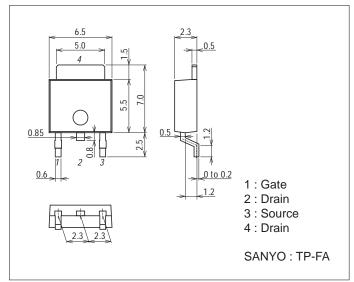
Package Dimensions

unit : mm (typ) 7518-004

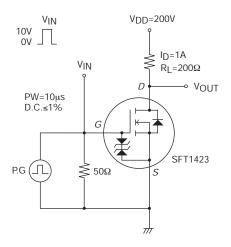


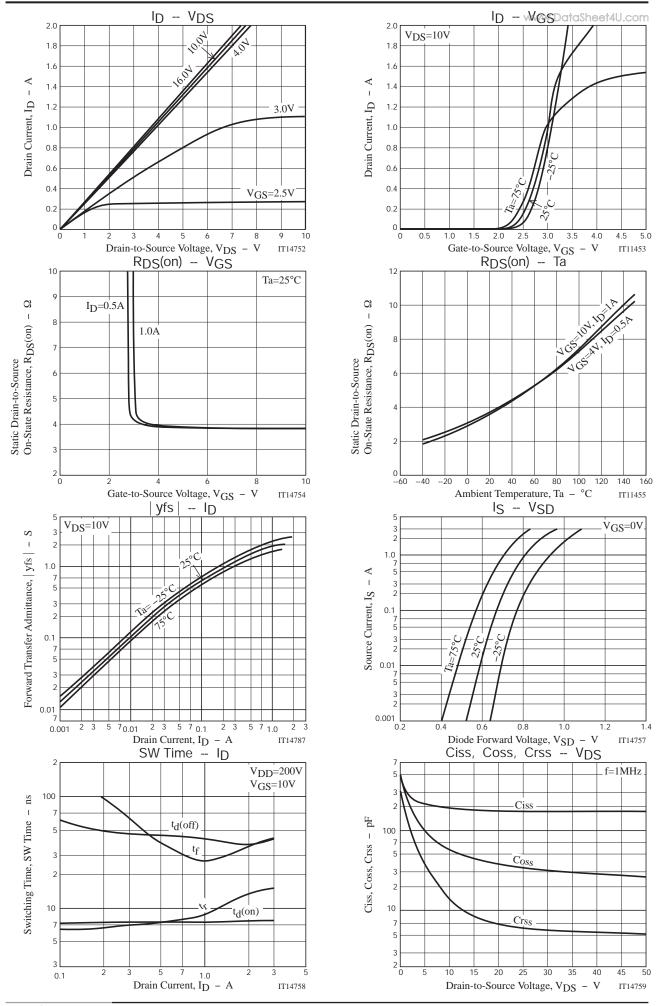
Package Dimensions

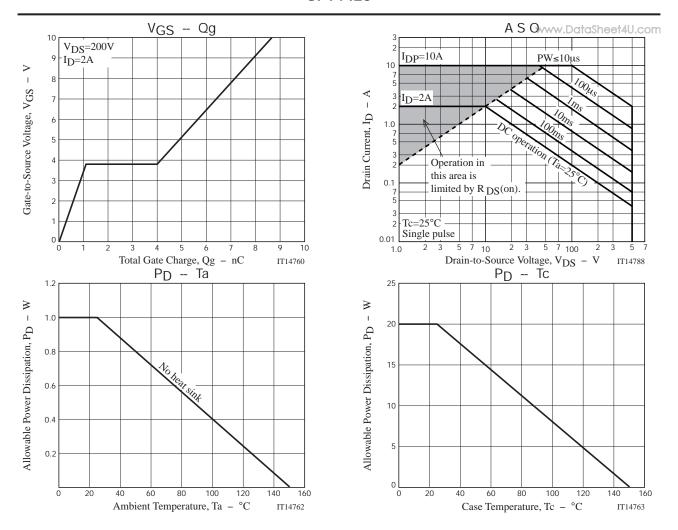
unit : mm (typ) 7003-004



Switching Time Test Circuit







Note on usage: Since the SFT1423 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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