



Solid State Devices, Inc.

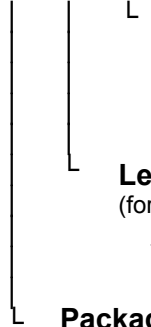
14701 Firestone Blvd * La Mirada, CA 90638
Phone: (562) 404-4474 * Fax: (562) 404-1773
ssdi@ssdi-power.com * www.ssdi-power.com

DESIGNER'S DATA SHEET

Part Number / Ordering Information ^{1/}

SFL630

J



Screening ^{2/}

— = Not Screened
TX = TX Level
TXV = TXV Level
S = S Level

Lead Options

(for TO-257 package)

— = Straight Leads
DB = Down Bend
UB = Up Bend

Package

J = TO-257
S.5 = SMD.5
G = Cerpack

SFL630 series

Logic Level
10 AMP, 200 Volts, 400 mΩ
Avalanche Rated N-MOSFET

Features:

- Rugged Technology
- Very low ON-resistance: 200mΩ typ
- Avalanche rated
- Hermetically Sealed Power Packaging
- Low Total Gate Charge, Fast Switching
- Enhanced performance replacement for IRL630 types and 2N6904
- TX, TXV, S-Level screening available

Maximum Ratings ^{3/}

	Symbol	Value	Units
Drain - Source Voltage	V_{DS}	200	V
Gate - Source Voltage, continuous	V_{GS}	±10	V
Gate - Source Voltage, transient		±15	V
Max. Continuous Drain Current (package limited)	I_{D1} I_{D2}	10 6	A
	@ $T_C = 25^\circ C$ @ $T_C = 100^\circ C$		
Max. Avalanche Current	I_{AR}	10	A
	@ L = 5.0mH		
Single Pulse Avalanche Energy	E_{AS}	250	mJ
	@ L = 5.0mH		
Total Power Dissipation @ $T_C = 25^\circ C$	P_D	120 150	W
	TO-257 SMD.5, Cerpack		
Operating & Storage Temperature	$T_{OP} \text{ \& } T_{STG}$	-55 to +175	°C
Maximum Thermal Resistance (Junction to Case)	$R_{\theta JC}$	3.5 1.70	°C/W
	TO-257 SMD.5, Cerpack		

NOTES:

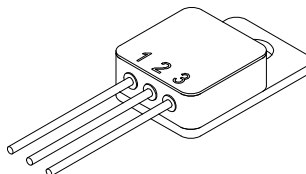
*Pulse Test: Pulse Width = 300μsec,
Duty Cycle = 2%.

^{1/} For ordering information, price, and
availability - contact factory.

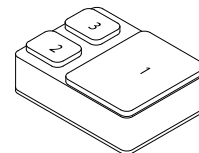
^{2/} Screening based on MIL-PRF-19500.
Screening flows available on request.

^{3/} Unless otherwise specified, all electrical
characteristics @25°C.

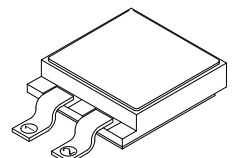
TO-257 (J)



SMD.5 (S.5)



Cerpack (G)



NOTE: All specifications are subject to change without notification.
SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: FT0057A

DOC



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SFL630 series

Electrical Characteristics ^{3/}		Symbol	Min	Typ	Max	Units
Drain to Source Breakdown Voltage	$V_{GS} = 0V, I_D = 0.25 \text{ mA}$	BV_{DSS}	200	225	-	V
Drain to Source On State Resistance	$V_{GS} = 5V, I_D = 5A, T_j = 25^\circ C$	$R_{DS(on)}$	-	200	400	mΩ
	$V_{GS} = 5V, I_D = 10A, T_j = 25^\circ C$		-	250	500	
	$V_{GS} = 4V, I_D = 5A, T_j = 25^\circ C$		-	200	-	
	$V_{GS} = 5V, I_D = 10A, T_j = 125^\circ C$		-	400	-	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 1mA, T_j = 25^\circ C$	$V_{GS(th)}$	1.0	1.6	2.0	V
	$V_{DS} = V_{GS}, I_D = 1mA, T_j = 125^\circ C$		0.5	1.0	-	
	$V_{DS} = V_{GS}, I_D = 1mA, T_j = -55^\circ C$		-	2.0	3.0	
Gate to Source Leakage	$V_{GS} = \pm 10V, T_j = 25^\circ C$	I_{GSS}	-	1	± 100	nA
	$V_{GS} = \pm 10V, T_j = 125^\circ C$		-	10	± 200	
Zero Gate Voltage Drain Current	$V_{DS} = 200V, V_{GS} = 0V, T_j = 25^\circ C$	I_{DSS}	-	0.01	1	μA
	$V_{DS} = 160V, V_{GS} = 0V, T_j = 125^\circ C$		-	100	250	μA
Forward Transconductance	$V_{DS} = 50V, I_D = 5.4A, T_j = 25^\circ C$	g_{fs}	3	6	-	Mho
Total Gate Charge	$V_{GS} = 10V$	Q_g	-	42	70	nC
Gate to Source Charge	$V_{DS} = 100V$	Q_{gs}	-	6	10	
Gate to Drain Charge	$I_D = 8A$	Q_{gd}	-	15	24	
Turn on Delay Time	$V_{GS} = 10V$	$t_{d(on)}$	-	7.5	15	nsec
Rise Time	$V_{DS} = 100V$	t_r	-	15	30	
Turn off Delay Time	$I_D = 9A$	$t_{d(off)}$	-	75	110	
Fall Time	$R_G = 6\Omega$	t_f	-	30	45	
Diode Forward Voltage	$I_F = 10A, V_{GS} = 0V$	V_{SD}	-	0.85	2.0	V
Diode Reverse Recovery Time	$I_F = 9A, di/dt = 100A/\mu sec$	t_{rr}	-	190	350	nsec
Peak Reverse Recovery Current		Q_{rr}	-	1	-	μC
Reverse Recovery Charge						
Input Capacitance	$V_{GS} = 0V$	C_{iss}	-	1100	-	pF
Output Capacitance	$V_{DS} = 25V$	C_{oss}	-	450	-	
Reverse Transfer Capacitance	$f = 1 \text{ MHz}$	C_{rss}	-	60	-	

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SFL630 series

PACKAGE OUTLINE:

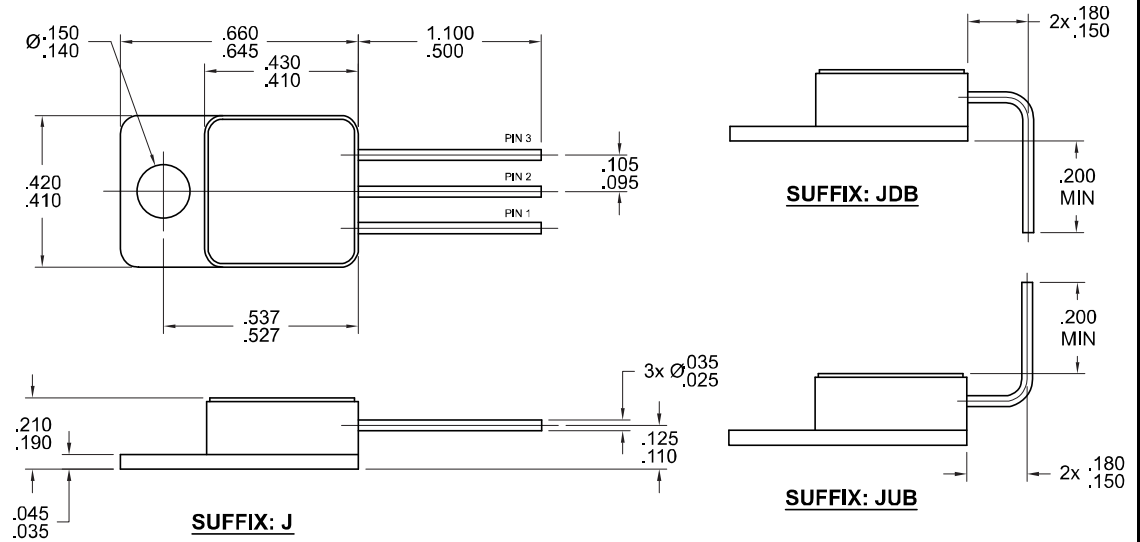
TO-257 (J)

PINOUT:

PIN 1: DRAIN

PIN 2: SOURCE

PIN 3: GATE



PACKAGE OUTLINE:

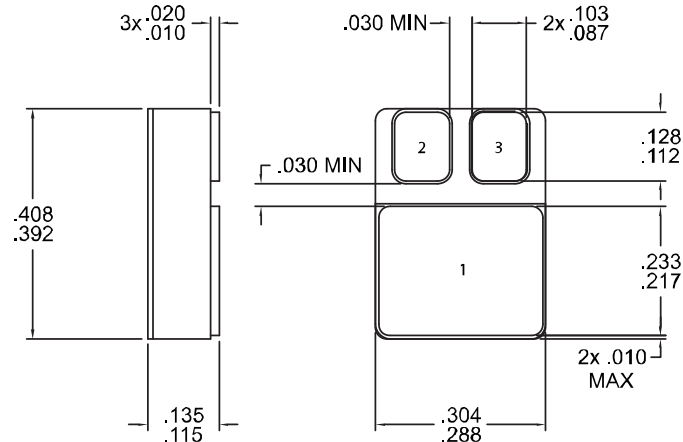
SMD.5 (S.5)

PINOUT:

PIN 1: DRAIN

PIN 2: SOURCE

PIN 3: GATE



PACKAGE OUTLINE:

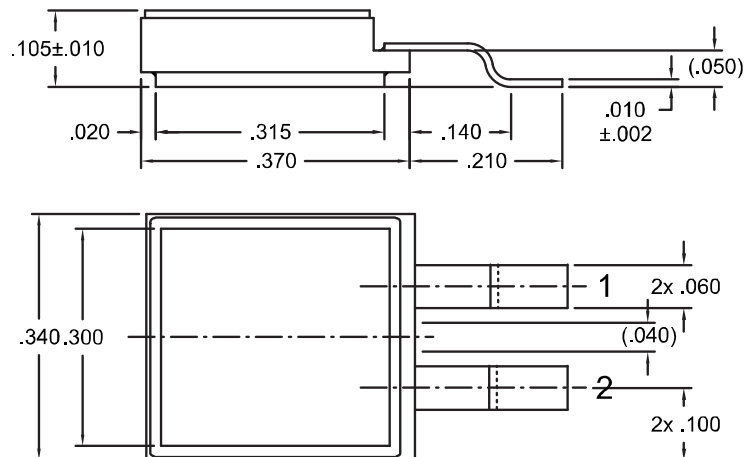
Cerpack (G)

PINOUT:

PIN 1: DRAIN

PIN 2: SOURCE

BOTTOM PAD: GATE



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