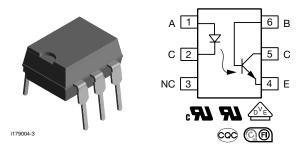


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# Optocoupler, Phototransistor Output, Low Input Current, With Base Connection, 5300 V<sub>RMS</sub>



### **LINKS TO ADDITIONAL RESOURCES**



### **DESCRIPTION**

The SFH608 is an optocoupler designed for high current transfer ratio at low input currents with the output transistor saturated. This makes the device ideal for low current switching applications. The SFH608 is packaged in a six pin plastic DIP.

#### **AGENCY APPROVALS**

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884), available with option 1
- BSI
- CQC GB4943.1
- CQC GB8898
- FIMKO

### **FEATURES**

- Very high CTR at  $I_F = 1.0$  mA,  $V_{CE} = 0.5$  V
- Specified minimum CTR at I<sub>F</sub> = 0.5 mA
- V<sub>CE</sub> = 1.5 V ≥ 32 % (typ. 120 %)
- · Good CTR linearity with forward current
- · Low CTR degradation
- High collector-emitter voltage, V<sub>CEO</sub> = 55 V
- Isolation test voltage: 5300 V<sub>RMS</sub>
- Low current input
- · Low coupling capacitance
- · High common mode transient immunity
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **APPLICATIONS**

- Telecommunications
- · Industrial controls
- · Office machines
- Microprocessor system interfaces

#### **ORDERING INFORMATION** Option 6 0 8 Χ 0 Т s Н 6 PART NUMBER CTR PACKAGE OPTION TAPE BIN AND Option 7 Option 9 REEL

AGENCY CERTIFIED / PACKAGE	CTR (%)					
UL, cUL, BSI, CQC	63 to 125	100 to 200	160 to 320	250 to 500		
DIP-6	SFH608-2	SFH608-3	SFH608-4	SFH608-5		
DIP-6, 400 mil, option 6	SFH608-2X006	SFH608-3X006	-	-		
SMD-6, option 7	=	SFH608-3X007 (1)	SFH608-4X007 (1)	SFH608-5X007		
UL, cUL, BSI, CQC, VDE (option 1)	63 to 125	100 to 200	160 to 320	250 to 500		
DIP-6	-	SFH608-3X001	SFH608-4X001	-		
DIP-6, 400 mil, option 6	=	=	SFH608-4X016	-		

#### Notes

Rev. 1.7, 11-Jan-2024

- · Additional options may be possible, please contact sales office
- (1) Also available in tubes; do not add T to end





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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		$V_{R}$	6	V		
DC forward current		I <sub>F</sub>	50	mA		
Surge forward current	t <sub>P</sub> ≤ 10 μs	I <sub>FSM</sub>	2.5	Α		
Total power dissipation		P <sub>diss</sub>	70	mW		
OUTPUT						
Collector emitter voltage		$V_{CEO}$	55	V		
Collector base voltage		$V_{CBO}$	55	V		
Emitter base voltage		$V_{EBO}$	7	V		
Collector current		I <sub>C</sub>	50	mA		
Surge collector current	t <sub>P</sub> ≤ 1.0 ms		100	mA		
Total power dissipation		P <sub>diss</sub>	150	mW		
COUPLER						
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		
Operating temperature range		T <sub>amb</sub>	-55 to +100	°C		
Soldering temperature (1)	Max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C		

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 5 \text{ mA}$		$V_{F}$	-	1.1	1.5	V
Reverse voltage	$I_R = 10 \mu A$		$V_R$	6	-	-	V
Reverse current	$V_R = 6 V$		I <sub>R</sub>	-	0.01	10	μΑ
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		Co	=	25	-	pF
Thermal resistance			R <sub>thja</sub>	-	1070	-	K/W
OUTPUT							
Collector emitter voltage	I <sub>CE</sub> = 10 μA		$V_{CEO}$	55	-	-	V
Emitter base voltage	I <sub>EB</sub> = 10 μA		$V_{EBO}$	7	-	-	V
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz		$C_{CE}$	-	10	_	pF
Collector base capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz		C <sub>CB</sub>	=	16	-	pF
Emitter base capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz		C <sub>EB</sub>	-	10	-	pF
Thermal resistance			R <sub>thja</sub>	-	500	-	K/W
Collector emitter leakage current	V <sub>CE</sub> = 10 V		I <sub>CEO</sub>	=	10	200	nA
COUPLER							
Coupling capacitance			$C_C$	-	0.6	-	pF
	$I_C = 0.32 \text{ mA}, I_F = 1 \text{ mA}$	SFH608-2	V <sub>CEsat</sub>	-	0.25	0.4	V
Catamatian called a salled a salitation	$I_C = 0.5 \text{ mA}, I_F = 1 \text{ mA}$	SFH608-3	$V_{CEsat}$	-	0.25	0.4	V
Saturation voltage, collector emitter	$I_C = 0.8 \text{ mA}, I_F = 1 \text{ mA}$	SFH608-4	$V_{CEsat}$	-	0.25	0.4	V
	$I_C = 1.25 \text{ mA}, I_E = 1 \text{ mA}$	SFH608-5	V <sub>CEsat</sub>	-	0.25	0.4	V

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.





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CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$I_F = 1 \text{ mA}, V_{CC} = 0.5 \text{ V}$	SFH608-2	CTR	63	-	125	%
	$I_F = 0.5 \text{ mA}, V_{CC} = 1.5 \text{ V}$	SFH608-2	CTR	32	75	-	%
	$I_F = 1 \text{ mA}, V_{CC} = 0.5 \text{ V}$	SFH608-3	CTR	100	-	200	%
Coupling transfer ratio	$I_F = 0.5 \text{ mA}, V_{CC} = 1.5 \text{ V}$	SFH608-3	CTR	50	120	-	%
Coupling transfer ratio	$I_F = 1 \text{ mA}, V_{CC} = 0.5 \text{ V}$	SFH608-4	CTR	160	-	320	%
	$I_F = 0.5 \text{ mA}, V_{CC} = 1.5 \text{ V}$	SFH608-4	CTR	80	200	-	%
	$I_F = 1 \text{ mA}, V_{CC} = 0.5 \text{ V}$	SFH608-5	CTR	250	-	500	%
	$I_F = 0.5 \text{ mA}, V_{CC} = 1.5 \text{ V}$	SFH608-5	CTR	125	300	-	%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_C$ = 2 mA (to adjust by $I_F$ ), $R_L$ = 100 $\Omega$ , $V_{CC}$ = 5 V	t <sub>on</sub>	-	8	-	μs
Rise time	$I_C$ = 2 mA (to adjust by $I_F$ ), $R_L$ = 100 $\Omega$ , $V_{CC}$ = 5 V	t <sub>r</sub>	-	5	-	μs
Turn-off time	$I_C$ = 2 mA (to adjust by $I_F$ ), $R_L$ = 100 $\Omega$ , $V_{CC}$ = 5 V	t <sub>off</sub>	-	7.5	-	μѕ
Fall time	$I_C = 2$ mA (to adjust by $I_F$ ), $R_L = 100 \Omega$ , $V_{CC} = 5 V$	t <sub>f</sub>	-	7	-	μs

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		55 / 100 / 21			
Comparative tracking index		CTI	175			
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	4420	V <sub>RMS</sub>		
Maximum transient isolation voltage		V <sub>IOTM</sub>	8000	V		
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	890	V		
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω		
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω		
Output safety power		P <sub>SO</sub>	700	mW		
Input safety current		I <sub>SI</sub>	400	mA		
Input safety temperature		T <sub>SI</sub>	175	°C		
Creepage distance	Standard DIP-4		≥ 7	mm		
Clearance distance	Standard DIP-4		≥ 7	mm		
Creepage distance	400 mil DIP-4		≥ 8	mm		
Clearance distance	400 mil DIP-4		≥ 8	mm		
Insulation thickness		DTI	≥ 0.4	mm		

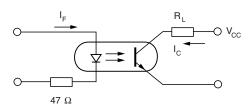
### Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.



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### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)



isfh608\_01

Fig. 1 - Switching Schematic

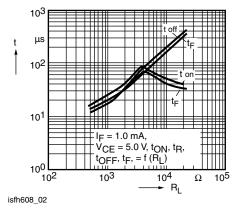


Fig. 2 - Switching Times

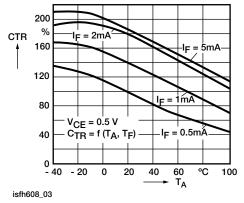
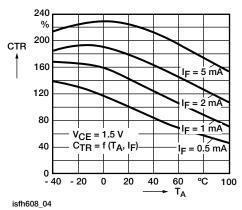


Fig. 3 - Current Transfer Ratio (typ.)



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Fig. 4 - Current Transfer Ratio (typ.)

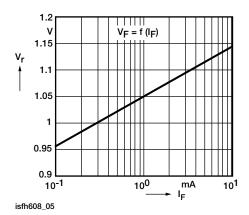


Fig. 5 - Diode Forward Voltage (typ.)

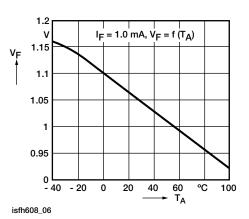


Fig. 6 - Diode Forward Voltage (typ.)



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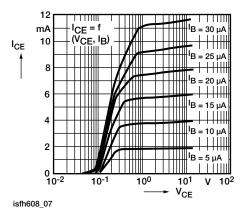


Fig. 7 - Output Characteristics

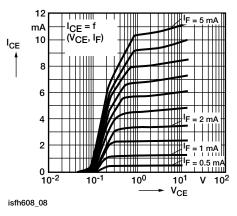


Fig. 8 - Output Characteristics

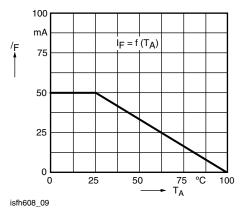


Fig. 9 - Permissible Forward Current Diode

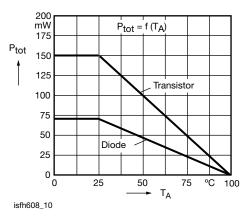


Fig. 10 - Permissible Power Dissipation for Transistor and Diode

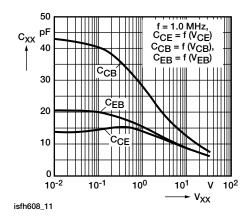


Fig. 11 - Transistor Capacitance

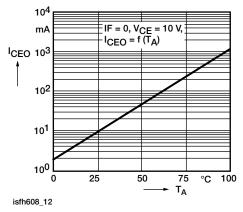
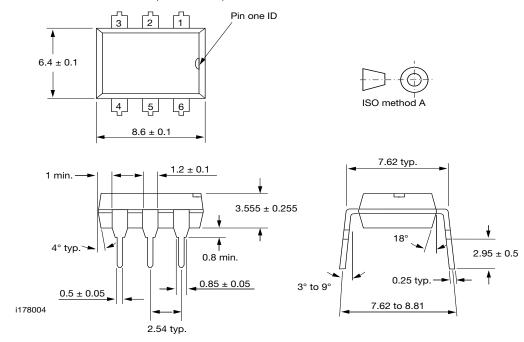


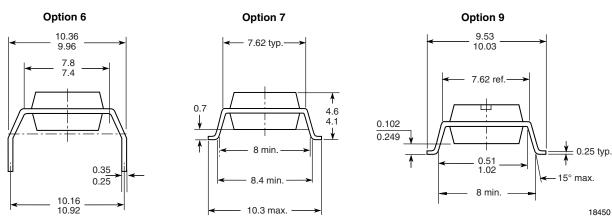
Fig. 12 - Collector Emitter Leakage Current vs. Temperature



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### PACKAGE DIMENSIONS in inches (millimeters)





### **PACKAGE MARKING** (example)

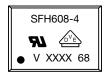


Fig. 13 - Example of SFH608-4X001

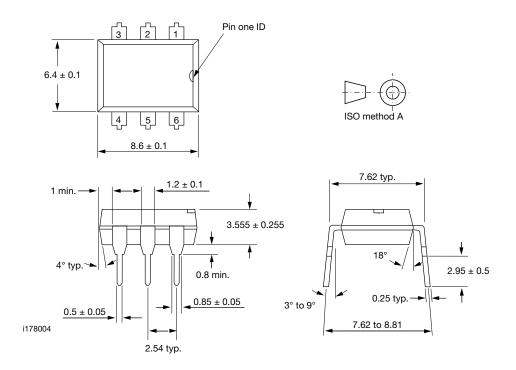
### **Notes**

- XXXX = LMC (lot marking code)
- · VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking



### **DIP-6A**

### **PACKAGE DIMENSIONS** in inches (millimeters)



#### Note

The information in this document provides generic information but for specific information on a product the appropriate product datasheet should be used.



### **Legal Disclaimer Notice**

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