

## 5A, 500V N-CHANNEL POWER MOSFET

### GENERAL DESCRIPTION

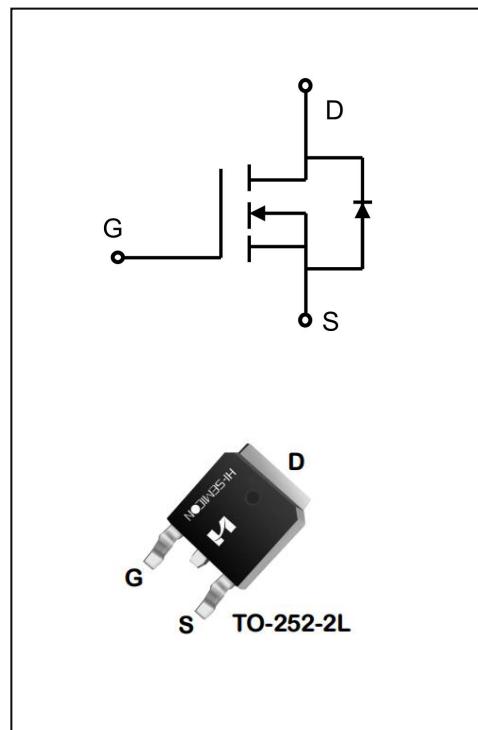
These N-Channel enhancement mode power field effect transistors are produced using Hi-semicon's proprietary, planar stripe, VDMOS technology.

### Features

- ◆  $V_{DS}(V)=500V$ ,  $I_D=5A$
- ◆  $R_{DS(on)}$   
TYP:  $1.5\Omega @ V_{GS}=10V$   $I_D=2.5A$   
MAX:  $1.8\Omega$

### Applications

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



### ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFD5N50TS	TO-252-2L	SFD5N50TS	Pb free	Reel

**ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub>=25°C unless otherwise noted)**

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	500	V
Gate-Source Voltage	V <sub>GS</sub>	±30	V
Drain Current T <sub>C</sub> = 25°C T <sub>C</sub> = 100°C	I <sub>D</sub>	5.0	A
		3.8	
Drain Current Pulsed (Note 1)	I <sub>DM</sub>	20	A
Power Dissipation(T <sub>C</sub> =25°C) -Derate above 25°C	P <sub>D</sub>	76	W
		0.61	W/°C
Single Pulsed Avalanche Energy (Note 2)	E <sub>AS</sub>	306.15	mJ
Operation Junction Temperature Range	T <sub>J</sub>	-55~+150	°C
Storage Temperature Range	T <sub>stg</sub>	-55~+150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	TL	300	°C

**THERMAL CHARACTERISTICS**

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	1.6	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	62.5	°C/W

**ELECTRICAL CHARACTERISTICS**

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
Drain -Source Breakdown Voltage	B <sub>VDSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	500	--	--	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V	--	--	100	nA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =30V, V <sub>DS</sub> =0V	--	--	100	nA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V	--	--	-100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
Static Drain- Source On State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2.5A	--	1.5	1.8	Ω
<b>Dynamic Characteristics</b>						
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V; f=1.0MHZ	--	3.1	--	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V V <sub>GS</sub> =0V f=1.0MHZ	--	529	--	pF
Output Capacitance	C <sub>oss</sub>		--	67	--	
Reverse Transfer Capacitance	C <sub>rss</sub>		--	16.6	--	
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =300V R <sub>G</sub> =25Ω I <sub>D</sub> =5A (Note 3.4)	--	13.1	--	ns
Turn-on Rise Time	t <sub>r</sub>		--	26.4	--	

Turn-off Delay Time	$t_{d(\text{off})}$	$V_{DD}=300V$ $R_G=25\Omega$ $I_D=5A$ (Note 3.4)	--	20.6	--	ns
Turn-off Fall Time	$t_f$		--	24.1	--	
Total Gate Charge	$Q_g$	$V_{DS}=400V$ , $I_D=5A$ $V_{GS}=10V$ (Note 3.4)	--	11.7	--	nc
Gate-Source Charge	$Q_{gs}$		--	4.04	--	
Gate-Drain Charge	$Q_{gd}$		--	5.37	--	

## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	$I_S$	Integral Reverse P-N Junction Diode in the MOSFET	--	--	5	A
Pulsed Source Current	$I_{SM}$		--	--	20	
Diode Forward Voltage	$V_{SD}$	$I_S=5A, V_{GS}=0V$	--	1.0	1.4	V
Reverse Recovery Time	$T_{rr}$	$I_F=5A, V_R=500V,$ $dI/dt=100A/\mu S$	--	44.6	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	59.6	--	nC

1. Pulse width limited by maximum junction temperature

2. L=10mH,  $I_{AS}=5.0A$ ,  $V_{DD}=100V$ ,  $V_G=10V$ ,  $R_G=25\Omega$ , starting  $T_J=25^\circ C$ 3. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ 

4. Essentially independent of operating temperature

## Typical Performance Characteristics

Figure 1. On-Region Characteristics

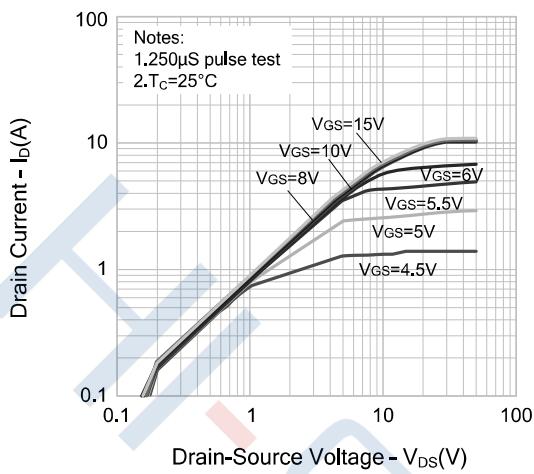


Figure 3. On-Resistance Variation vs. Drain Current

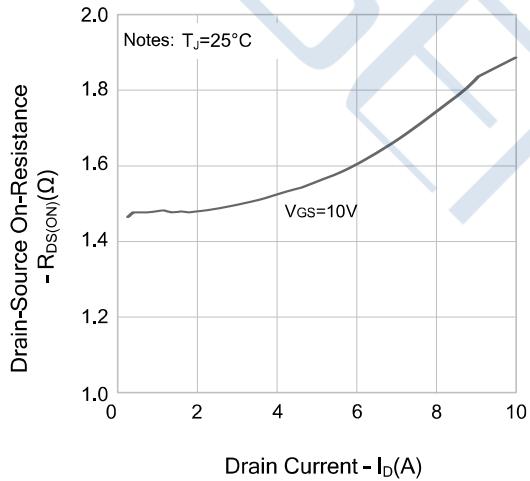


Figure 5. Capacitance Characteristics

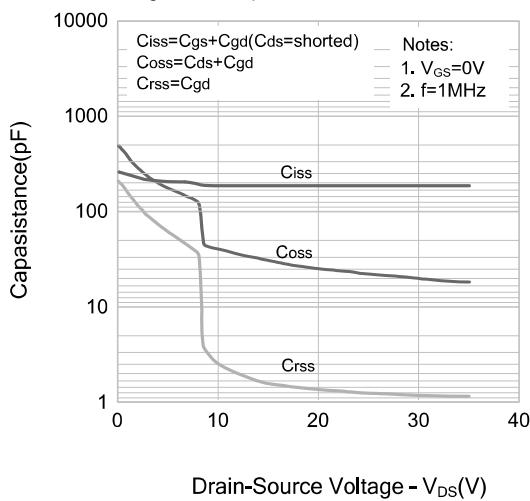


Figure 2. Transfer Characteristics

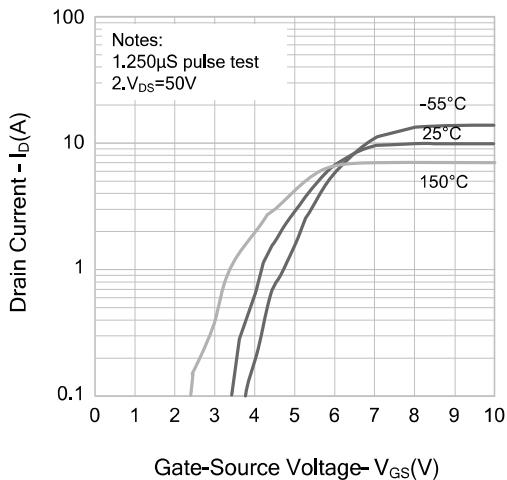


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

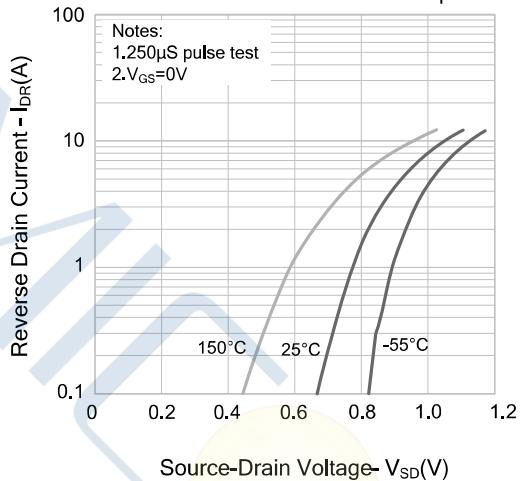
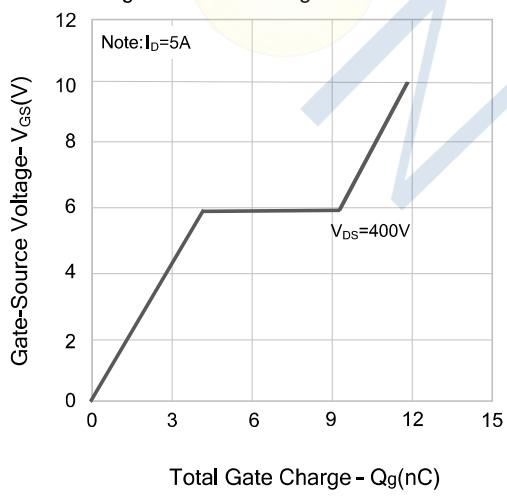


Figure 6. Gate Charge Characteristics



## Typical Performance Characteristics

Figure 7. Breakdown Voltage Variation vs. Temperature

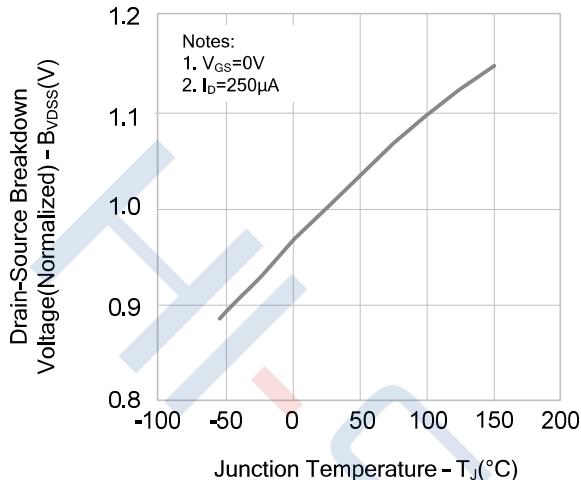


Figure 8. On-resistance Variation vs. Temperature

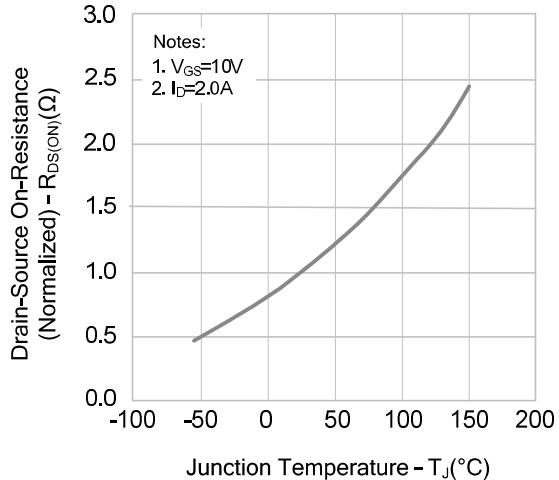


Figure 9 . Max. Safe Operating Area

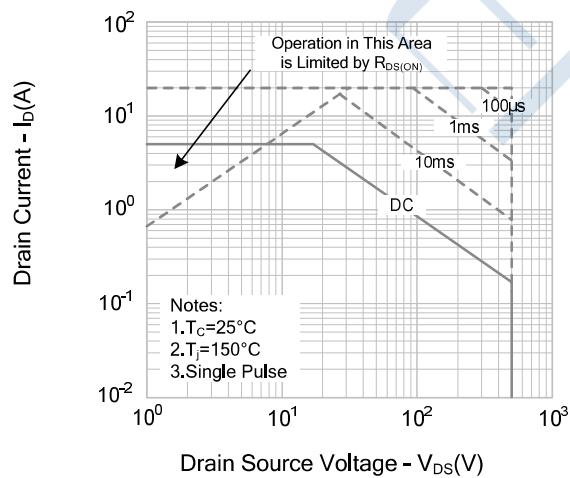
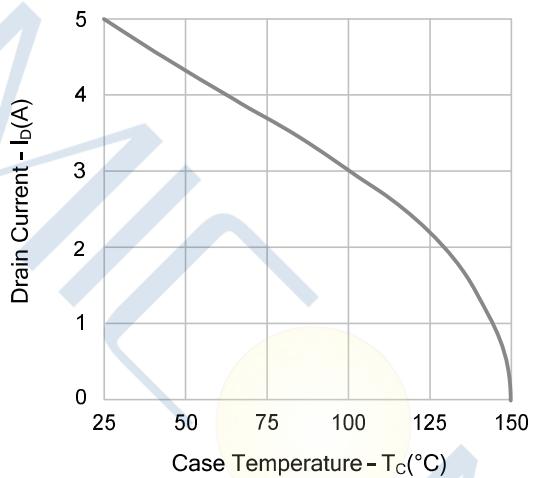
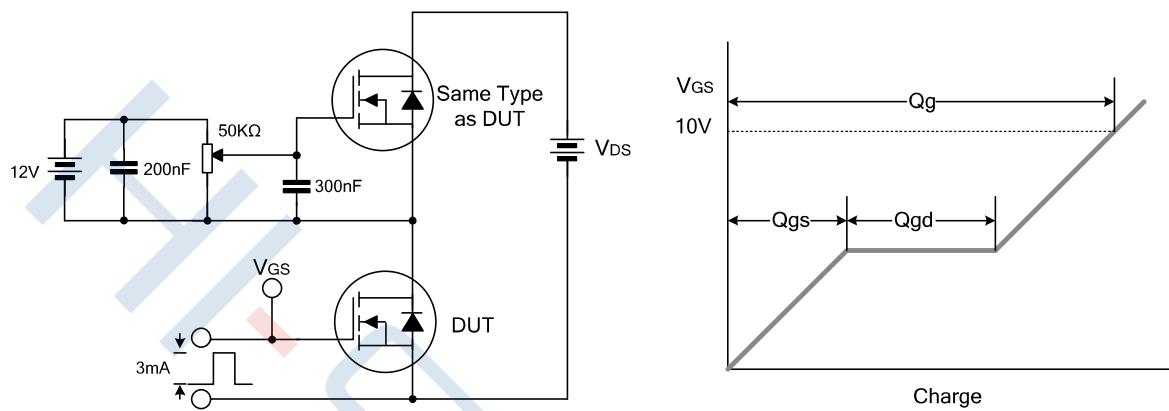


Figure 10. Maximum Drain Current vs. Case Temperature

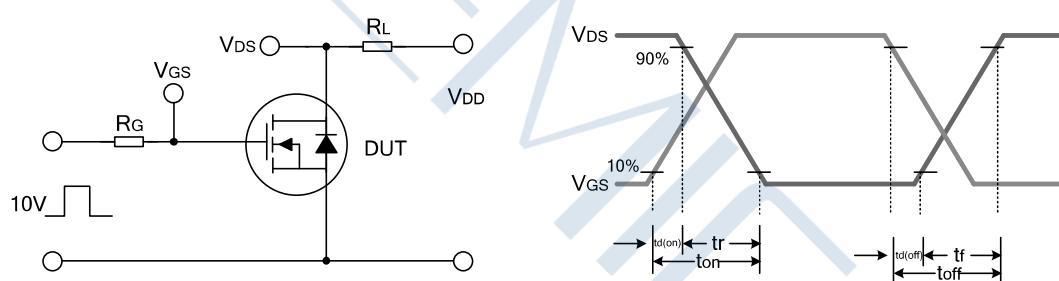


**Test Circuit**

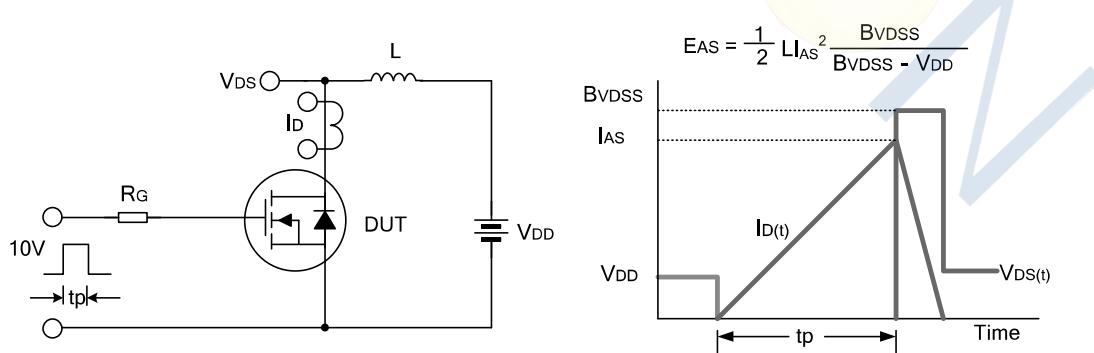
Gate Charge Test Circuit &amp; Waveform



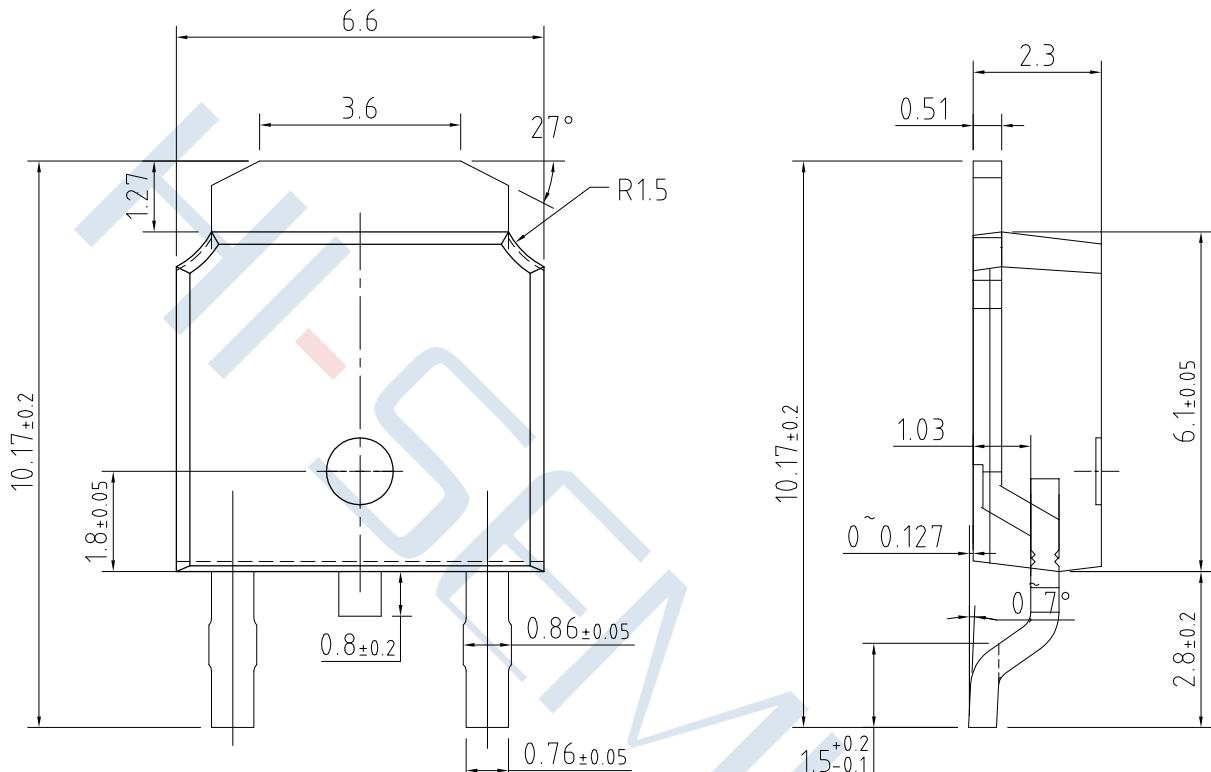
Resistive Switching Test Circuit &amp; Waveform



Unclamped Inductive Switching Test Circuit &amp; Waveform



## Package Dimensions of TO-252-2L



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- Hi-semicon will supply the best possible product for customers!