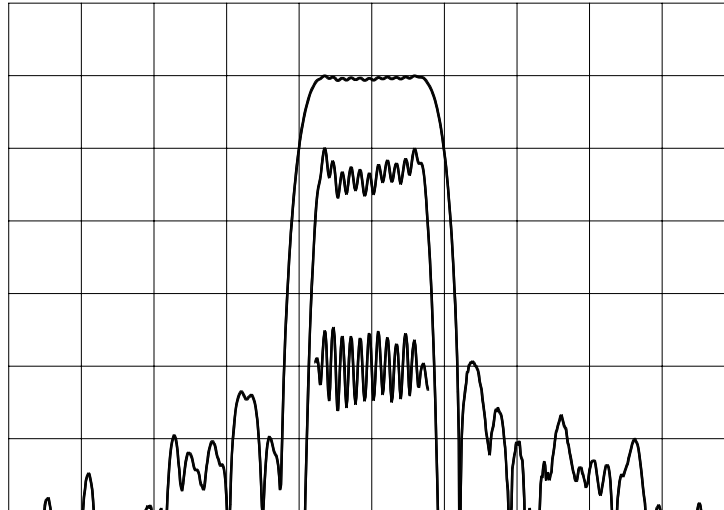




TYPICAL PERFORMANCE



Horizontal: 3 MHz/div

Vertical (from top):

Magnitude 10 dB/div

Magnitude 1 dB/div

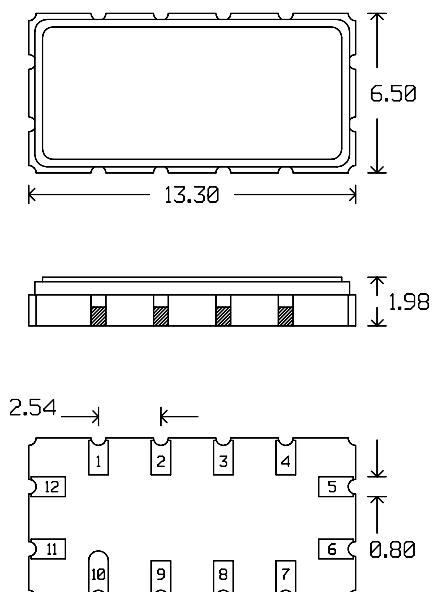
Group Delay 100 ns/div

SPECIFICATION

Parameter	Min	Typ	Max	Units
Center Frequency (Fc)		190.0		MHz
Insertion Loss		11	13	dB
1 dB Bandwidth		4.8		MHz
Lower 1 dB Frequency			188.0	MHz
Upper 1 dB Frequency	192.0			MHz
3 dB Bandwidth		5.2		MHz
Lower 3 dB Frequency			187.7	MHz
Upper 3 dB Frequency	192.3			MHz
10 dB Bandwidth		6.0		MHz
Lower 10 dB Frequency	186.6			MHz
Upper 10 dB Frequency			193.4	MHz
30 dB Bandwidth		6.9		MHz
Lower 30 dB Frequency	186.2			MHz
Upper 30 dB Frequency			193.8	MHz
Rejection				
30 MHz to Fc-13 MHz	50	55		dB
Fc-13 MHz to Fc-5 MHz	40	43		dB
Fc+5 MHz to Fc+13 MHz	40	44		dB
Fc+13 MHz to 300 MHz	50	55		dB
Passband Amplitude Ripple ¹		0.65	1.1	dB p-p
Passband Group Delay Ripple ¹		120	200	ns p-p
Source and Load Impedance		50		Ω
Operating Temperature Range	-10	23	+85	$^{\circ}\text{C}$

 Notes: 1. Over Fc $\pm$ 1.92 MHz.

PACKAGE OUTLINE

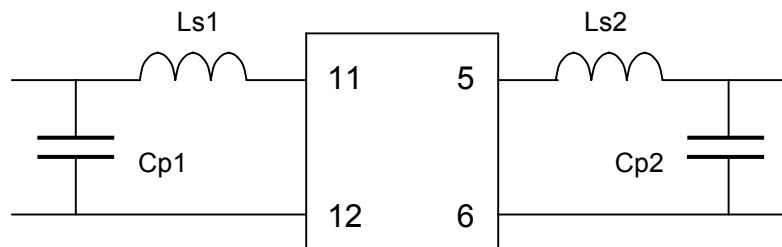


Units: mm

Pin Configuration:

Input: 11
 Input Return: 12
 Output: 5
 Output Return: 6
 Ground: 1, 2, 3, 4, 7, 8, 9, 10

MATCHING CIRCUIT



Component values in 50 Ω : $L_{s1} = 36$ nH $L_{s2} = 33$ nH
 (Minimum $Q = 45$) $C_{p1} = 39$ pF $C_{p2} = 45$ pF

Notes

- Optimum component values may change depending on board layout. The values shown here are intended as a guide only.
- Requires 2% matching components.

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