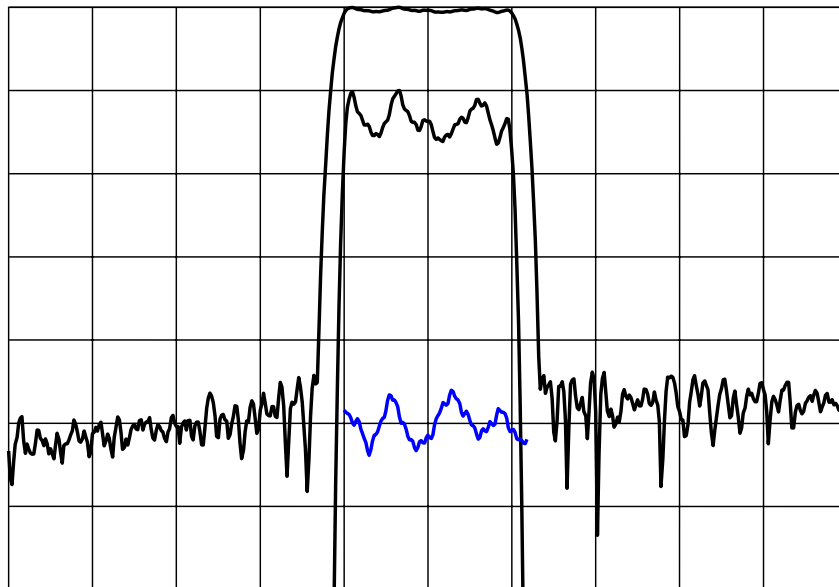




TYPICAL PERFORMANCE



Horizontal: 2.5 MHz/div

Vertical (from top):

Magnitude

10 dB/div

Magnitude

1 dB/div

Group Delay Deviation

50 ns/div

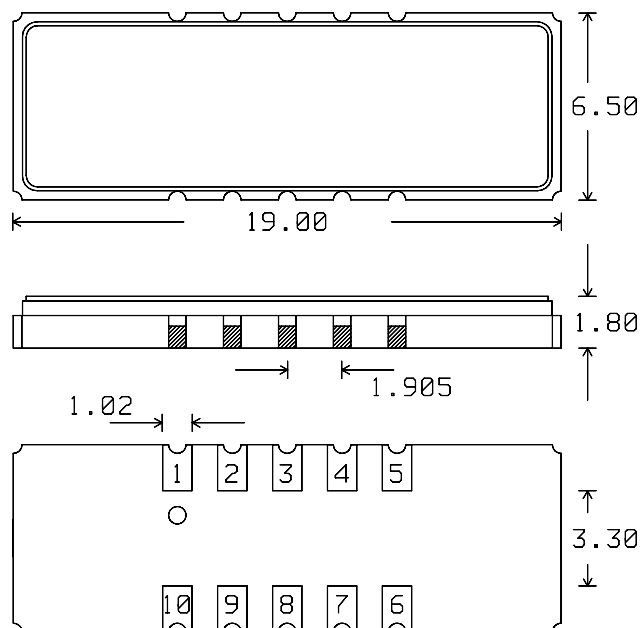
SPECIFICATION

Parameter ¹	Min.	Typ.	Max.	Units.
Center Frequency (Fc) ²	95.90	96.00	96.10	MHz
Minimum Insertion Loss ³			25	dB
1 dB Bandwidth	4.80	5.00		MHz
35 dB bandwidth		6.60	6.80	MHz
Passband Variation (Fc +/- 2.4 MHz)		0.6	0.8	dB p-p
Group Delay Variation (Fc +/- 2.4 MHz) ⁴		60	80	ns p-p
Change in Absolute Delay from unit to unit	-10		10	ns
Absolute Delay Variation from -5°C to 75°C			10	ns
Input Return Loss (Fc +/- 1.92 MHz)	10			dB
Output return Loss (Fc +/- 1.92 MHz)	10			dB
Absolute Delay		2.00	3.00	us
Rejection (40 to 87 MHz)	43	46		dB
(111 to 150 MHz)	43	46		dB
Source and Load Impedance	50			Ω
Substrate Material	LiTaO ₃			
Ambient Temperature		23		°C

- Notes:
1. All specifications for a device matched to a 50 ohm system impedance.
 2. Average of Lower and Upper 3 dB Frequencies.
 3. All dB values are referenced to the insertion loss
 4. A smoothing aperture of 0.125 MHz may be applied to this measurement.



PACKAGE OUTLINE

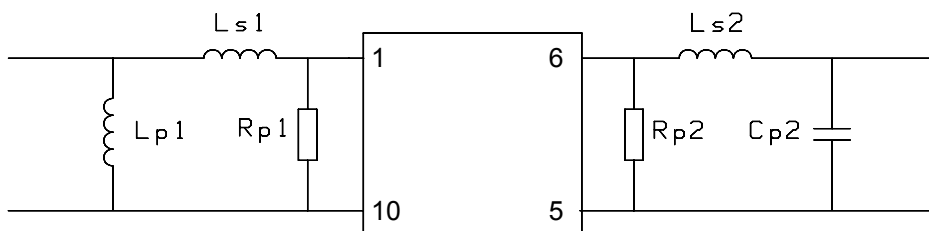


Units: mm

Pad Configuration:

Input: 1
Input Return: 10
Output: 6
Output Return: 5
Ground: 2,3,4,7,8,9

MATCHING CIRCUIT



Typical component values:

$L_{s1} = 6.8 \text{ nH}$
 $L_{p1} = 56 \text{ nH}$
 $R_{p1} = 100 \Omega$

$L_{s2} = 91 \text{ nH}$
 $C_{p2} = 47 \text{ pF}$
 $R_{p2} = 120 \Omega$

(Minimum inductor $Q = 40$)

Notes

- Recommend use of 2% tolerance components.
- Optimum values depend on board layout. Values intended as guide only.

ISO 9001
Registered

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