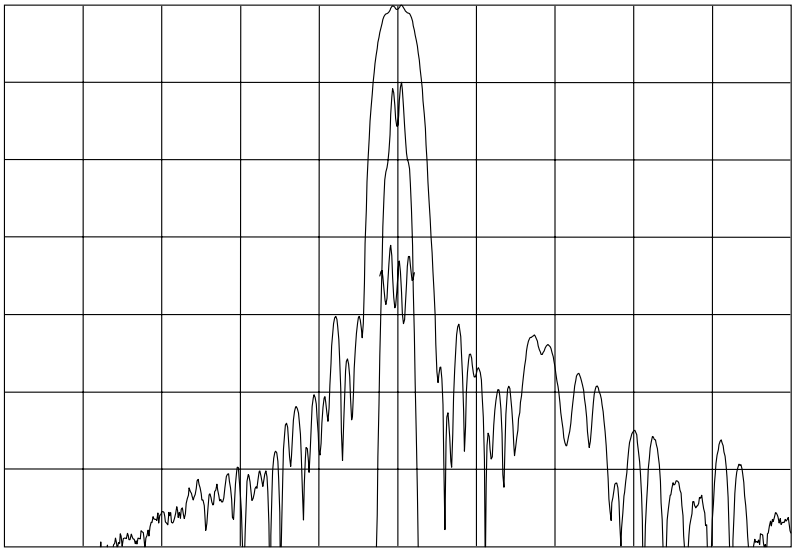
TYPICAL PERFORMANCE



Horizontal: 0.5 MHz/div

Vertical (from top):

Magnitude	10	dB/div
Magnitude	1	dB/div
Phase Linearity	5	deg/div

SPECIFICATION

Parameter	Min	Typ	Max	Units
Center Frequency (Fc) [1]		70.00		MHz
Minimum Insertion Loss [2]		11.5	13.0	dB
Lower 1 dB Frequency			69.80	MHz
Upper 1 dB Frequency	70.20			MHz
3 dB Bandwidth	0.60			MHz
Lower 25 dB Frequency	69.10			MHz
Upper 25 dB Frequency			70.90	MHz
Lower 35 dB Frequency	69.00			MHz
Upper 35 dB Frequency			71.00	MHz
Passband Variation (69.80 to 70.20 MHz)			1	dB p-p
Device Delay			2.40	us
Group Delay Variation (69.80 to 70.20 MHz)			250	ns p-p
Rejection (47.8 to 49.8 MHz) [3]	45			dB
Source and Load Impedance		50		Ω
Input Power		+10	+13	dBm
Operating Temperature Range	0	23	70	° C

Notes:
1. Average of the lower and upper 3 dB band edge frequencies.
2. All dB values are referenced to the insertion loss.
3. Ultimate rejection outside of this area from 0.5*Fc to 1.5*Fc will be 35 dB (min).

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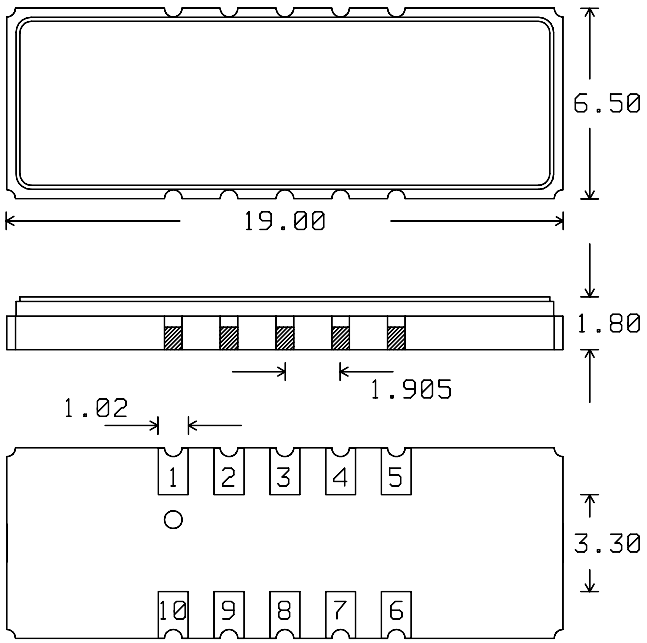
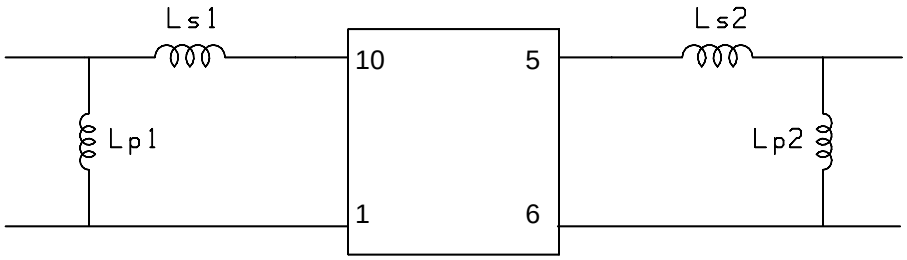
PACKAGE OUTLINE



Units: mm

Pad Configuration:
Input: 10
Input Return: 1
Output: 5
Output Return: 6
Ground: 2,3,4,7,8,9

MATCHING CIRCUIT



Typical component values: Ls1 = 363 nH Ls2 = 497 nH
Lp1 = 82 nH Lp2 = 120 nH

(Minimum inductor Q = 40)

Notes:

- Recommend use of 2% matching components.
- Optimum values depend on board layout. Values shown are intended as a guide only.

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