

Trench IGBT Modules

SEMiX603GB12E4Ip

Features

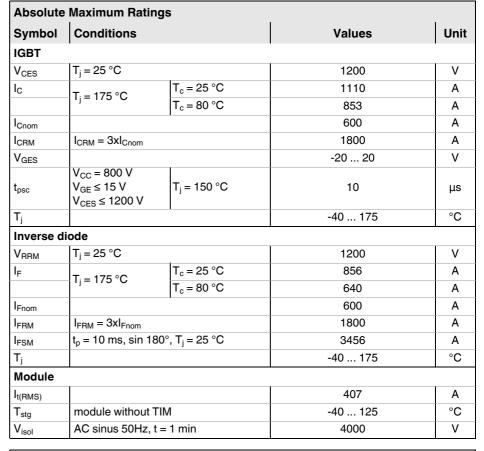
- · Homogeneous Si
- Trench = Trenchgate technology
- V_{CE(sat)} with positive temperature coefficient
- · High short circuit capability
- · Press-fit pins as auxiliary contacts
- Thermally optimized ceramic
- · Current sensing shunt resistor
- UL recognized, file no. E63532

Typical Applications*

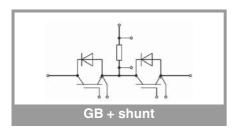
- · AC inverter drives
- UPS
- Renewable energy systems

Remarks

- Product reliability results are valid for T-150°C
- V_{isol} between temperature sensor and power section is only 2500V
- For storage and case temperature with TIM see document "TP(*) SEMiX 3p"



Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
IGBT	•					•	
$\begin{array}{c c} V_{CE(sat)} & I_{C} = 600 \text{ A} \\ V_{GE} = 15 \text{ V} \\ \text{chiplevel} \end{array}$	•	T _j = 25 °C		1.80	2.05	V	
	T _j = 150 °C		2.03	2.30	V		
V _{CE0}	chiplevel	T _j = 25 °C		0.87	1.01	V	
		T _j = 150 °C		0.77	0.90	V	
r _{CE}	r_{CE} $V_{GE} = 15 V$	T _j = 25 °C		1.55	1.73	mΩ	
	chiplevel	T _j = 150 °C		2.1	2.3	mΩ	
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_C=22.2$ mA		5.3	5.8	6.3	V	
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, $T_j = 25 ^{\circ}\text{C}$			5	mA	
C _{ies}	V 05 V	f = 1 MHz		37.5		nF	
Coes	V _{CE} = 25 V V _{GF} = 0 V	f = 1 MHz		2.31		nF	
C _{res}	I GL U	f = 1 MHz		2.04		nF	
Q_{G}	V _{GE} = - 8 V+ 15 V			3450		nC	
R _{Gint}	T _j = 25 °C			1.2		Ω	
t _{d(on)}	V _{CC} = 600 V	T _j = 150 °C		260		ns	
t _r	$\begin{array}{l} I_{C} = 600 \text{ A} \\ V_{GE} = +15/\text{-}15 \text{ V} \\ R_{G \text{ on}} = 1.5 \Omega \\ R_{G \text{ off}} = 1.5 \Omega \\ \text{di/dt}_{\text{on}} = 6800 \text{ A/}\mu\text{s} \\ \text{di/dt}_{\text{off}} = 3700 \text{ A/}\mu\text{s} \\ \text{du/dt} = 3400 \text{ V/}\mu\text{s} \\ L_{s} = 21 \text{ nH} \end{array}$	T _j = 150 °C		85		ns	
Eon		T _j = 150 °C		63		mJ	
t _{d(off)}		T _j = 150 °C		560		ns	
t _f		T _j = 150 °C		145		ns	
E _{off}		T _j = 150 °C		80		mJ	
R _{th(j-c)}	per IGBT				0.037	K/W	
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.035		K/W	
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.025		K/W	





SEMiX® 3p shunt

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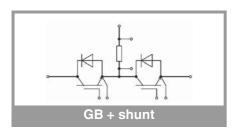
• Product reliability results are valid for $T_i=150^{\circ}C$

 V_{isol} between temperature sensor and power section is only 2500V

 For storage and case temperature with TIM see document "TP(*) SEMiX 3p"

Characteristics								
Symbol	Conditions		min.	typ.	max.	Unit		
Inverse diode								
$V_F = V_{EC}$	I _F = 600 A	T _j = 25 °C		2.08	2.44	V		
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.08	2.34	V		
V _{F0}	chiplevel	T _j = 25 °C		1.39	1.59	V		
		T _j = 150 °C		1.08	1.18	V		
r _F	chiplevel	T _j = 25 °C		1.16	1.42	mΩ		
		T _j = 150 °C		1.67	1.93	mΩ		
I _{RRM}	I _F = 600 A	T _j = 150 °C		465		Α		
Q _{rr}	di/dt _{off} = 6500 A/μs V _{GE} = -15 V	T _j = 150 °C		108		μC		
E _{rr}	$V_{CC} = 600 \text{ V}$	T _j = 150 °C		40		mJ		
R _{th(j-c)}	per diode				0.065	K/W		
R _{th(c-s)}	per diode (λ _{grease} =0	.81 W/(m*K))		0.039		K/W		
R _{th(c-s)}	per diode, pre-applied phase change material			0.031		K/W		
Module	•							
L _{CE}				20		nΗ		
R _{CC'+EE'}	measured per	T _C = 25 °C		1.2		mΩ		
	switch, shunt excluded	T _C = 125 °C		1.65		mΩ		
Rth _{(c-s)1}	calculated without thermal coupling		0.009			K/W		
Rth _{(c-s)2}	including thermal coupling, Ts underneath module $(\lambda_{grease}=0.81 \text{ W/} (\text{m}^*\text{K}))$		0.015			K/W		
Rth _{(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.011		K/W		
Ms	to heat sink (M5)		3		6	Nm		
M_{t}		to terminals (M6)	3		6	Nm		
						Nm		
W					350	g		
Temperat	ture Sensor							
R ₁₀₀	T _c =100°C (R ₂₅ =5 k	T _c =100°C (R ₂₅ =5 kΩ)		493 ± 5%		Ω		
B _{100/125}	$R_{(T)}=R_{100}exp[B_{100/125}(1/T-1/T_{100})];T[K];$			3550 ±2%		К		

Characteristics								
Symbol	Conditions	min.	typ.	max.	Unit			
Shunt	Shunt							
I _{Shunt}	$T_c = 100 ^{\circ}\text{C}, T_{Shunt,max} = 170 ^{\circ}\text{C}, \ R_{th} = 2.3 \text{K/W}$			407	А			
R _{Shunt}	Tolerance = ±5 %		0.19		mΩ			
α				75	ppm/K			



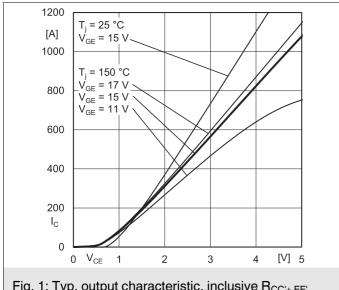


Fig. 1: Typ. output characteristic, inclusive R_{CC'+ EE'}

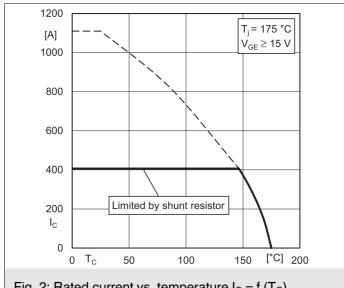


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

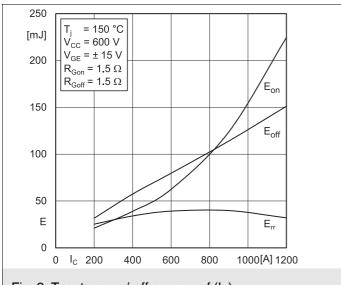


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

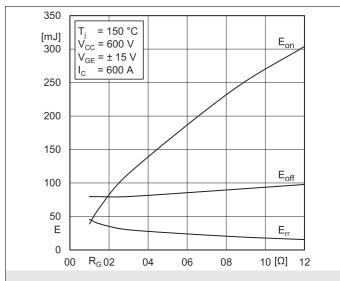


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

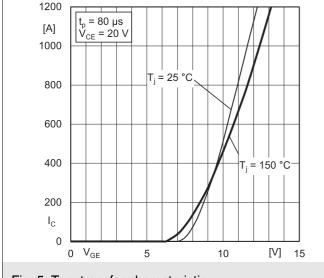


Fig. 5: Typ. transfer characteristic

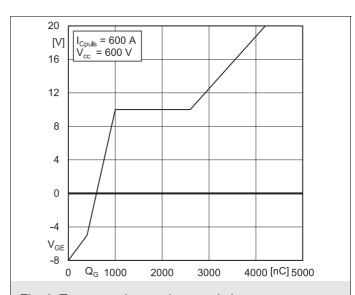
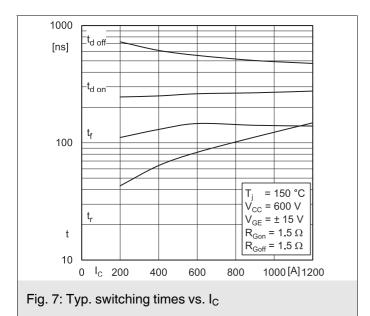
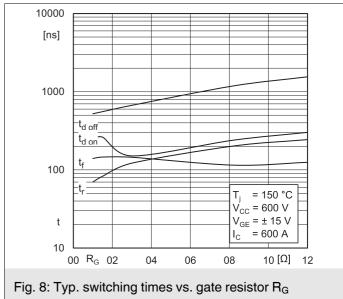
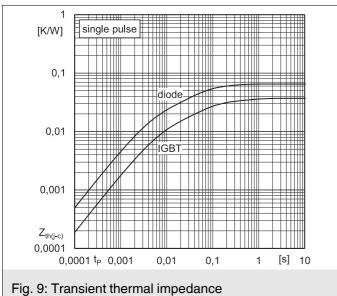
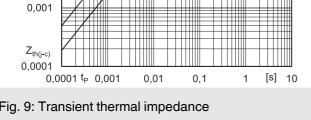


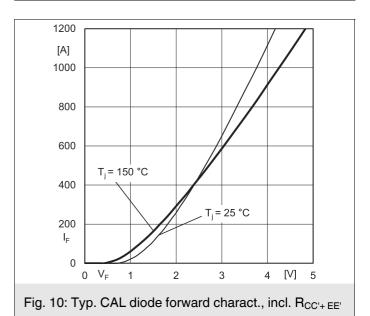
Fig. 6: Typ. gate charge characteristic









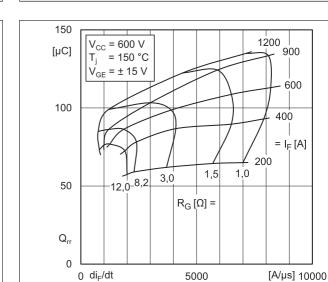


900

600

400

= I_F [A]



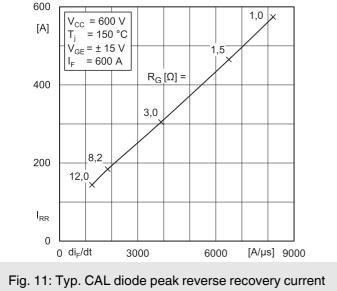
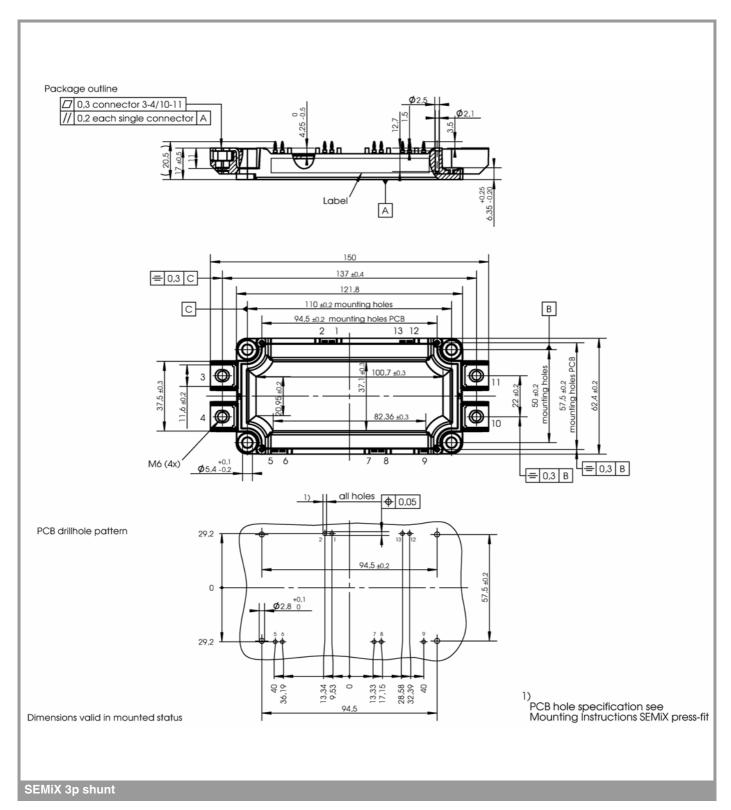
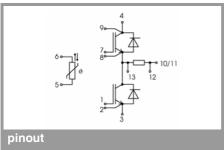


Fig. 12: Typ. CAL diode recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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