

SEMiX® 3p shunt

Trench IGBT Modules

SEMiX453GB12E4Ip

Features

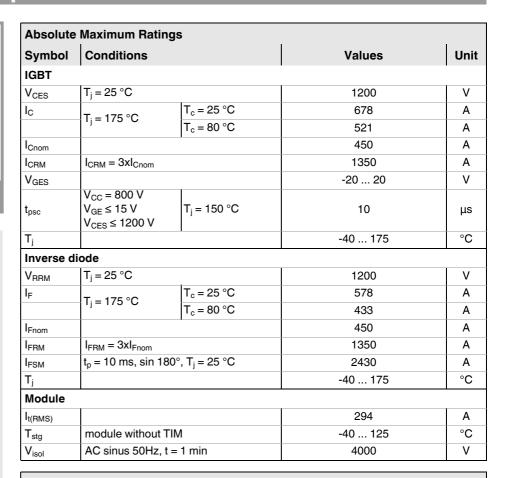
- · Homogeneous Si
- Trench = Trenchgate technology
- V_{CE(sat)} with positive temperature coefficient
- · High short circuit capability
- · Press-fit pins as auxiliary contacts
- · Current sensing shunt resistor
- UL recognized, file no. E63532

Typical Applications*

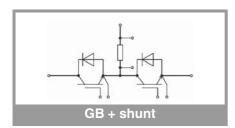
- · AC inverter drives
- UPS
- Renewable energy systems

Remarks

- Product reliability results are valid for T_j=150°C
- V_{isol} between temperature sensor and power section is only 2500V
- For storage and case temperature with TIM see document "TP(*) SEMiX 3p"



Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						•
• CE(Sai)	$I_{\rm C} = 450 {\rm A}$	T _j = 25 °C		1.80	2.05	V
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.19	2.40	V
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V
		T _j = 150 °C		0.70	0.80	V
r_{CE} $V_{GE} = 15$	V _{GE} = 15 V	T _j = 25 °C		2.2	2.6	mΩ
	chiplevel	T _j = 150 °C		3.3	3.6	mΩ
$V_{\text{GE(th)}}$	V _{GE} =V _{CE} , I _C = 18 mA		5	5.8	6.5	V
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, T _j = 25 °C			5	mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		27.9		nF
Coes		f = 1 MHz		1.74		nF
C _{res}		f = 1 MHz		1.53		nF
Q_{G}	V _{GE} = - 8 V+ 15 V			2550		nC
R _{Gint}	T _j = 25 °C			1.7		Ω
t _{d(on)}	$V_{CC} = 600 \text{ V}$ $I_{C} = 450 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1.1 \Omega$ $R_{G \text{ off}} = 1.1 \Omega$ $di/dt_{on} = 6600 \text{ A/µs}$	T _j = 150 °C		195		ns
t _r		T _j = 150 °C		67		ns
Eon		T _j = 150 °C		33		mJ
t _{d(off)}		T _j = 150 °C		505		ns
t _f		T _j = 150 °C		110		ns
E _{off}	$\begin{array}{l} \text{di/dt}_{\text{off}} = 3400 \text{ A/}\mu\text{s} \\ \text{du/dt} = 4800 \text{ V/}\mu\text{s} \\ \text{L}_{\text{s}} = 21 \text{ nH} \end{array}$	T _j = 150 °C		57		mJ
R _{th(j-c)}	per IGBT				0.066	K/W
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.03		K/W
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.021		K/W





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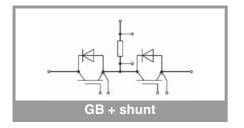
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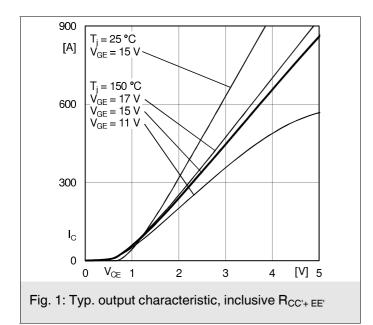
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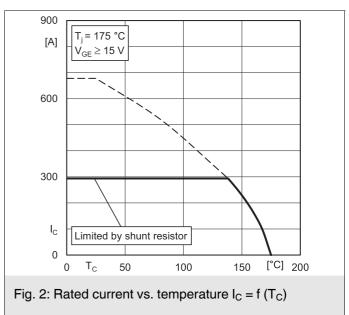
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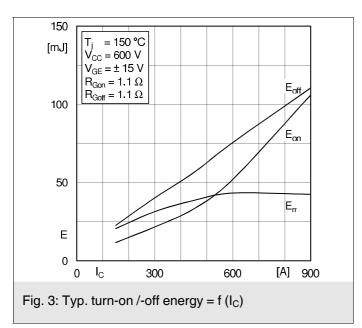
Characte	ristics							
Symbol	Conditions		min.	typ.	max.	Unit		
Inverse diode								
$V_F = V_{EC}$	I _F = 450 A	T _j = 25 °C		2.14	2.46	V		
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.07	2.38	V		
V _{F0} chiplevel	ahinlayal	T _j = 25 °C		1.30	1.50	V		
	Chipievei	T _j = 150 °C		0.90	1.10	V		
r _F chip	chiplevel	T _j = 25 °C		1.87	2.1	mΩ		
	Chipievei	T _j = 150 °C		2.6	2.8	mΩ		
I _{RRM}	I _F = 450 A	T _j = 150 °C		455		Α		
Q _{rr}	di/dt _{off} = 6800 A/μs V _{GE} = -15 V	T _j = 150 °C		85		μC		
E _{rr}	$V_{CC} = 600 \text{ V}$	T _j = 150 °C		39		mJ		
R _{th(j-c)}	per diode				0.1	K/W		
R _{th(c-s)}	per diode (λ _{grease} =0).81 W/(m*K))		0.045		K/W		
R _{th(c-s)}	per diode, pre-applied phase change material			0.036		K/W		
Module						•		
L _{CE}				20		nΗ		
R _{CC'+EE'}	measured per	T _C = 25 °C		1.2		mΩ		
	switch, shunt excluded	T _C = 125 °C		1.65		mΩ		
Rth _{(c-s)1}	calculated without thermal coupling		0.009			K/W		
Rth _{(c-s)2}	including thermal coupling, Ts underneath module (λ _{grease} =0.81 W/ (m*K))			0.014		K/W		
Rth _{(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.011		K/W		
Ms	to heat sink (M5)		3		6	Nm		
M_t		to terminals (M6)	3		6	Nm		
						Nm		
W					350	g		
Temperat	ure Sensor							
R ₁₀₀	T _c =100°C (R ₂₅ =5 k	T _c =100°C (R ₂₅ =5 kΩ)		493 ± 5%		Ω		
B _{100/125}	$R_{(T)} = R_{100} exp[B_{100/125}(1/T-1/T_{100})]; T[K];$			3550 ±2%		К		

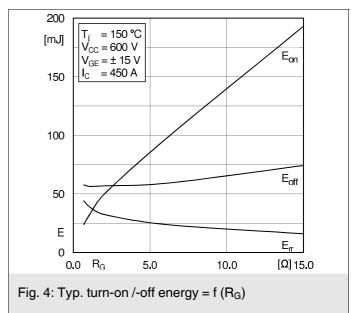
Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Shunt						
I _{Shunt}	$T_c = 100$ °C, $T_{Shunt,max} = 170$ °C, $R_{th} = 2.9$ K/W			294	Α	
R _{Shunt}	Tolerance = ±1 %		0.29		mΩ	
α				50	ppm/K	

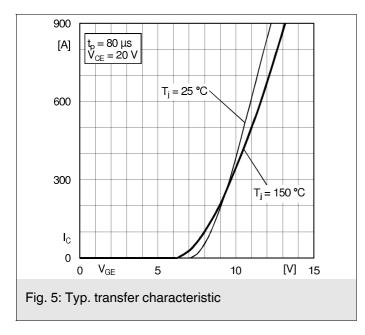


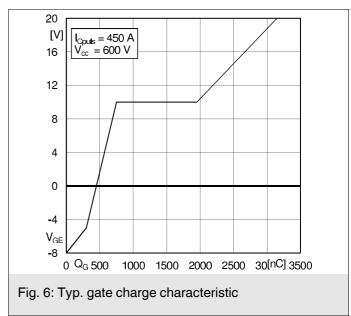


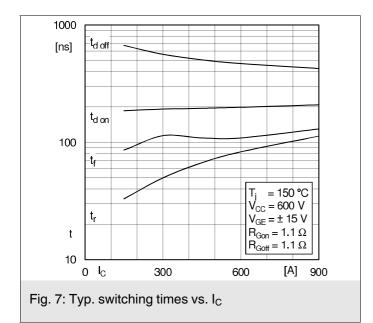


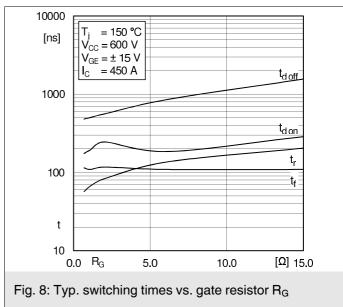


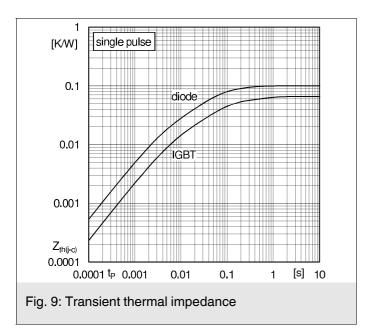


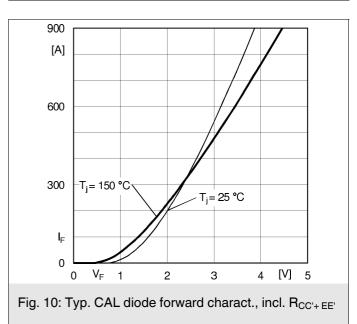


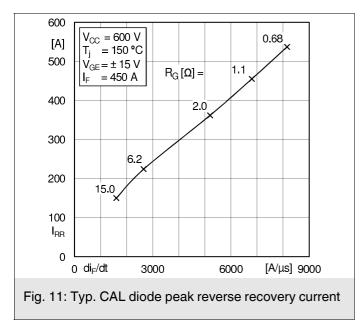


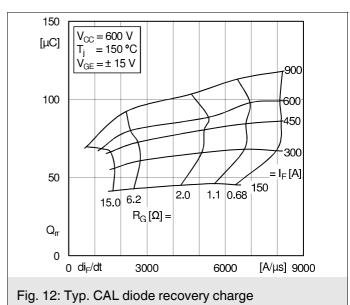


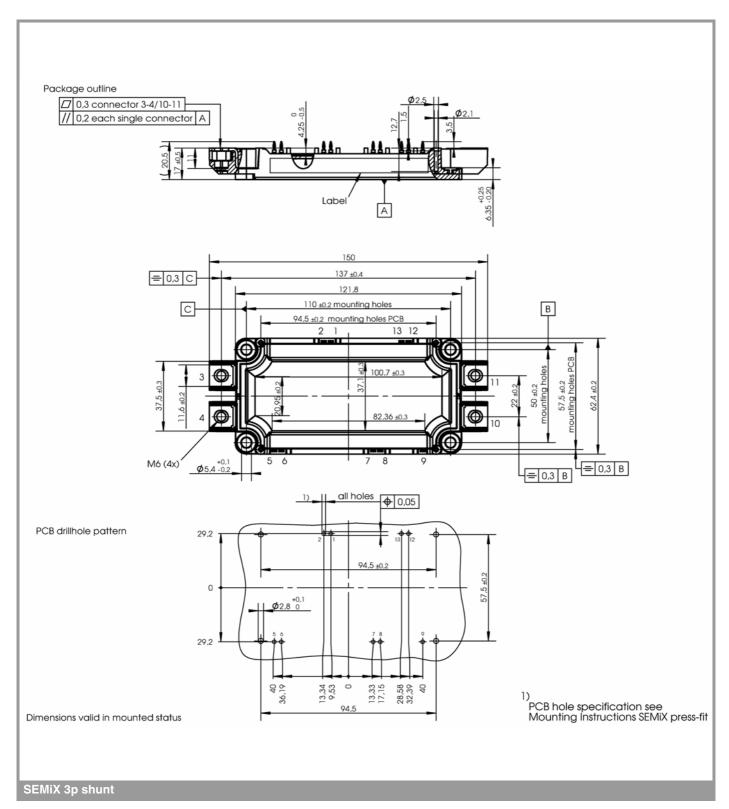


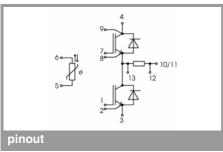












This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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