Small Signal Zener Diode

General Description

These diodes small signal Zener diodes, fabricated in planar technology. Miniature surface mount package is excellent for hand-held and portable applications where is space is limited.

Features and Benefits

- Silicon epitaxial planar diode
- Low Zener impedance and low leakage current
- Standard Zener voltage tolerance is 4.3%.

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- Full lead (Pb)-free device and RoHS compliant device
- Available in "Green" device

Applications

• Voltage regulator

Ordering Information

Part Number	Marking Code	Package	Packaging
SDZ4V3D	U9 🗆	SOD-323	Tape & Reel

Marking Information

- U 9 = Specific Device Code
- □ = Year & Week Code Marking
 - = Color band denote cathode

Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Cathode		
2	Anode		





SOD-323





SDZ4V3D

SDZ4V3D

Absolute Maximum Ratings (Tamb=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Power dissipation ¹⁾	P _D	200	mW
Operating junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55℃ to +150℃	°C

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Thermal Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient ¹⁾	$R_{th(j-a)}$	625	°C/W

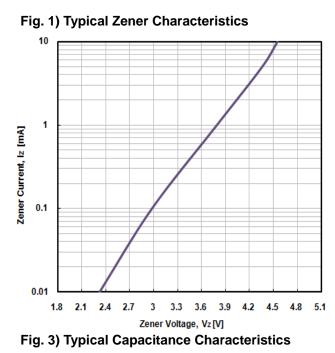
¹⁾ Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Zener voltage	Vz	I _Z =5mA	4.11	-	4.49	V
Dynamic impedance	Z _{ZT}	I _Z =5mA	-	-	88	Ω
KNEE dynamic impedance	Z _{ZK}	I _Z =0.25mA	-	-	2500	Ω
Reverse leakage current	I _R	V _R =1V	-	-	5	μA

SDZ4V3D

Rating and Characteristic Curves



1000 f = 1 [MHz], Ta = 25 ℃ Total Capacitance, C⊤ [pF] 100 10 0 0.5 1 1.5 2 2.5 3 DC Reverse Voltage, Vr [V]

Vz [V] @ Iz = 5 [mA] 4.5 4.2

Fig. 2) Zener voltage vs. Ambient Temperature

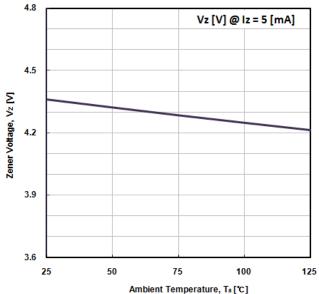
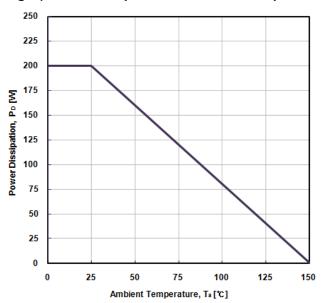
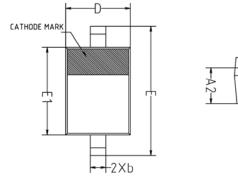
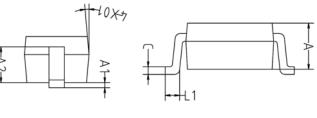


Fig. 4) Power Dissipation vs. Ambient Temperature



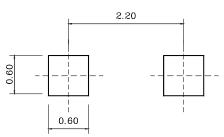
Package Outline Dimensions





SYMBOL	١	NOTE		
STIDUE	MINIMUM	NOMINAL	MAXIMUM	NOTE
A	0.850	-	0.950	
A1	0.000	-	0.100	
A2	0.650	0.700	0.750	
b	0.250	0.300	0.350	
с	0.110	0.150	0.190	
D	1.200	1.250	1.300	
E	2.400	2.500	2.600	
E1	1.650	1.700	1.750	
L1	0.200	-	0.300	
0 2		5° REF		

% Recommend PCB solder land (Unit : mm)



SDZ4V3D

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