

SDZ3V9D

ZENER DIODE

Small Signal Zener Diode

General Description

These diodes small signal Zener diodes, fabricated in planar technology. Miniature surface mount package is excellent for hand-held and portable applications where is space is limited.

Features and Benefits

- Silicon epitaxial planar diode
- Low Zener impedance and low leakage current
- Standard Zener voltage tolerance is 4.3%.
- Full lead (Pb)-free device and RoHS compliant device
- · Available in "Green" device



SOD-323







Applications

Voltage regulator

Ordering Information

Part Number	Marking Code	Package	Packaging
SDZ3V9D	U8 □	SOD-323	Tape & Reel

Marking Information



U 8 = Specific Device Code

☐ = Year & Week Code Marking

= Color band denote cathode

Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Cathode	1 2	
2	Anode		\ \ \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\

Absolute Maximum Ratings (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Power dissipation 1)	P_{D}	200	mW
Operating junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55°C to +150°C	°C

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Thermal Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient 1)	R _{th(j-a)}	625	°C/W

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Zener voltage	Vz	I _Z =5mA	3.73	-	4.07	V
Dynamic impedance	Z _{ZT}	I _Z =5mA	-	-	90	Ω
KNEE dynamic impedance	Z _{ZK}	I _Z =0.25mA	-	-	2400	Ω
Reverse leakage current	I _R	V _R =1V	-	-	10	μА

Rating and Characteristic Curves

Fig. 1) Typical Zener Characteristics

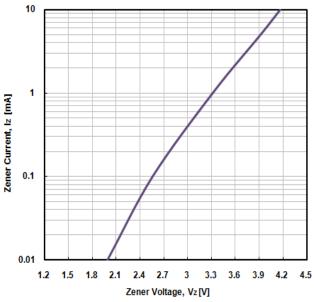


Fig. 3) Typical Capacitance Characteristics

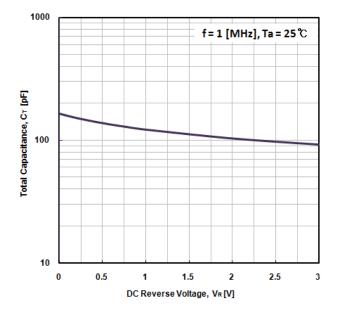


Fig. 2) Zener voltage vs. Ambient Temperature

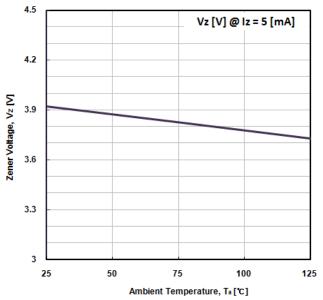
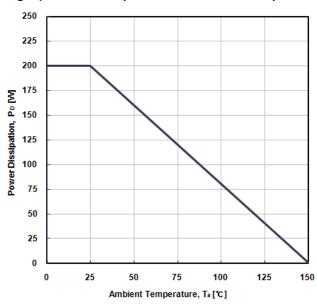
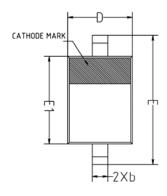
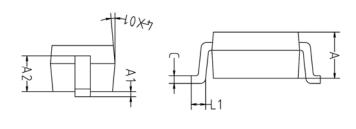


Fig. 4) Power Dissipation vs. Ambient Temperature



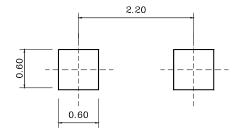
Package Outline Dimensions





SYMBOL	1	NOTE		
STITIBUL	MINIMUM	NOMINAL	MAXIMUM	INOTE
Α	0.850	-	0.950	
A1	0.000	-	0.100	
A2	0.650	0.700	0.750	
Ь	0.250	0.300	0.350	
С	0.110	0.150	0.190	
D	1.200	1.250	1.300	
E	2.400	2.500	2.600	
E1	1.650	1.700	1.750	
L1	0.200	-	0.300	
0 2		5° REF		

X Recommend PCB solder land (Unit : mm)



SDZ3V9D

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