

# 1A HIGH POWER LED DRIVER WITH 6~60V INPUT

## DESCRIPTION

The SD42527 is a step-down PWM control LED driver with a builtin power MOSFET. It achieves 1A continuous output current in 6~60V input voltage range. It provides thermal shutdown circuit, current limit circuit and PWM dimming circuit.

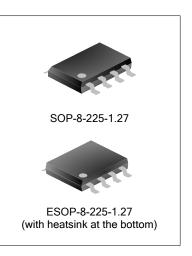
The SD42527 adopts current mode control which provides fast transient response, excellent constant current characteristic and simple loop stabilization design. It has high efficiency up to 96%, and has excellent constant current characteristic. When the input/output voltage changed, the output current change rate is within  $\pm 1.5\%$ .

## **FEATURES**

- \* 6~60V input voltage range;
- \* Maximum 1A output current;
- \*  $0.60\Omega$  built-in power MOSFET;
- \* Integrate Jitter function;
- \* Thermal compensation;
- \* PWM dimming function;
- \* 300kHz fixed frequency;
- \* Output current accuracy between ICs: ±3%;
- \* High efficiency up to 96%;
- \* Over temperature protection;
- \* Cycle-by-cycle over current protection.
- \* Dedicated current limit
- \* Open-circuit detection & protection
- \* ADJ voltage detection
- \* Short-circuit protection
- \* 90% Maximum duty cycle operation

## **ORDERING INFORMATION**

Part No.	Package	Marking	Material	Packing
SD42527	SOP-8-225-1.27	SD42527	Pb free	Tube
SD42527TR	SOP-8-225-1.27	SD42527	Pb free	Tape&Reel
SD42527E	ESOP-8-225-1.27	SD42527E	Pb free	Tube
SD42527ETR	ESOP-8-225-1.27	SD42527E	Pb free	Tape&Reel

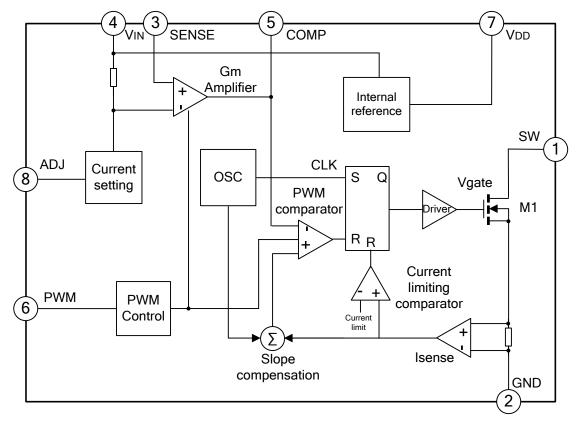


## APPLICATIONS

- \* LED building illumination;
- \* LED street lamp.



# **BLOCK DIAGRAM**



# ABSOLUTE MAXIMUM RATING

Characteristics	Symbol	Ratings	Unit
Supply Voltage	V <sub>IN</sub>	60	V
Switch Voltage	V <sub>SW</sub>	80	V
PWM Voltage	V <sub>PWM</sub>	-0.3~6	V
Comp Voltage	V <sub>COMP</sub>	-0.3~6	V
ADJ Voltage	V <sub>ADJ</sub>	-0.3~6	V
VDD Voltage	V <sub>DD</sub>	-0.3~6	V
SENSE voltage	V <sub>SENSE</sub>	-0.3~V <sub>IN</sub>	V
Junction Temperature	Ti	150	°C
Lead Temperature	T∟	260	°C
Operating Temperature Range	T <sub>OPR</sub>	-20~85	°C
Storage Temperature Range	T <sub>STG</sub>	-40~125	°C

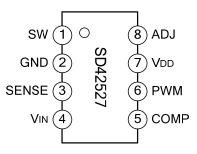
# $\label{eq:expected_$

Characteristics	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Supply Voltage	V <sub>IN</sub>		6		60	V
Operating Current	I <sub>IN</sub>	V <sub>IN</sub> =6/60V, V <sub>PWM</sub> =5V		1.4	2.0	mA
Upper Switch Leakage	I <sub>L</sub>	V <sub>sw</sub> =80V		0	5	μA



Characteristics	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Current Limit	I <sub>LIM</sub>	V <sub>IN</sub> =V <sub>SENSE</sub> =12V	1.4	1.6	1.8	А
Maximum Duty Cycle	D <sub>max</sub>		85	90		%
Oscillator Frequency	f <sub>osc</sub>		270	300	330	kHz
PWM Dimming on Voltage	V <sub>PWMON</sub>	V <sub>PWM</sub> is from 0V to 5V On voltage	2.2	2.4	2.6	V
PWM Dimming Off Voltage	Vpwmoff	V <sub>PWM</sub> is from 5V to 0V Off voltage	1.9	2	2.1	V
PWM pull-up current	I <sub>PWM1</sub>	V <sub>PWM</sub> =0V		0		μA
PWM pull-down current	I <sub>PWM2</sub>	V <sub>PWM</sub> =5V	20	30	40	μA
COMP Clamp Voltage	V <sub>COMP</sub>	Open without load		0		V
Switch Transistor on Resistance	R <sub>ON</sub>	1LED, I <sub>OUT</sub> =0.5A		0.6	0.8	Ω
VDD voltage	V <sub>DD</sub>	No load	5.3	5.5	5.7	V
ADJ threshold voltage	V <sub>ADJ</sub>	V <sub>ADJ</sub> is from 5V to 0V, when output current decreases	1.20	1.22	1.25	V
ADJ off voltage value	V <sub>ADJOFF</sub>	V <sub>ADJ</sub> decreases from 1 V to 0V		80		mV
ADJ on voltage value	V <sub>ADJON</sub>	V <sub>ADJ</sub> increases from 0V to 1V		90		mV
Sense voltage threshold value	V <sub>IN</sub> -V <sub>SENSE</sub>	Voltage difference of $V_{\text{IN}}$ and $V_{\text{SENSE}}$		100		mV
Over temperature protection threshold value	T <sub>OVP</sub>	Temperature increases		150		°C
Over temperature protection recovery value	T <sub>RECOVERY</sub>	Temperature decreases		130		°C

# PIN CONFIGURATION



# **PIN DESCRIPTION**

Pin No.	Pin Name	I/O	Description
1	SW	Ι	Power output.
2	GND	0	Ground.
3	SENSE	Ι	Current sense pin.

Pin No.	Pin Name	I/O	Description
4	V <sub>IN</sub>	0	Input supply voltage.
5	COMP	I/O	Compensation pin, connects to external capacitor.
6	PWM	Ι	PWM dimming pin, connects PWM signal to change the output current.
7	V <sub>DD</sub>	-	5V reference output.
8	ADJ	I	Thermal compensation pin/linear dimming pin.

# **FUNCTION DESCRIPTION**

The SD42527 is a current mode LED driver. The voltage on compensation pin COMP is proportional to the current delivered to the load.

At the beginning of a cycle: the upper transistor M1 is off; the COMP pin voltage is higher than the current sense amplifier output; and the PWM comparator's output is low. The rising edge of the 300KHz CLK signal sets the RS Flip-Flop whose output turns on M1 thus  $V_{IN}$ ,  $R_S$ , LED, inductor and M1 comprises a circuit loop and the inductor current increases. The inductor current is sensed and amplified by the current sense amplifier. Ramp compensation is summed to current sense amplifier output and compared to the Gm amplifier output by the PWM comparator. When the current sense amplifier plus ramp compensation signal exceeds the comp pin voltage, the RS Flip-Flop is reset and the chip reverts to its initial M1 off state. The inductor energy storage released via the circuit loop composed by  $R_S$ , LED, inductor and M1 and the current sense amplifier plus slope compensation signal does not exceed the comp voltage in one cycle, then the falling edge of the CLK resets the Flip-Flop.

The Gm amplifier compares the output current with the threshold current (threshold current is set by internal). When the output current is higher than threshold current, the COMP pin's voltage is lower down. Since the COMP pin's voltage is proportional to the peak inductor current a decrease in its voltage decreases current delivered to the output. When the output current is lower than threshold current, the COMP pin's voltage is up while the output current increases. The output current is stable at the set value by adjusting the circuit loop.

## 1. PWM dimming function

SD42527 has internal PWM dimming function. When PWM pin connects to high level, the chip is working normally; when PWM pin is open or connects to low level, COMP pin disconnects with the chip, the charge of the capacitor is in hold state, voltage level holds, and the output of PWM comparator is high, transistor is off without output current. When PWM signal is high, COMP pin is connected with the chip to improve the startup speed of the chip. Adjust the output current by control the duty of external PWM signal.

The minimum setup time of SD42527 PWM dimming is less than  $20 \,\mu$ S, and the maximum PWM dimming ratio is up to 500:1. When high dimming ratio is needed, the dimming frequency is recommended 500Hz below; or else the dimming frequency can be up to 2 KHz.

## 2. Output current setting

The output current is determined by the sampling resistor and setting voltage. The sampling voltage of  $V_{IN}$ - $V_{SENSE}$ (Drop voltage on  $R_S$ ) is 100mV, and adjust the output current by adjusting the sampling resistor  $R_S$  (refer to Typical Application Circuit).



$$I_{OUT} = \frac{V_{IN} - V_{SENSE}}{R_s}$$

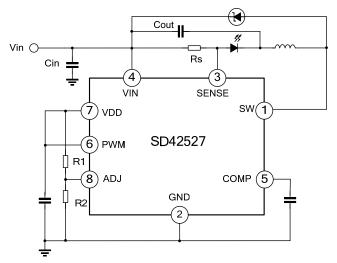
## 3. Current limiting

SD42527 is current mode IC with internal cycle-by-cycle current limiting function. Voltage on COMP should be no higher than 2.2V. The current limiting comparator is adopted, so the peak current of MOS is limited at about 1.6A.

## 4. Frequency jitter

SD42527 has internal frequency jitter function to improve the EMI performance of the system. The internal frequency is hopping in a very small range to reduce the single frequency radiation which simplifies the EMI design.

## 5. Thermal compensation and linear dimming function



SD42527 has linear dimming and thermal compensation functions.

Linear dimming function: Set the output current by adjust the ratio of R1 and R2. The relation between voltage on pin ADJ and output current lout is shown below:

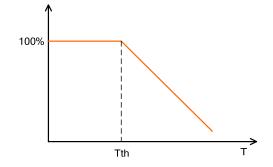
$$\begin{split} V_{ADJ} &\geq 1.22V \ , \ I_{OUT} = \frac{0.1}{R_s} \\ 0.08V &< V_{ADJ} < 1.22V \ , \ \ I_{OUT} = \frac{0.1}{R_s} \times \frac{V_{ADJ}}{1.22} \\ V_{ADJ} &< 0.08V \ , \ \ I_{OUT} = 0 \end{split}$$

ADJ voltage is auto measured. When it is lower than 0.08V,  $I_{OUT}=0$ . Then LED can be well turned off in linear dimming.

Thermal compensation function: the application circuit is show n above, use NTC resistor to take place of R2 for therma I compensation. In normal t emperature, the voltage on R2 is higher than 1.22V, and 100% current is delivered to outp ut; when LED temperature detected by R2 is higher th an the setting value Tth, the voltage on R2 is lo wer than 1.22V, and output current w ill decrease following the rising temperature. Connect NTC R2 with other resistor in series to get different LED falling curves under different current. If thermal compensation and linear dimming are not used, connect ADJ to  $V_{DD}$ .







The relation between LED output current and temperature

## 6. Over temperature protection

When the temperature is 150°C above, this protection w orks and comp voltage is pulled dow n, MOS is turned off. And all these are recovered when temperature falls to 130°C below.

## 7. Open-circuit detection & protection

SD42527 ado pt open-circuit detection, a nd voltage at comp pin is p ulled to 0 V. So Comp voltage and inductor current increase from 0V in operation to decrease overshoot current for the safety.

#### 8. Short-circuit protection

SD42527 can protect IC when output is shorted in operation. It adjusts Comp voltage and lower the switching frequency, also the switch current is limited by the current limiting comparator.

#### 9. Maximum duty cycle operation

When SD42527 operates at maximum duty cycle or larger, the switching frequency will fall following that Vin drops, but output current will not drop until Vin drops below Vout.

## **COMPONENTS SELECTION**

#### 1. Input Capacitor Selection

The input capacitor provides the pulse current when the power MOSFET is on, and charge the capacitor when the power MOSFET is off, thus to keep the stability of the input voltage. The input capacitor is recommended to be more than  $47\mu$ F, which can reduce the peak current drawn from input source and the switch noise. The input capacitor should be near to the input pin in real routing.

## 2. Output Capacitance Selection

Parallel connecting a capacitor between the two ends of LED can reduce the output voltage ripple accordingly reduce the ripple current of LED, while this capacitor will not effect the operating frequency and efficiency, but the start time will be longer by reducing the rising speed of the voltage on LED. The larger the output capacitor is, the smaller the current ripple on LED is. It is recommend use the capacitor of  $2.2\mu$ F or larger.

#### 3. Inductance Selection

The inductance is used to keep the output current constant, the bigger the inductance is, the smaller the



output current ripple is; while the bigger the physical size is, the bigger the series-wound resistor is. The selected effective current (RMS current rating)of inductance current should be bigger than the maximum output current, and the saturation current should be 30% bigger than maximum output current. In order to improve the efficiency, the series-wound resistor (DCR) of inductance should be smaller than  $0.2\Omega$ . The relation between inductance and ripple:

$$L = V_{OUT} * \frac{V_{IN} - V_{OUT}}{V_{IN} * f * \Delta I}$$

Where: L: Inductance value

F: Oscillator frequency

 $\Delta I$  is ripple current

When select inductance, you should consider the combination of various factors to select suitable inductance.

## 4. Diode Selection

SD42527 is a non-synchronous step-down adjuster, so the diode should provide continuous current when the power MOSFET is off. Because the forward voltage of Schottky diode is small, and the reverse continuous current time is short, so it is usually used for continuous current. During the power MOSFET is conducting, the diode will withstand high voltage, so the reverse voltage of selected diode should be bigger than the input voltage.

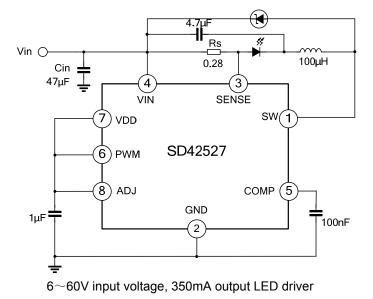
The average current through the diode is  $I_{D:}$ 

$$I_{D} = (1 - D)^* I_{LED}$$

 $I_{LED}$  is the current of LED.

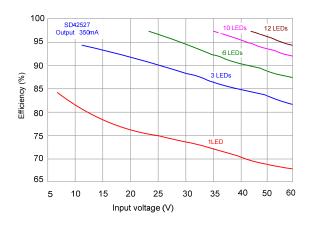
When the input voltage is high with a small duty,  $I_D$  increases, so the selected maximum continuous current diode should be bigger than the output current.

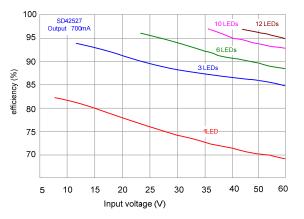
## **TYPICAL APPLICATION CIRCUIT**



**Note:** The circuit and parameters are reference only, please set the parameters of the real application circuit based on the real test.

# **ELECTRICAL CHARACTERISTICS CURVE**





efficiency versus input voltage (lout=700mA)

12 LEDs

2 LEDs

35 40

50

60

SD42527 Output 700mA

15

20 25

Input voltage (V)

2.0

1.5

1.0

0.5

-0.5

-1.0

-1.5

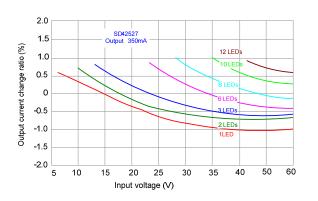
-2.0

5 10

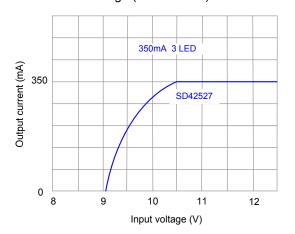
0

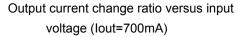
Output current change ratio (%)

#### efficiency versus input voltage (lout=350mA)

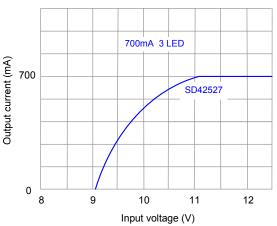


Output current change ratio versus input voltage (lout=350mA)

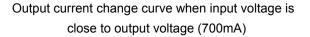




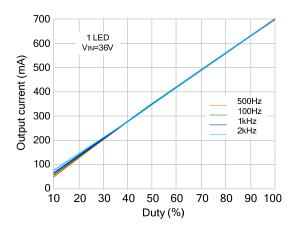
30



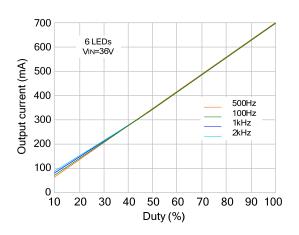
Output current change curve when input voltage is close to output voltage (350mA)



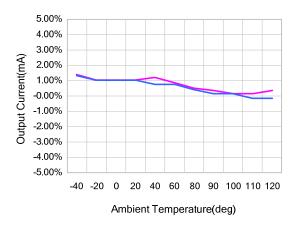
# **ELECTRICAL CHARACTERISTICS CURVE (continued)**

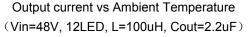


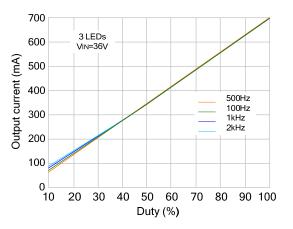
#### PWM dimming behavior of 1 LED(700mA)



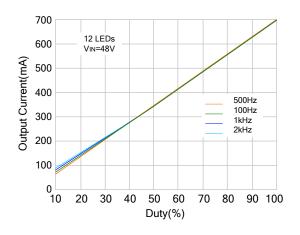




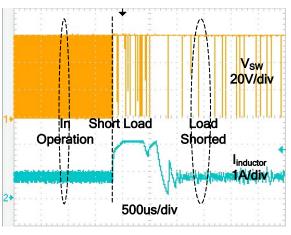


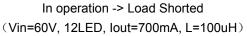


PWM dimming behavior of 3 LEDs(700mA)



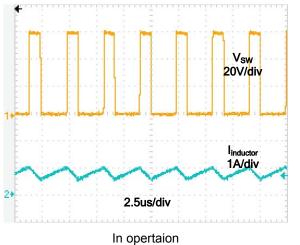
PWM dimming behavior of 8 LEDs(700mA)

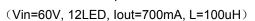


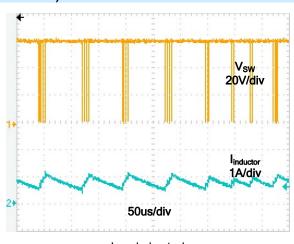




# ELECTRICAL CHARACTERISTICS CURVE (continued)



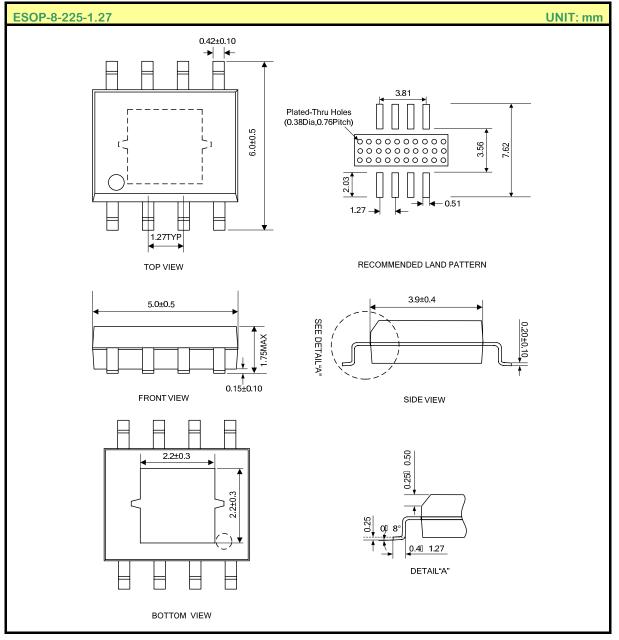




Load shorted (Vin=60V, 12LED, lout=700mA, L=100uH)



# PACKAGE OUTLINE

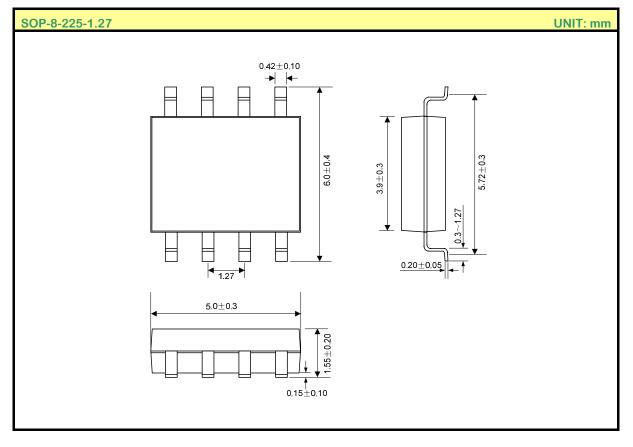


#### Note:

Please soldering together of PCB copper foil and pad which is at the bottom of ESOP package, and enlarge copper foil on PCB for heat dissipation. Better for more plated-thru holes on PCB attached to PAD.



## PACKAGE OUTLINE(continued)





#### HANDLING MOS DEVICES:

Electrostatic charges can exist in many things. All of our MOS devices are internally protected against electrostatic discharge but they can be damaged if the following precautions are not taken:

- Persons at a work bench should be earthed via a wrist strap.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed for dispatch in antistatic/conductive containers.

#### **Disclaimer:**

- Silan reserves the right to make changes to the information herein for the improvement of the design and performance without further notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
- All semiconductor products malfunction or fail with some probability under special conditions. When using Silan products in system design or complete machine manufacturing, it is the responsibility of the buyer to comply with the safety standards strictly and take essential measures to avoid situations in which a malfunction or failure of such Silan products could cause loss of body injury or damage to property.
- Silan will supply the best possible product for customers!



# ATTACHMENT

## **Revision History**

Date	REV	Description	Page
2011.06.15	1.0	Initial release	
2011.08.02	1.1	Modify "FEATURES"	