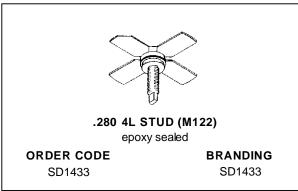


### **SD1433**

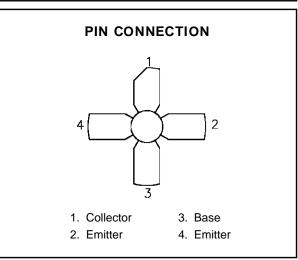
# RF & MICROWAVE TRANSISTORS UHF MOBILE APPLICATIONS

- 470 MHz
- 12.5 VOLTS
- CLASS C
- EFFICIENCY 60%
- COMMON EMITTER
- Pout = 10 W MIN. WITH 8.0 dB GAIN



#### **DESCRIPTION**

The SD1433 is a Class C epitaxial silicon NPN planar transistor designed for driver applications in the 450 - 512 MHz frequency range. This device uses an emitter ballasted geometry specifically designed for optimum stable power gain, maximum efficiency and infinite VSWR.



#### **ABSOLUTE MAXIMUM RATINGS** $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	16	V
V <sub>CES</sub>	Collector-Emitter Voltage	36	V
V <sub>EBO</sub>	Emitter-Base Voltage	4.0	V
lc	Device Current	2.5	А
P <sub>DISS</sub>	Power Dissipation	58	W
TJ	Junction Temperature	+200	°C
T <sub>STG</sub>	Storage Temperature	- 65 to +150	°C

#### THERMAL DATA

R <sub>TH(j-c)</sub> Junction-Case Thermal Resistance	3.0	°C/W
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November 1992 www.DataSheet4U.com

#### **ELECTRICAL SPECIFICATIONS** (Tcase = 25°C)

#### STATIC

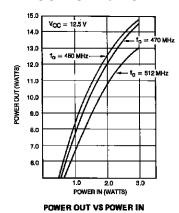
Symbol	Test Conditions	Value			Unit		
Symbol	rest conditions		Min.	Тур.	Max.		
BVces	I <sub>C</sub> = 25mA	$V_{BE} = 0V$		36	_	_	V
BVCEO	I <sub>C</sub> = 20mA	$I_B = 0mA$		16	_		V
BV <sub>EBO</sub>	I <sub>E</sub> = 10mA	$I_C = 0mA$		4.0	_	_	V
I <sub>CES</sub>	V <sub>CE</sub> = 10V	$I_E = 0mA$		_	_	3	mA
<sub>Ј.соп</sub> Сво	V <sub>CB</sub> = 15V	I <sub>E</sub> = 0mA		_	_	2	mA
hFE	V <sub>CE</sub> = 5V	Ic = 1A		10	_	_	_

DYNAMIC

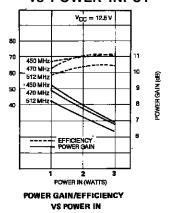
Symbol	Test Conditions		Value		Unit		
Symbol	rest conditions			Min.	Тур.	Max.	Unit
Роит	f = 470 MHz	$P_{IN} = 2.0 W$	$V_{CE} = 12.5 V$	10	_	_	W
Gp	f = 470 MHz	Pout = 10 W	VCE = 12.5 V	7	_	_	dB
C <sub>OB</sub>	f = 1 MHz	V <sub>CB</sub> = 12.5 V			19	_	pF

#### TYPICAL PERFORMANCE

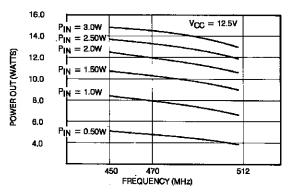
#### POWER OUTPUT vs POWER INPUT



### POWER GAIN & EFFICIENCY vs POWER INPUT

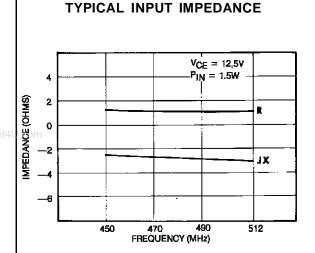


#### **POWER OUTPUT vs FREQUENCY**

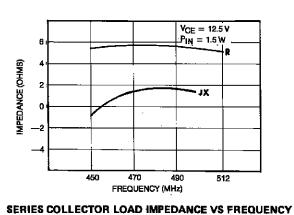


**POWER OUT V\$ FREQUENCY** 

#### **IMPEDANCE DATA**



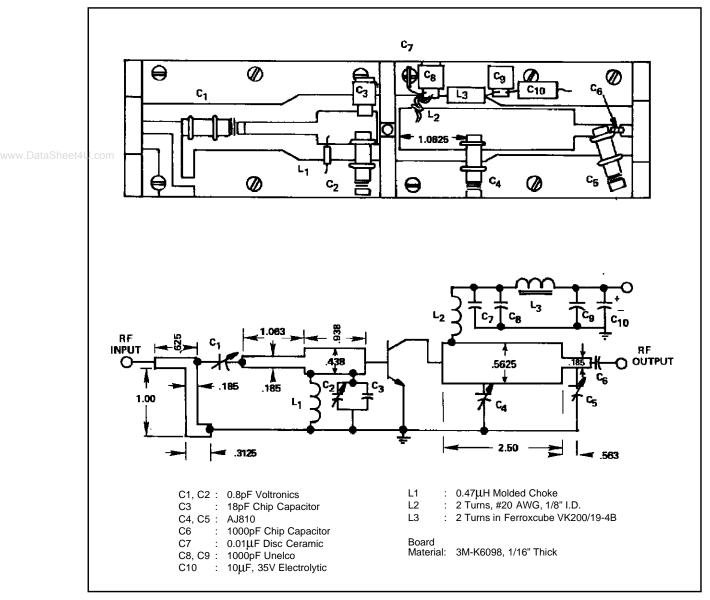
## TYPICAL COLLECTOR LOAD IMPEDANCE



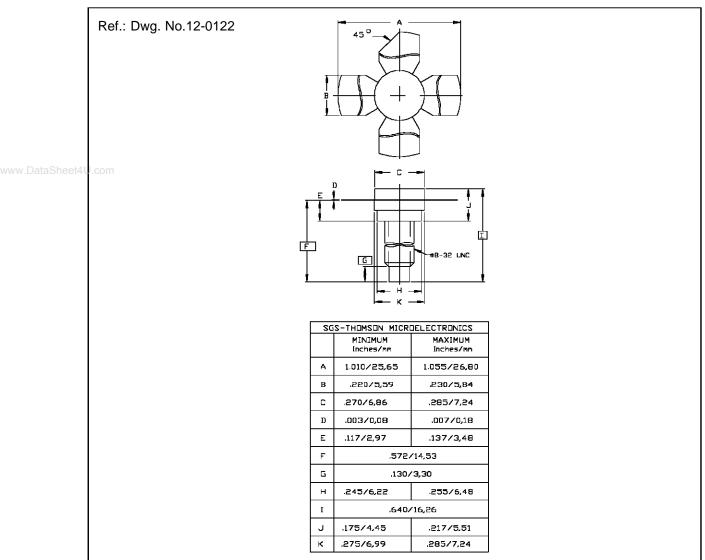
**SERIES SOURCE IMPEDANCE VS FREQUENCY** 

FREQ.	Z <sub>IN</sub> (Ω)	Z <sub>CL</sub> (Ω)
470 MHz	1.5 – j 2.7	5.7 + j 1.5

#### **TEST CIRCUIT**



#### PACKAGE MECHANICAL DATA



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