

SCQH040N120

1200V 40mΩ N-Channel SiC Power MOSFET

Features

- Typical on-Resistance: $R_{DS(on)}=40m\Omega$ (typ.)
- High Blocking Voltage
- 100% Avalanche Test
- Good Stability and Uniformity with High E_{AS}

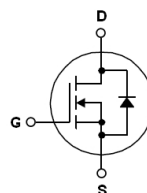
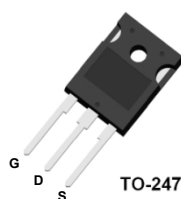
Description

The SCQH040N120 is a high blocking voltage N-Channel SiC power MOSFET. This device provide excellent performance for high voltage power supplies or pulse circuits.

Applications

- Solar Inverters
- High Voltage DC/DC Converters
- Motor Drivers
- Switch Mode Power Supplies

Package Type & Internal Circuit



Absolute Maximum Ratings @ $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter		Ratings	Unit
V _{DSS}	Drain to Source Voltage		1200	V
V _{GSS}	Gate to Source Voltage		-10/+25	V
V _{GSop}	Recommended operation Values of Gate –Source Voltage		-5/+20	V
I _D	Drain Current	T _C =25°C	61	A
		T _C =100°C	42	A
I _{DM}	Pulsed Drain Current (Note1)		120	A
P _D	Maximum Power Dissipation	T _C =25°C	250	W
	Derate above 25°C		1.67	W/°C
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		300	mJ
T _J	Operating Junction Temperature Range		-50~+175	°C
T _{STG}	Storage Temperature Range		-50~+175	°C

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
$R_{th(J-C)}$	Thermal Resistance, Junction to case	0.6	$^\circ\text{C/W}$
$R_{th(J-A)}$	Thermal Resistance, Junction to Ambient	40	$^\circ\text{C/W}$

Electrical Characteristics @T_C=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} =0V, I _D =2mA	1200	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =5mA	2.0	2.5	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} =20V, I _D =20A	-	40	65	mΩ
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =V _{DSS} , V _{GS} =0V	-	-	100	uA
I _{GSS}	Gate to Source Leakage Current	V _{GS} =V _{GSS} , V _{DS} =0V	-	-	±500	nA

D-S Diode Characteristics and Maximum Rating @T_C=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =0V, I _S =40A	-	3.9	-	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =40A, di/dt=-290A/us	-	30	-	ns
Q _{rr}	Reverse Recovery Charge		-	590	-	nC

Switching Characteristics @T_C=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
t _{d(on)}	Turn-on Delay Time	I _D =40A , V _{DD} =800V, R _G =2.5Ω V _{GS} = -5/20V, (Note 3)	-	12	-	ns
t _r	Turn-on Rise Time		-	12	-	ns
t _{d(off)}	Turn-off Delay Time		-	23	-	ns
t _f	Turn-off Fall Time		-	6.7	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =1000V, f=1.0MHz	-	2946	-	pF
C _{oss}	Output Capacitance		-	167	-	pF
C _{rss}	Reverse Transfer Capacitance		-	6.6	-	pF
Q _g	Total Gate Charge	I _D =40A, V _{DD} =800V V _{GS} =-5V/20V (Note 3)	-	142	-	nC
Q _{ge}	Gate to Source Charge		-	37	-	nC
Q _{gd}	Gate to Drain Charge		-	18	-	nC

Note:

1. Repetitive rating: pulse-width limited by maximum junction temperature
2. V_{DD}=100V, L=10mH, V_{clamp}=1600V, V_G=10V, I_D=19.0A
3. Essentially independent of operating temperature typical characteristics

Typical Performance Characteristics

Fig. 1. Typical on-Resistance Characteristics

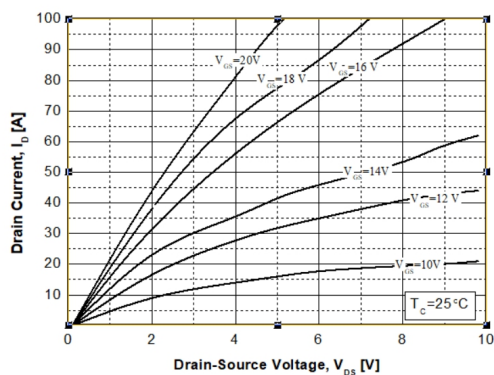


Fig. 3. Normalized On-Resistance vs. Junction Temperature

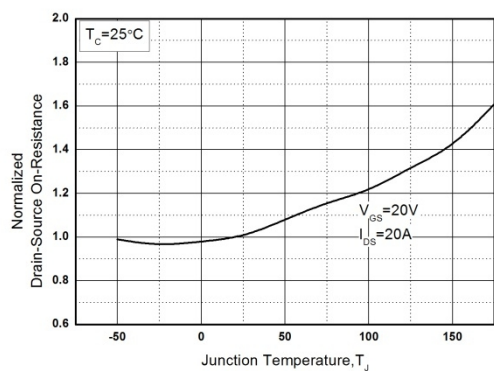


Fig. 5. Transfer Characteristics

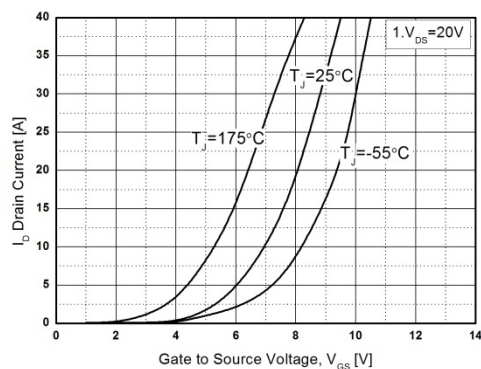


Fig. 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

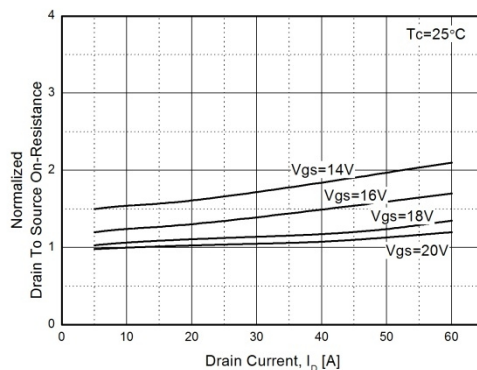


Fig. 4. On-Resistance vs. Gate-to-source Voltage

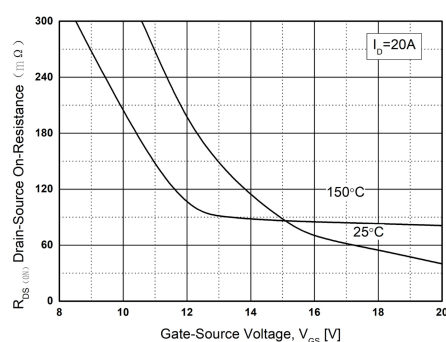
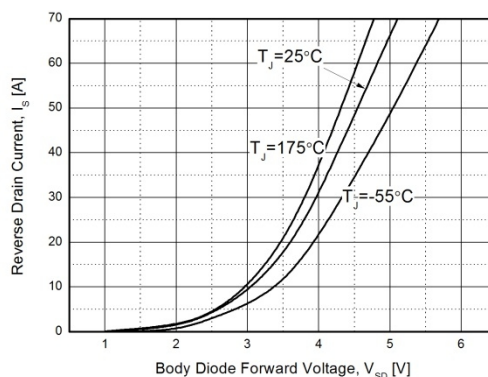


Fig. 6. Source-to-Drain Diode Forward Voltage vs. Source Current



Typical Performance Characteristics

Fig. 7. Gate Charge Characteristics

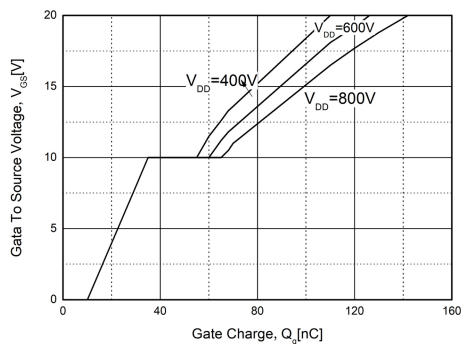


Fig. 8. Characteristics vs. Drain-to-Source Voltage

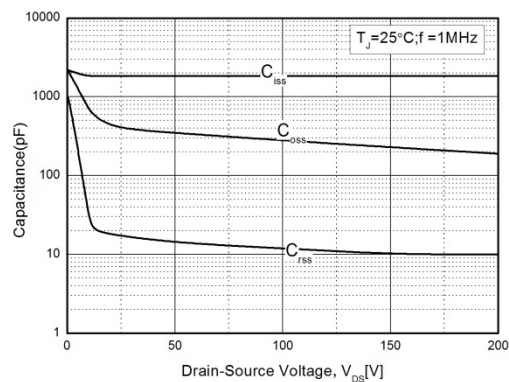
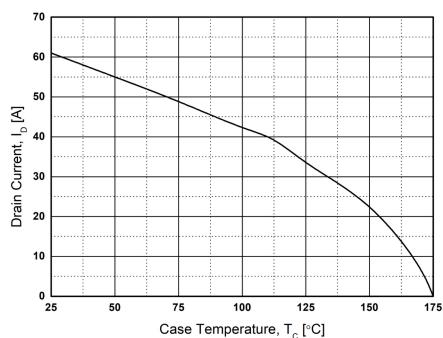


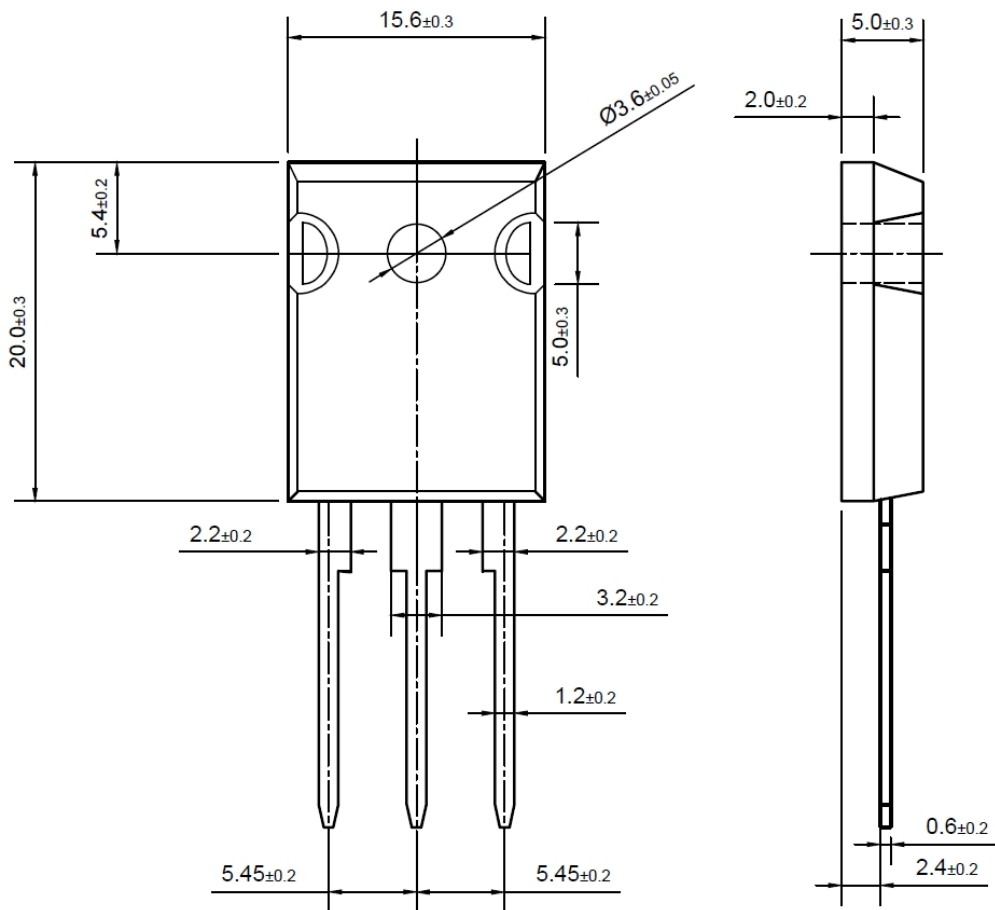
Fig. 9. Maximum Drain Current vs. Temperature



Package Dimensions

TO-247

(Dimensions in Millimeters)



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