

SCI7660C/M

DC-DC Converter



- 95% (Typical) Power Efficiency
- Voltage doubler
- Voltage Conversion(Positive↔Negative)

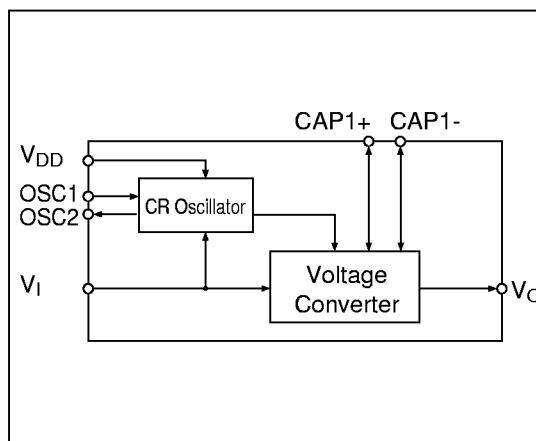
DESCRIPTION

The SCI7660C/M CMOS DC-DC Converter features high operational performance with low power dissipation. The booster generates a doubled output voltage from the input. It is possible to drive the LSI that need another power supply than main power supply.(LCD drivers•Analog LSI etc.) Its very low power requirement makes it ideal to supply handy equipments with power.

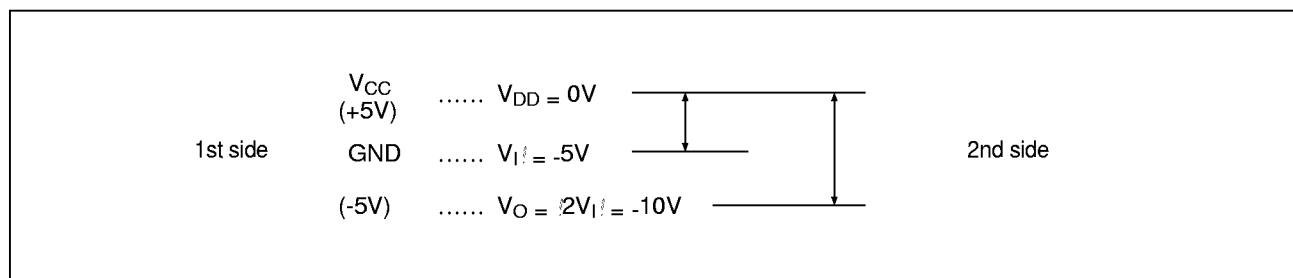
FEATURES

- High performance with low power dissipation
- Simple conversion of V_{DD} (-5V) to + V (+5V) and $-2V_I$ (-10V)
- Output current 30mA Max.
($V_{DD}=-5V$)
- Power conversion efficiency 95% Typ.
- Cascade connection
(two device connected $V_{DD}=-5V$, $V_O=-15V$)
- Low power Ideal for dry cell battery
- On-chip CR oscillator
- Package DIP-8pin (plastic)
SOP4-8pin (plastic)

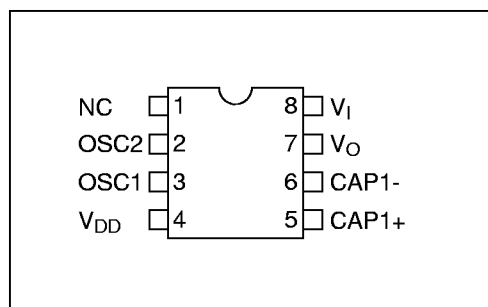
BLOCK DIAGRAM



VOLTAGE RELATIONS



PIN CONFIGURATION



PIN DESCRIPTION

Pin Name	Pin No.	Function
OSC1	3	Oscillation resistor connection terminal
OSC2	2	
V_{DD}	4	Power supply terminal(positive, V_{CC} as system)
CAP1+	5	Terminal for connection of capacitor for booster(positive)
CAP1-	6	Terminal for connection of capacitor for booster(negative)
V_O	7	Output terminal at doubling
V_I	8	Power supply terminal(negative, GND as system)

■ ABSOLUTE MAXIMUM RATINGS

(V_{DD} = 0V, Ta = 25°C)

Rating	Symbol	Value	Unit
Input voltage	V _I	-10.0 to 0.3	V
Output voltage	V _O	-20.0 to 0.3	V
Power dissipation	P _D	300	mW
Operating temperature	T _{opr}	-30 to 85	°C
Storage temperature	T _{stg}	-65 to 150	°C
Soldering temperature and time	T _{sol}	260°C, 10s(at lead)	-

Note: When this IC is soldered in the solder-reflow process, be sure to maintain the reflow furnace at the curve shown in "Fig. 1-5 Reflow Furnace Temperature Curve" of this DATA BOOK. And this IC can not be exposed to high temperature of the solder dipping.

■ ELECTRICAL CHARACTERISTICS

(V_{DD}=0V, Ta=-30 to 85°C)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Input voltage	V _I		-8.0	-	-1.2	V
Output voltage	V _O		-16.0	-	-	V
Booster current consumption	I _{opr}	R _L = , R _{OSC} =1M Ω , V _I =-5V	-	40	70	μ A
Stationary current	I _Q	R _L = , V _I =-8V	-	-	2.0	μ A
Output impedance	R _O	I _O =10mA, V _I =-5V	-	80	120	Ω
Booster power conversion efficiency	P _{eff}	I _O =5mA, V _I =-5V	90	95	-	%
Input leakage current	I _{LI}	OSC1 terminal, V _I =-8V	-	-	2.0	μ A
Oscillation frequency	f _{OSC}	R _{OSC} =1M Ω , V _I =-5V	16	20	24	kHz

■ RECOMMENDED OPERATING CONDITIONS

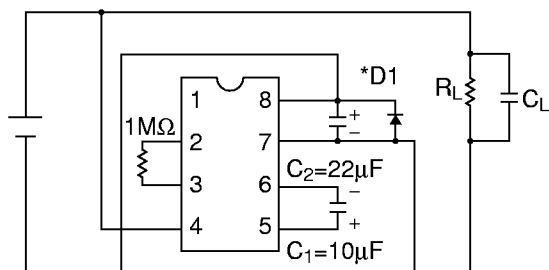
(V_{DD}=0V, Ta=-30 to 85)

Condition	Symbol	Remark	Min.	Typ.	Max.	Unit
Booster start voltage	V _{STA1}	R _{OSC} =1M Ω , C _L C ₂ ≤1/20*1 C ₂ ≥10 μ F Ta= /20 to 85	-	-	-1.2	V
	V _{STA2}	R _{OSC} =1M Ω	-	-	-2.2	V
Booster stop voltage	V _{STP}	R _{OSC} =1M Ω	-1.2	-	-	V
Output load resistance	R _L		R _L Min.*2	-	-	Ω
Output current	I _O		-	-	30	mA
Oscillation frequency	f _{OSC}		10	-	30	kHz
External resistance for oscillation	R _{OSC}		680	-	2000	k Ω
Capacitor for booster	C ₁ , C ₂ , C ₃		3.3	-	-	μ F

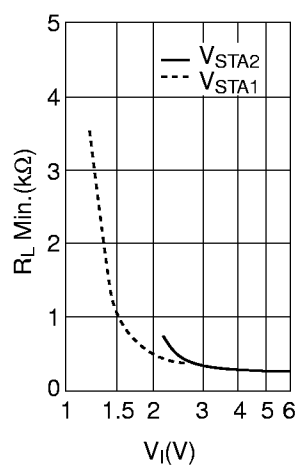
*1 Recommended circuitry in low voltage operation is shown below.

*2 R_L Min. depends on input voltage as shown below.

Recommended circuit in low voltage operation



*D1 ($V_F(I_F=1\text{mA}) \leq 0.6\text{V}$)



■ PERFORMANCE CURVES

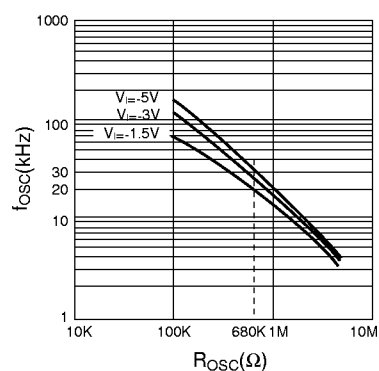


Fig. 1 Oscillation Frequency(f_{OSC})vs. External-Resistance(R_{OSC})

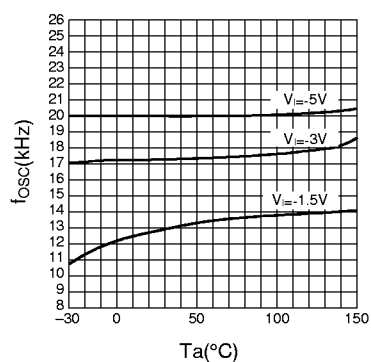


Fig. 2 Oscillation Frequency (f_{OSC})vs. Temperature (T_a)

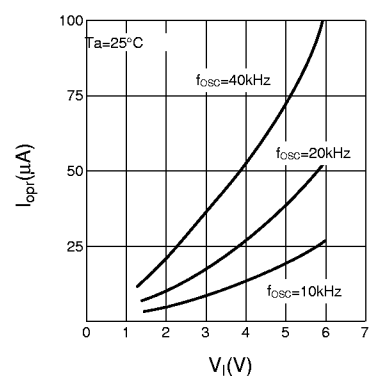


Fig. 3 Booster Current Consumption (I_{opr})vs. Input Voltage(V_I)

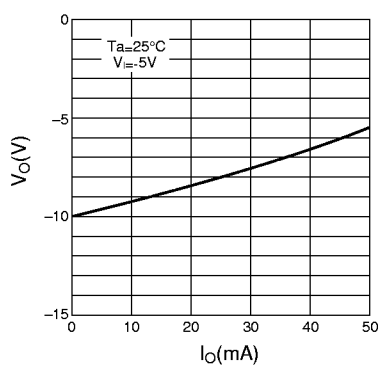


Fig. 4 Output Voltage(V_O)vs. Output Current(I_O)

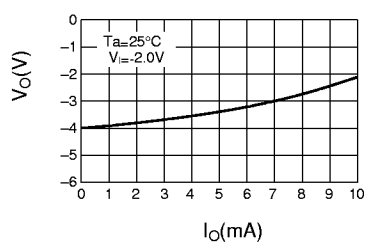


Fig. 5 Output Voltage(V_O)vs. Output Current(I_O)

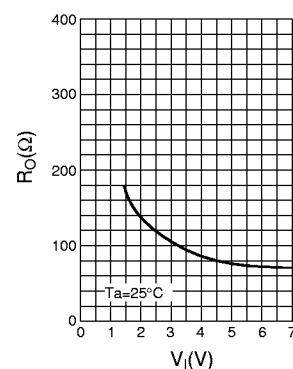


Fig. 6 Output Impedance(R_O)vs. Input Voltage(V_I)

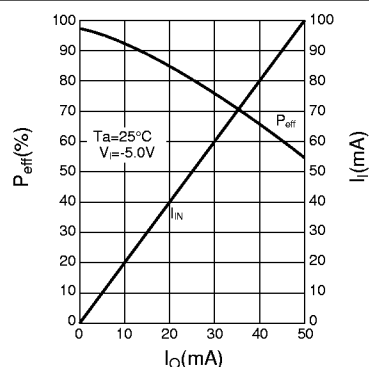


Fig. 7 Power Conversion Efficiency (P_{eff})/Input Current (I_i) vs. Output Current (I_o)

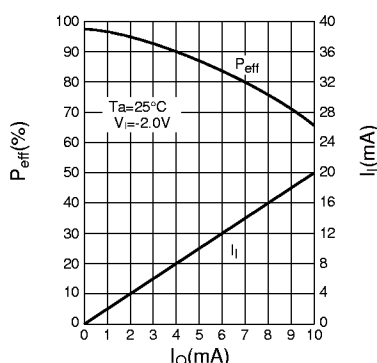


Fig. 8 Power Conversion Efficiency (P_{eff})/Input Current (I_i) vs. Output Current (I_o)

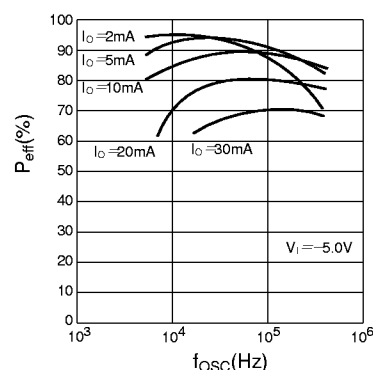


Fig. 9 Power Conversion Efficiency (P_{eff}) vs. Oscillation Frequency (f_{osc})

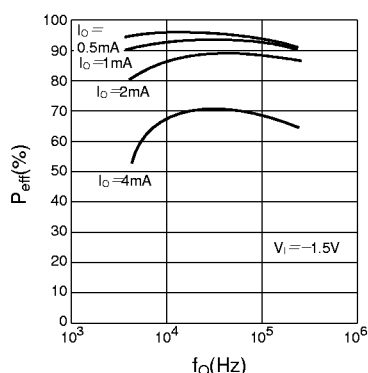


Fig. 10 Power Conversion Efficiency (P_{eff}) vs. Oscillation Frequency (f_{osc})

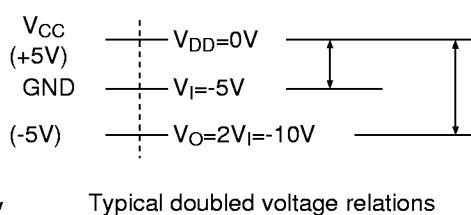
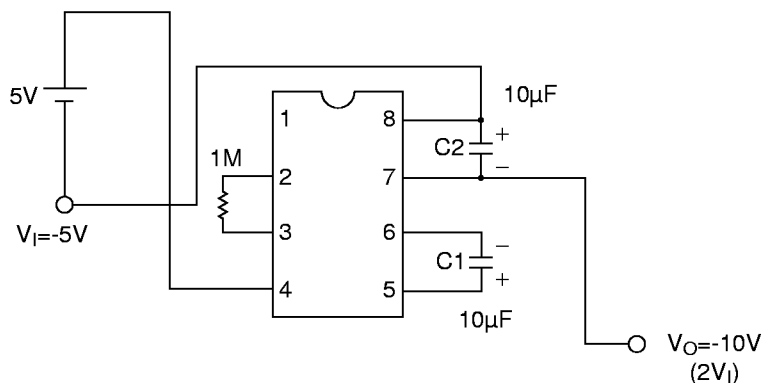
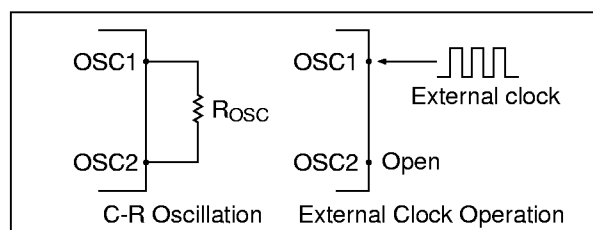
CIRCUIT DESCRIPTION

C-R Oscillator

The SCI7660C/M contains a C-R oscillator for internal oscillation. It consists of an external resistor R_{OSC} connected between the OSC1 pin and OSC2 pin.

Voltage Converters

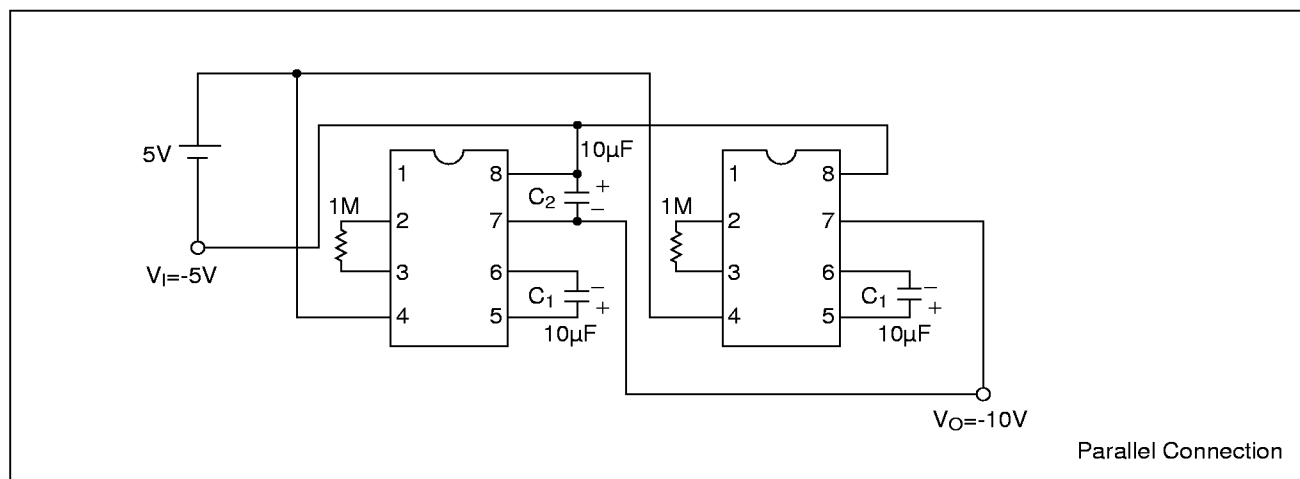
The voltage converters double the input supply voltage (V_i) using clocks generated by the C-R oscillator. A doubled voltage can be obtained with a booster capacitor between CAP + and CAP-, and with an external smoothing capacitor between V_i and V_o .



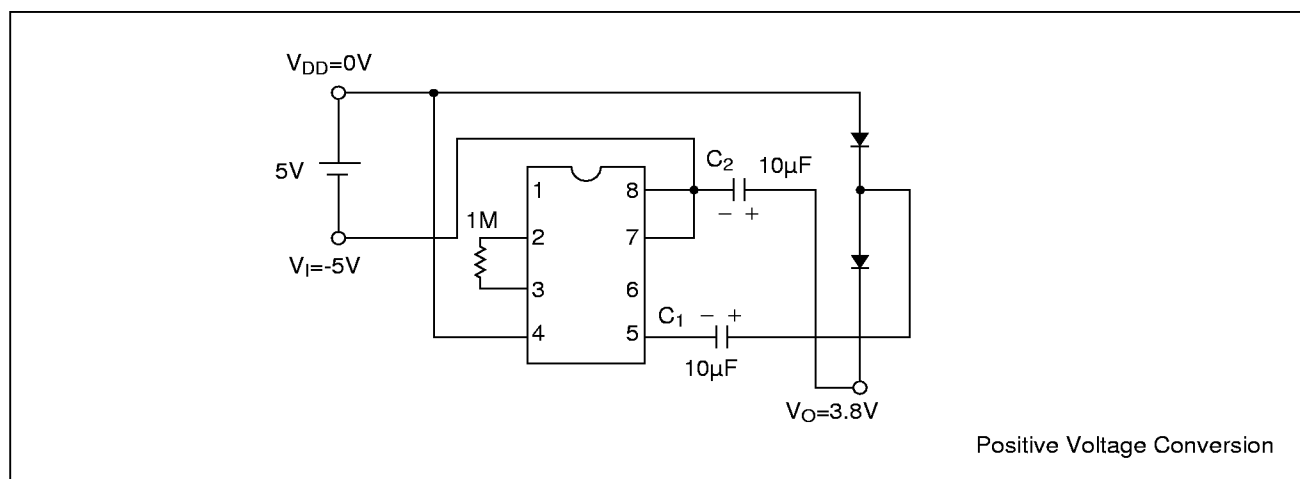
Typical doubled voltage relations

■ BASIC EXTERNAL CONNECTION

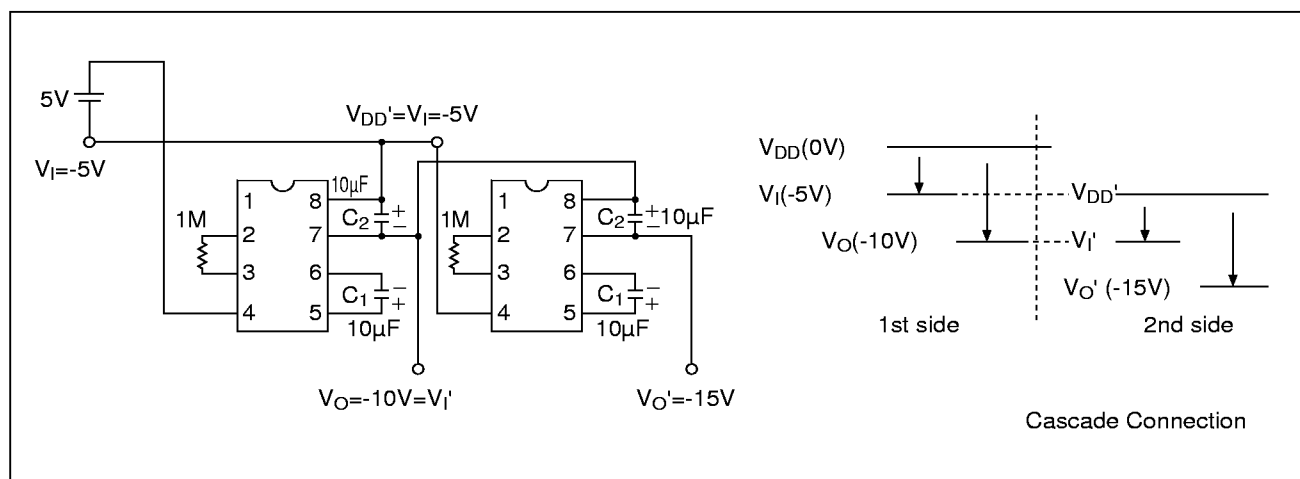
- Parallel Connection (Output impedance can be reduced by parallel connections.)



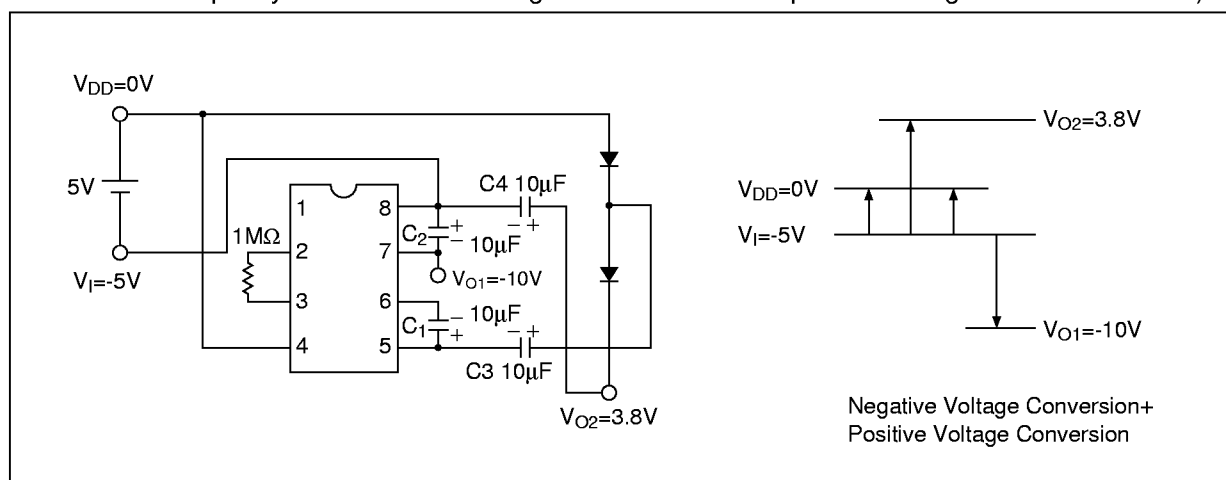
- Positive Voltage Conversion (Input voltage can be doubled toward the positive side with diode.)



- Cascade Connection (Cascade connection of SCI7660C/M further increase the output voltage. Note, however, that the cascade connection increase the output impedance.)



- Negative Voltage Conversion + Positive Voltage Conversion (This circuit produces outputs of -10V and $+3.8\text{V}$ from the -5V input by combination of voltage doubler circuit and positive voltage conversion circuit.)



PACKAGE DIMENSIONS

