



SUPER BARRIER RECTIFIER

**40A SBR** 

#### Product Summary (Per Leg)

V <sub>RRM</sub> (V)	I <sub>0</sub> (A)	V <sub>F(MAX)</sub> (V) @ +25°C	I <sub>R(MAX)</sub> (mA) @ +25°С
200	20	0.93	0.2

### **Description and Applications**

Packaged in the robust industry-standard TO263AB (D2PAK) package, the SBR40U200CTBQ provides very low V<sub>F</sub> and excellent reverse leakage stability at high temperatures. It is ideal for use as a rectifier, freewheel diode or blocking diode in:

- SMPS
- DC-DC converters
- AC-DC adaptors

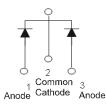
### **Features and Benefits**

- Ultra Low Forward-Voltage Drop
- Low-Leakage Current
- Excellent High-Temperature Stability
- Patented Super Barrier Rectifier SBR<sup>®</sup> Technology
- Soft, Fast Switching Capability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The SBR40U200CTBQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: TO263AB
- Package Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish. Solderable per MIL-STD-202, Method 208 @3
- Polarity: See Below
- Weight: 1.6 grams (Approximate)



Package Pin Out Configuration

### Ordering Information (Note 4)

Part Number	Backaga	Packing	
Fait Nulliber	Package	Qty.	Carrier
SBR40U200CTBQ-13	TO263AB (D2PAK)	800	Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



)'' = Manufacturers' Marking
SBR40U200CTB = Product Type Marking Code
AB = Foundry and Assembly Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 24 = 2024)
WW = Week (01 to 53)



Top View



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		Vrrm Vrwm Vrm	200	V
Average Rectified Output Current	(Per Leg) (Total)	lo	20 40	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Loa		IFSM	280	A

# Thermal Characteristics (Per Leg) (Note 8)

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Case (Note 5)	R <sub>0JC</sub>	14	°C/W
Typical Thermal Resistance, Junction to Case (Note 6)	Rejc	3	°C/W
Typical Thermal Resistance, Junction to Ambient (Note 5)	Reja	60	°C/W
Typical Thermal Resistance, Junction to Ambient (Note 6)	Reja	15	°C/W
Typical Thermal Resistance, Junction to Lead (Cathode Tab)	Rejl	3	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +175	°C

### Electrical Characteristics (Per Leg) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop (Note 7)	VF	_	0.85	0.93	V	I <sub>F</sub> = 20A, T <sub>J</sub> = +25°C
	٧F	_	0.70	0.75	v	IF = 20A, TJ = +125°C
Leakage Current (Note 7)		_	_	0.2	mA	V <sub>R</sub> = 200V, T <sub>J</sub> = +25°C
Leakage Current (Note 7)	IR	—	—	40		V <sub>R</sub> = 200V, T <sub>J</sub> = +125°C
Junction Capacitance	CJ	_	500	—	pF	V <sub>R</sub> = 4V, T <sub>J</sub> = +25°C
Switching Speed	t <sub>RR</sub>	_	26	—	ns	IF = 0.5A, IR = 1A, IRR = 0.25A (RG1)

Notes: 5. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

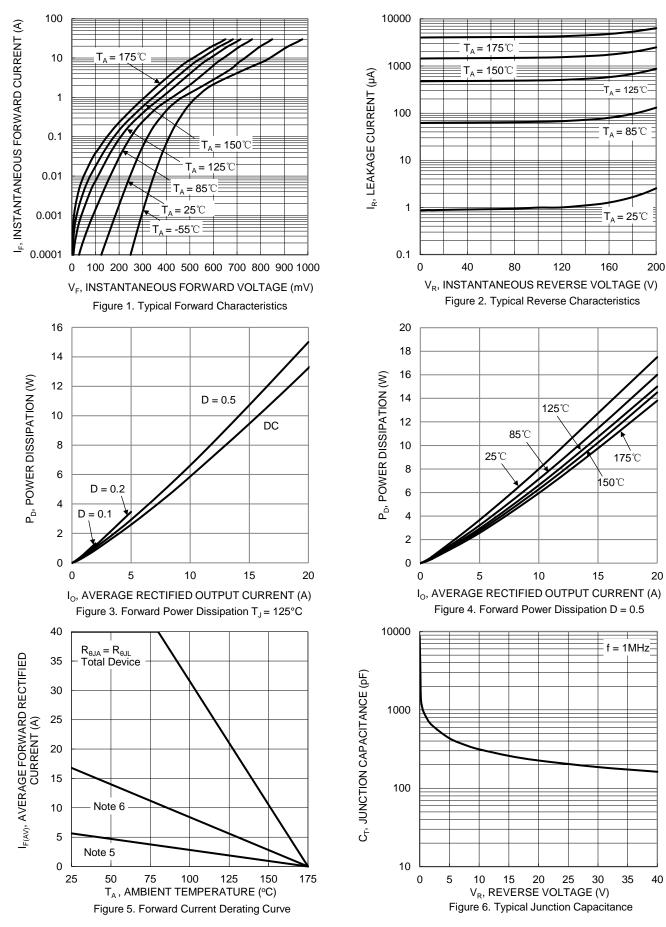
6. 2inch \* 2inch Al board.

7. Short duration pulse test used to minimize self-heating effect.

8. The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R\theta JA$ .



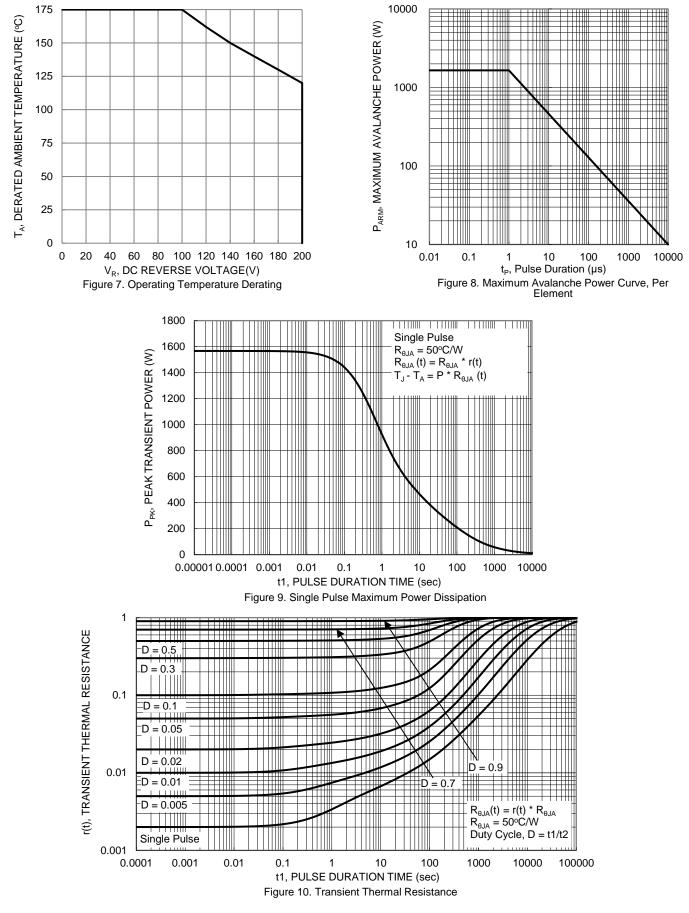
# SBR40U200CTBQ



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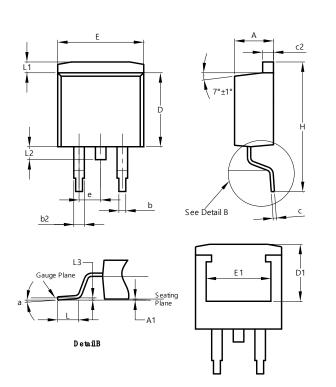
### SBR40U200CTBQ





### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



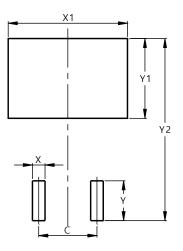
TO263AB (D2PAK)				
Dim	Min	Max	Тур	
Α	4.07	4.82	-	
A1	0.00	0.25	-	
b	0.51	0.99	-	
b2	1.15	1.77	-	
С	0.356	0.73	-	
c2	1.143	1.65	-	
D	8.39	9.65	-	
D1	6.55	6.95	-	
е	1	2.54 TYP		
E	9.66	10.66	-	
E1	6.23	8.23	-	
Н	14.61	15.87	-	
L	1.78	2.79	-	
L1	-	1.67	-	
L2	-	1.77	-	
L3	-	-	0.254	
а	0°	8°	-	
All D	All Dimensions in mm			

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TO263AB (D2PAK)

TO263AB (D2PAK)



Dimensions	Value (in mm)
С	5.08
Х	1.10
X1	10.41
Y	3.50
Y1	7.01
Y2	15.99



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