



# S8MNC4P2



## 30V N-Channel MOSFETs

### General Description

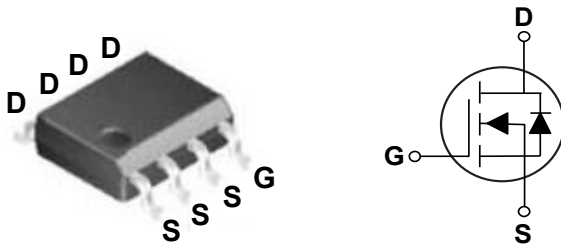
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
30 V	4.2 m $\Omega$	30 A

### Features

- $R_{DS(ON)} \leq 4.2m\Omega @ V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

SOP-8 Pin Configuration



### Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

### Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current - Continuous ( $T_C=25^\circ\text{C}$ )	30	A
	Drain Current - Continuous ( $T_C=100^\circ\text{C}$ )	19	A
$I_{DM}$	Drain Current - Pulsed (NOTE 1)	120	A
EAS	Single Pulse Avalanche Energy (NOTE 2)	125	mJ
IAS	Single Pulse Avalanche Current (NOTE 2)	50	A
$P_D$	Power Dissipation ( $T_C=25^\circ\text{C}$ )	7	W
	Power Dissipation - Derate above $25^\circ\text{C}$	0.056	W/ $^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 175	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 175	$^\circ\text{C}$
Marking Code		NC4P2 , DS3904	

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	---	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	18	$^\circ\text{C/W}$

**S8MNC4P2****30V N-Channel MOSFETs****Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V$ , $I_D=250\mu A$	30	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=30V$ , $V_{GS}=0V$ , $T_J=25^{\circ}\text{C}$	---	---	1	$\mu A$
		$V_{DS}=24V$ , $V_{GS}=0V$ , $T_J=125^{\circ}\text{C}$	---	---	10	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA

**On Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance (NOTE 3)	$V_{GS}=10V$ , $I_D=12A$	---	3.8	4.2	m $\Omega$
		$V_{GS}=4.5V$ , $I_D=6A$	---	5.2	6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu A$	1.2	1.6	2.5	V
gfs	Forward Transconductance	$V_{DS}=10V$ , $I_D=6A$	---	12	---	S

**Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$Q_g$	Total Gate Charge	$V_{DS}=15V$ , $V_{GS}=4.5V$ , $I_D=12A$ (NOTE 3 & 4)	---	24	34	nC
$Q_{gs}$	Gate-Source Charge		---	4.2	6	
$Q_{gd}$	Gate-Drain Charge		---	13	18	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=15V$ , $V_{GS}=10V$ , $R_G=3.3\Omega$ , $I_D=15A$ (NOTE 3 & 4)	---	12.6	24	nS
$T_r$	Rise Time		---	19.5	37	
$T_{d(off)}$	Turn-Off Delay Time		---	42.8	81	
$T_f$	Fall Time	$V_{DS}=25V$ , $V_{GS}=0V$ , $F=1\text{MHz}$	---	13.2	25	pF
$C_{iss}$	Input Capacitance		---	2200	3190	
$C_{oss}$	Output Capacitance		---	280	405	
$C_{rss}$	Reverse Transfer Capacitance	$V_{GS}=0V$ , $V_{DS}=0V$ , $F=1\text{MHz}$	---	177	255	$\Omega$
$R_g$	Gate resistance		---	2	4	

**Guaranteed Avalanche Energy**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	$V_{DD}=25V$ , $L=0.1\text{mH}$ , $I_{AS}=10A$	31	---	---	mJ

**Drain-Source Diode Characteristics and Ratings**

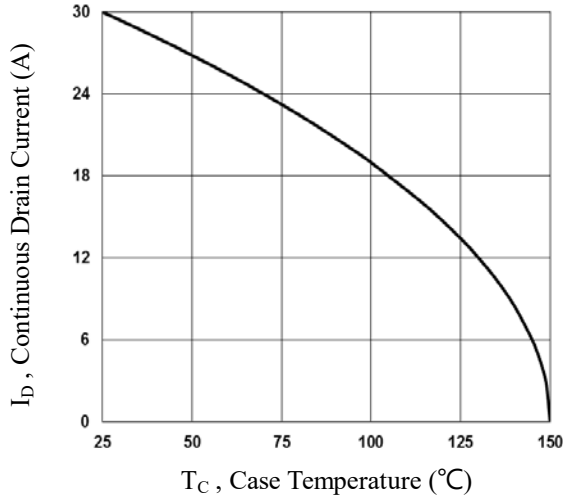
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	30	A
$I_{SM}$	Pulsed Source Current (NOTE 3)		---	---	120	A
$V_{SD}$	Diode Forward Voltage (NOTE 3)	$V_{GS}=0V$ , $I_S=1A$ , $T_J=25^{\circ}\text{C}$	---	---	1	V

**NOTES :**

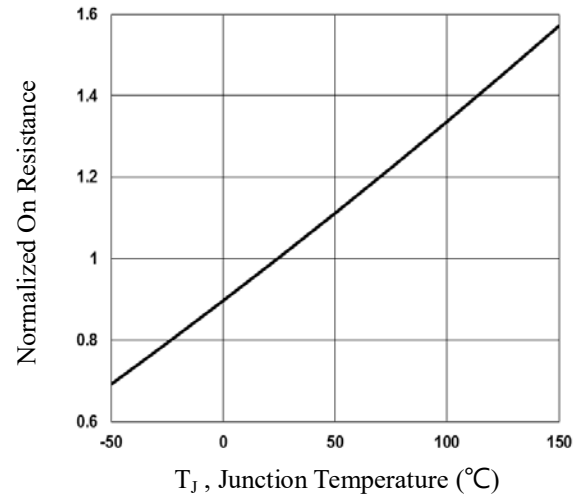
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25V$ ,  $V_{GS}=10V$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=50A$ ,  $R_G=25\Omega$ , Starting  $T_J=25^{\circ}\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.



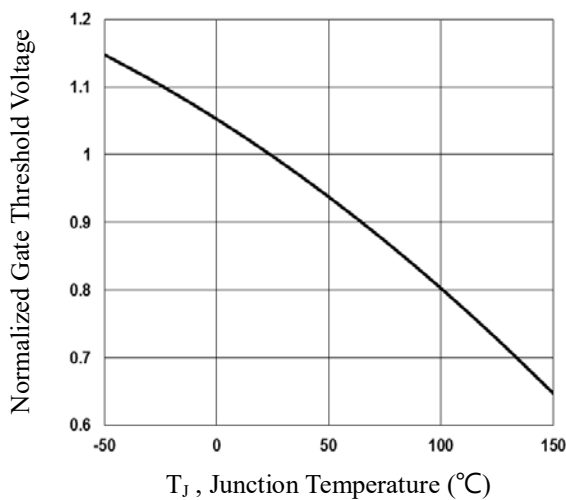
### Characteristics Curves



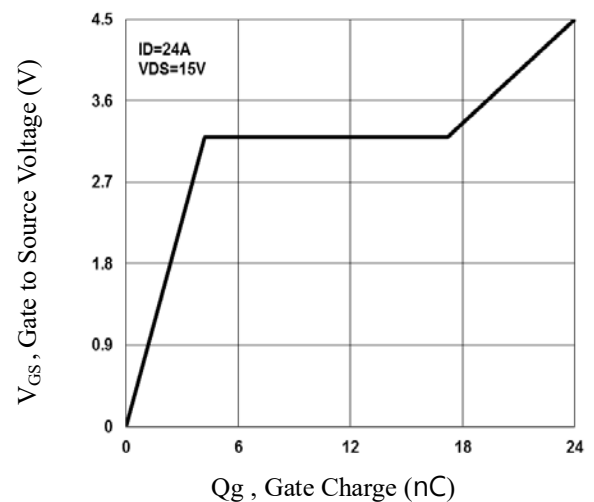
**Fig.1 Continuous Drain Current vs.  $T_C$**



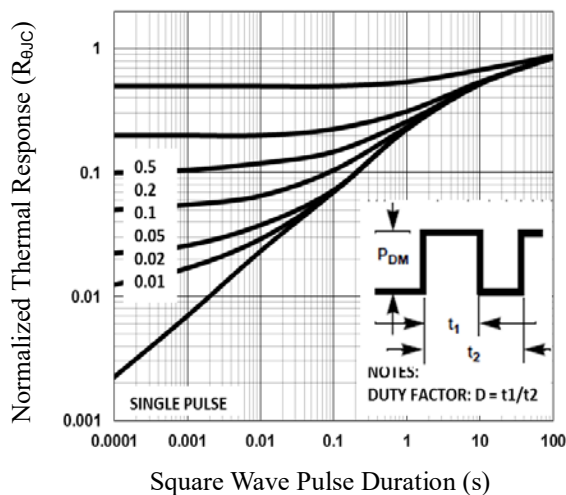
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$**



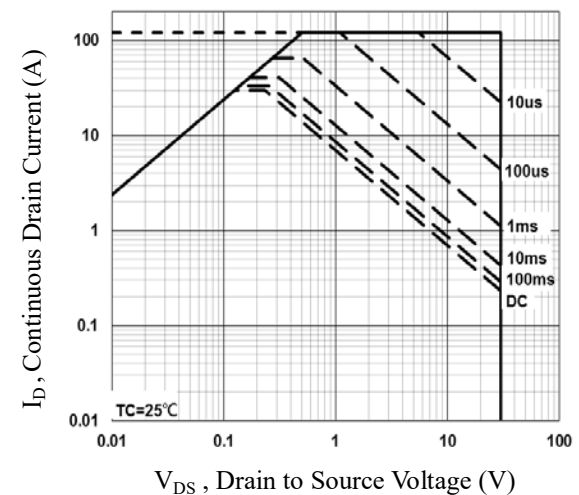
**Fig.3 Normalized  $V_{th}$  vs.  $T_J$**



**Fig.4 Gate Charge Waveform**



**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**

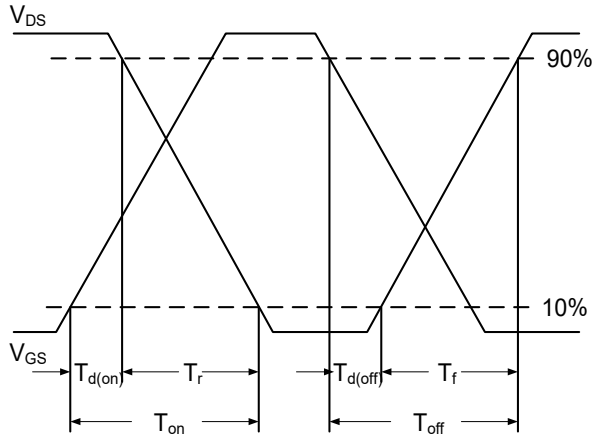


**S8MNC4P2**



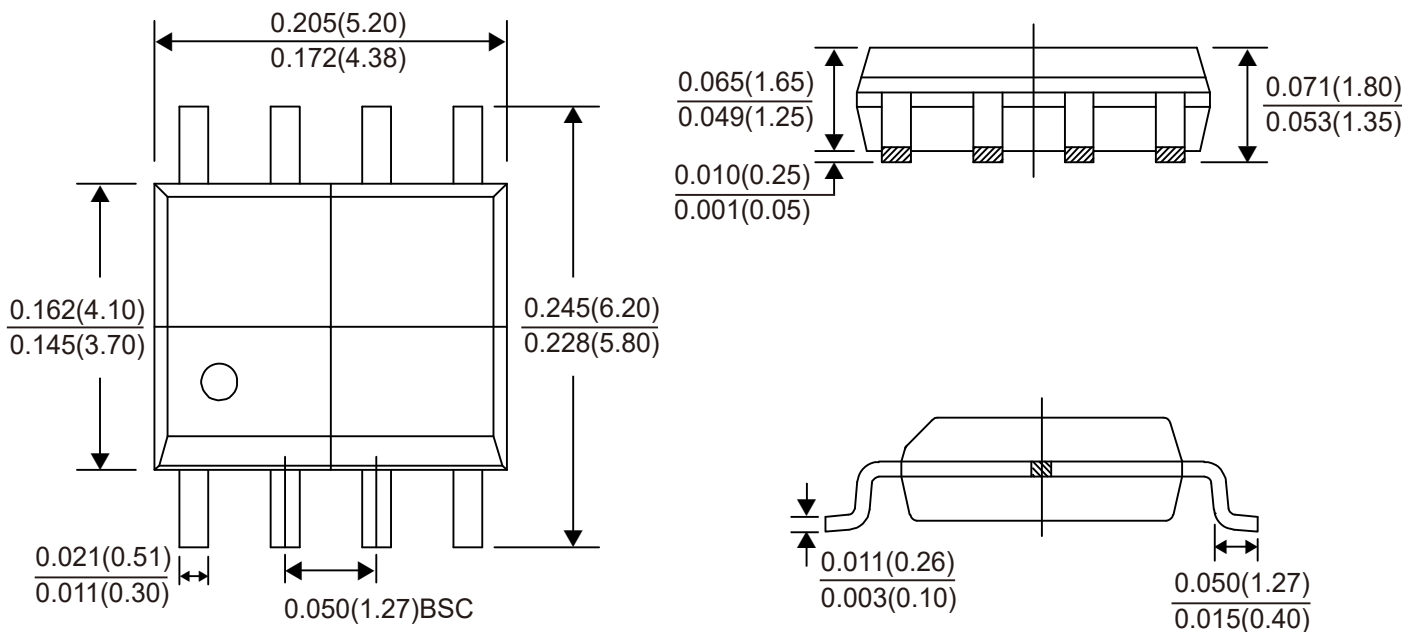
**30V N-Channel MOSFETs**

### Characteristics Curves



**Fig.7 Switching Time Waveform**

### Package Outline Dimensions



**SOP-8**

Dimensions in inches and (millimeters)



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